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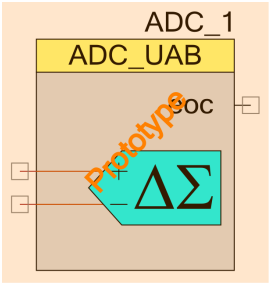
Incremental ADC (ADC_UAB) 1.10

Features



- Single sample incremental ADC
- Rail to rail input voltage capability
- Differential or single-ended input
- Full scale ranges from ±Vdda down to ±150 mV
- Data scaling, saturation, and calibration managed by API functions

General Description

The Incremental ADC (ADC_UAB) Component is a general purpose retriggerable ADC that can provide results with a resolution of 14 bits and a conversion time of 1 ms.

Results are returned through API function calls that perform scaling, saturation, and calibration tasks.

When to Use an Incremental ADC

The ADC_UAB is useful for making slow, irregular, or asynchronous measurements of system voltages.

Input/Output Connections

This section describes the various input and output connections for the ADC_UAB Component. An asterisk (∗) near the terminal name indicates that the terminal may not be shown on the Component symbol for the conditions listed in the description of that I/O terminal.

Terminal Name	I/O Type	Description
eoc	Digital Output	This output can be used to trigger the transfer of data once the conversion is complete. This terminal is always present.
+Input	Analog Input	This input (not marked; it is always the upper terminal of the differential input pair on the symbol) is the 'positive' (also called non-inverting) analog signal input to the ADC.
-Input ∗	Analog Input	This input (not marked; it is always the lower terminal of the differential input pair on the symbol) is the 'negative' (also called inverting) analog signal input to the ADC. When the ADC_UAB is in single-ended mode, the inverting input of the ADC is connected internally to the reference.

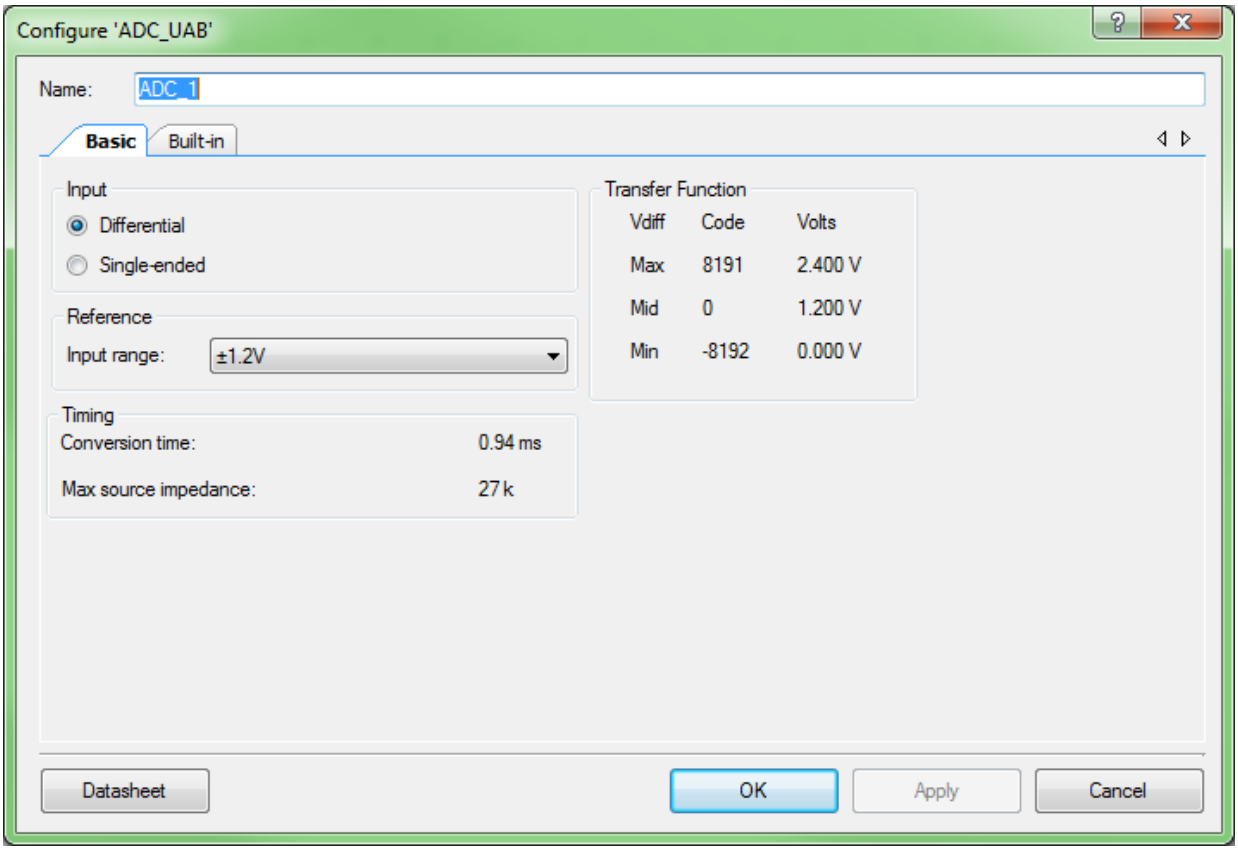
Component Parameters

This section covers the various parameters that can be altered or inspected through the setup customizer of the Component, grouped within a series of tabs. To explore this, drag a Incremental ADC onto your design and double-click it to open the Configure dialog.

For any selectable parameter, the option shown here in **bold** is the default.

Drag an Incremental ADC Component onto your design and double-click it to open the parameter list.

Basic Tab



Input

The input mode can be chosen as single-ended or differential with this pair of buttons. In single-ended mode, the voltage at the input terminal is converted with respect to its reference voltage. The returned code is zero when the single-ended input is equal to the reference voltage, negative when below the reference voltage and positive when above it.

- **Differential**
- Single-ended

Input range

The selection available in the pull-down is dependent on whether differential or single-ended input mode has been selected.

Some input ranges are expressed as absolute voltages, and are referenced to the internal bandgap voltage of 1.2 V. Others are dependent on the actual value of the Vdda supply voltage (not the numeric value entered in the Design Wide Resources (DWR)), through the use of a Vdda/2 reference voltage. In single-ended operation, the Vneg terminal of the ADC is always connected to its reference voltage, of either 1.2 V

or Vdda/2.

Note Range selections that don't cover the complete code range will be marked below with *italics*.

Differential mode

- ±2.4 V
- **±1.2 V**
- ±0.6 V
- ±0.3 V
- ±0.15 V
- ±Vdda
- ±Vdda/2
- ±Vdda/4
- ±Vdda/8
- ±Vdda/16

Single-ended mode

- *-0.15 V to 3.6 V*
- **0 V to 2.4 V**
- 0.6 V to 1.8 V
- 0.9 V to 1.5 V
- 1.05 V to 1.35 V
- *-0.15 V to Vdda*
- 0 V to Vdda
- 0.25 Vdda to 0.75 Vdda
- 0.375 Vdda to 0.625 Vdda
- 0.4375 Vdda to 0.5625 Vdda

In differential operation, full code range can be obtained if the voltage difference between the inputs meets the range criterion and both input voltages are between -0.15 V and Vdda.

Conversion time (display only)

This field displays the total conversion time in milliseconds. This is the time from calling the StarConvert() function to returning a calibrated value from the GetResult16() function in the ISR.

Maximum source impedance (display only)

This field displays the maximum resistive source impedance to ensure specified accuracy.

Transfer Function (display only)

This section displays the converter transfer functions relating output data to input voltage (for data returned by the API function call, not the raw data).

Application Programming Interface

By default, PSoC Creator assigns the instance name **ADC_1** to the first instance of the ADC_UAB in a given design. You can rename it to any unique value that follows the syntactic rules for identifiers. The instance name becomes the prefix of every global function name, variable, and constant symbol. For readability, the instance name used in the following section is **ADC**.

This Component's API contains the following sections:

- Initialization
- General
- Interrupt Service Routine
- Power
- Global Variables
- Global Constants

- Code Examples and Applications
- API Memory Usage

Initialization

Functions that are used for starting and stopping the Component.

Functions

- void ADC_Start (void)
- uint32 ADC_StartConvert (void)
- void ADC_Stop (void)
- void ADC_Init (void)
- void ADC_Enable (void)

Function Documentation

void ADC_Start (void)

Performs all required initialization for this component, and enables the power. The power is set to the appropriate level based on the clock frequency. Does not start a conversion.

uint32 ADC_StartConvert (void)

Begins a conversion.

Return values

uint32	The function returns cystatus of its operation. ■ CYRET_STARTED - The ADC is already converting. No new conversion started. ■ CYRET_SUCCESS - A new conversion has started.

void ADC_Stop (void)

Disables underlying hardware. Terminates conversion if there is a conversion in progress.

void ADC_Init (void)

Configures hardware and data structures according to component parameters. Does not power hardware. Called from ADC_Start(). ADC_Start() is the preferred method for initializing the component. The ISR is set to the ADC_ConversionInt() here. During calibration, the ISR is temporarily set to the ADC_CalibrateInt() and then restored to ADC_ConversionInt().

void ADC_Enable (void)

Enables underlying hardware. Does not start a conversion. Called from ADC_Start(). ADC_Start() is the preferred method for initializing the component.

General

General APIs that are used for run-time configuration of the Component during active power mode.

These include reading from registers, and writing to registers.

Functions

- uint32 ADC_IsEndConversion (ADC_eoc_enum retMode)
- uint32 ADC_Calibrate (ADC_calibrate_mode_enum calibrateMode)
- int16 ADC_RawToResult16 (int32 raw)
- int16 ADC_CountsTo_mVolts (int32 counts)
- int32 ADC_CountsTo_uVolts (int32 counts)
- float32 ADC_CountsTo_Volts (int32 counts)
- int32 ADC_GetRaw (void)
- int16 ADC_GetResult16 (void)

Function Documentation

uint32 ADC_IsEndConversion (ADC_eoc_enum retMode *)*

Immediately returns the status of the conversion or does not return (blocking) until the conversion completes, depending on the retMode parameter.

Parameters

retMode	Check conversion return mode. See the following table for options.

Options	Description
ADC_RETURN_STATUS	Immediately returns the conversion or calibrate status. If the value returned is non-zero, the conversion is not complete, and this function should be retried until a result of zero is returned.
ADC_WAIT_FOR_RESULT	Does not return a result until the ADC conversion is complete.

Return values

uint32	If a nonzero value is returned, the last conversion is complete. If the returned value is zero, the ADC is still calculating the last result.

uint32 ADC_Calibrate (ADC_calibrate_mode_enum calibrateMode *)*

Configures hardware for calibration. The status of the calibration is either returned immediately or is blocked until the calibration completes, depending on the calibrateMode input.

Parameters

calibrateMode	Check calibration return mode. See the following table for options.

Options	Description
ADC_USE_INTERRUPT	Calibration will be interrupt-driven. Will enter calibration interrupt twice, then set ADC_calStatus to IDLE. Other tasks may be performed while waiting for calibration to complete.
ADC_WAIT_FOR_CALIBRATE	Does not return a result until the ADC calibration is complete.

Return values

uint32	The status of the calibration. If nonzero, calibration was successful.

Side Effects

Sets the UAB interrupt vector to ADC_CalibrateInt during calibration. Sets it back to ADC_ConversionInt when finished.

int16 ADC_RawToResult16 (int32 raw *)*

For given raw input, returns the calibrated ADC result. This result is saturated at the lower and upper limits of the operating resolution. The calibration formula is:

(((raw - calibrateHigh) ∗ (MAX_HIGH - MAX_LOW)) / (calibrateHigh - calibrateLow)) + MAX_LOW.

Parameters

raw	The direct output of the decimator register.

Return values

int16	Saturated, calibrated ADC result.

int16 ADC_CountsTo_mVolts (int32 counts *)*

For given counts input, returns the voltage relative to Vneg. For example, if the input voltage to the ADC was 0.534 volts, the return value would be 534. The equation for the result is:

result (mV) = counts ∗ (reference/(Resolution/2)) ∗ gain + Vneg.

In Differential Mode, Vneg is zero. In Single-Ended Mode, Vneg is the reference.

Resolution is 14-bit.

Note that the multiplication of counts (14-bit max), reference (14-bit max) and gain (2 max) results in a maximum of of 29-bits. An increase in any of these parameters will cause the result to overflow 32 bits.

Parameters

counts	A calibrated ADC result.

Return values

int16	The voltage relative to Vneg in units of millivolts.

int32 ADC_CountsTo_uVolts (int32 counts *)*

For given counts input, returns the voltage relative to Vneg. For example, if the ADC measured 0.534 volts, the return value would be 534000. The equation for the result is:

result (μV) = counts $*$ (reference/(Resolution/2)) $*$ gain + Vneg.

In Differential Mode, Vneg is zero. In Single-Ended Mode, Vneg is reference.

Parameters

counts	A calibrated ADC result.

Return values

int32	The voltage relative to Vneg in units of microvolts.

float32 ADC_CountsTo_Volts (int32 counts *)*

For given counts input, returns the voltage relative to Vneg. For example, if the ADC measured 0.534 volts, the return value would be 0.534. The equation for the result is:

result (V) = counts $*$ (reference/(Resolution/2)) $*$ gain + Vneg.

In Differential Mode, Vneg is zero. In Single-Ended Mode, Vneg is the reference.

Parameters

counts	A calibrated ADC result.

Return values

float32	The voltage relative to Vneg in units of volts.

int32 ADC_GetRaw (void)

Get a raw result value from the ADC's decimator output register.

Return values

int32	Uncalibrated contents of decimator's result register.

int16 ADC_GetResult16 (void)

Returns the calibrated ADC result. This result is saturated at the lower and upper limits of the 14-bit operating resolution.

Return values

int16	Saturated, calibrated ADC result.

Interrupt Service Routine

The ADC generates an interrupt before and after calibration and conversion.

Call ADC_EnableIrq() to enable Component interrupts.

Functions

- void ADC_EnableIRQ (void)
- void ADC_DisableIRQ (void)
- void ADC_ConversionInt_EntryCallback (void)
- void ADC_ConversionInt_ExitCallback (void)
- void ADC_CalibrateInt_EntryCallback (void)
- void ADC_CalibrateInt_ExitCallback (void)
- void ADC_ClearInterrupt (void)

Function Documentation

void ADC_EnableIRQ (void)

Enables interrupt after a conversion finishes. Conversion entry and exit callbacks should be defined in *cyapicallbacks.h*.

void ADC_DisableIRQ (void)

Disables interrupt after a conversion finishes.

void ADC_ConversionInt_EntryCallback (void)

Conversion interrupt entry callback. Perform a task before ADC_StartConvert() takes place.

Enabled by defining ADC_CONVERSIONINT_ENTRY_CALLBACK in the automatically generated *cyapicallbacks.h* file, and defining this function.

Conversion interrupts are only called when interrupts are enabled (see ADC_EnableIRQ()).

void ADC_ConversionInt_ExitCallback (void)

Conversion interrupt exit callback. Perform a task after ADC_StartConvert() completes.

Enabled by defining ADC_CONVERSIONINT_EXIT_CALLBACK in the automatically generated *cyapicallbacks.h* file, and defining this function.

Conversion interrupts are only called when interrupts are enabled (see ADC_EnableIRQ()).

void ADC_CalibrateInt_EntryCallback (void)

Calibrate interrupt entry callback. Perform a task before **each** calibration phase (ADC_Calibrate() has two phases).

Enabled by defining ADC_CALIBRATEINT_ENTRY_CALLBACK in the automatically generated *cyapicallbacks.h* file, and defining this function.

Calibrate interrupts are called every time ADC_Calibrate() is called, even if ADC_EnableIRQ() hasn't been called.

void ADC_CalibrateInt_ExitCallback (void)

Calibrate interrupt exit callback. Perform a task after **each** calibration phase (ADC_Calibrate() has two phases).

Enabled by defining ADC_CALIBRATEINT_EXIT_CALLBACK in the automatically generated *cyapicallbacks.h* file, and defining this function.

Calibrate interrupts are called every time ADC_Calibrate() is called, even if ADC_EnableIRQ() hasn't been called.

void ADC_ClearInterrupt (void)

Clear a pending interrupt. Sets the ADC_HALF_A_INTR_REG register to ADC_DCM_INTR_MASK.

Power

Functions used to control power consumption of the ADC

The following power control functions are available in the Component

Functions

- void ADC_Sleep (void)
- void ADC_Wakeup (void)

Function Documentation

void ADC_Sleep (void)

Prepare the component for entering deep sleep. The underlying hardware requires a high speed clock, so it will not run in deep sleep. Do not call this function before normal sleep (cpu off). For normal sleep, a conversion will continue operating as normal.

void ADC_Wakeup (void)

Restore the component to its pre-deep sleep condition. Should be called just after awaking from deep sleep.

Global Variables

Global variables used in the Component.

Globals are noted in the description of the functions that use globals.

Variables

- uint8 ADC_initVar
- uint8 ADC_irqEnabled

Variable Documentation

uint8 ADC_initVar

Indicates whether the ADC has been initialized. Set by ADC_Start().

uint8 ADC_irqEnabled

Indicates whether the ADC uses interrupts. Set by ADC_EnableIRQ(), cleared by ADC_DisableIRQ().

Global Constants

Global constants used in the Component.

Variables

- ADC_PARAM_VREF_VOLTAGE_MV
- ADC_PARAM_VREF_VOLTAGE_UV_SCALEDOWN
- ADC_PARAM_VREF_VOLTAGE_V

Variable Documentation

ADC_PARAM_VREF_VOLTAGE_MV

Full-scale center voltage, in millivolts.

ADC_PARAM_VREF_VOLTAGE_UV_SCALEDOWN

Full-scale center voltage, in microvolts.

ADC_PARAM_VREF_VOLTAGE_V

Full-scale center voltage, in volts.

Code Examples and Applications

No code examples are available with this initial release.

API Memory Usage

The Component memory usage varies significantly, depending on the compiler, device, number of APIs used and Component configuration. The following table provides the memory usage for all APIs available in

the given Component configuration.

The measurements have been done with the associated compiler configured in the Release mode with an optimization set for size. For a specific design, the map file generated by the compiler can be analyzed to determine the memory usage.

PSoC Analog Coprocessor (GCC)

Configuration	Flash Usage (Bytes)	SRAM Usage (Bytes)
Typical	1976	318
All APIs	2202	318

PSoC Analog Coprocessor Resources

The Incremental ADC Component uses the following device resources:

Resource Type	Used	Free
Interrupts	1	25
IO	3	35
Decimator	1	1
UAB Channels	2	0
Voltage References	1	3

Functional Description

The ADC has two unbuffered switched-capacitor inputs that connect to the pair of voltage signals to be sampled differentially. In single-ended operation, one of these inputs is pre-connected to the potential with which single-ended measurements are defined. This is an internal reference voltage of either 1.2 V or Vdda/2.

Both inputs to the ADC can be taken up to 150 mV more positive than Vdda or 150 mV more negative than Vssa.

The ADC's modulator utilizes a two-phase clock. On phase 1, the Vin terminal is sampled in a non-inverting mode. On phase 2, the Vneg terminal is sampled in an inverting mode. The modulator's output therefore represents the difference between the two input voltages. There is a small time offset (1 μs) between the sampling of the two inputs. This causes a slight deterioration of AC CMRR at high input frequencies.

The 1-bit output of the modulator is decimated by a dedicated filter. The result of that process is further adjusted for scale factor and offset, shifted and saturated in the supplied API functions in order to deliver a final result.

The ADC Hardware makes one complete conversion and then stops.

Raw digital output from the ADC hardware can be accessed through DMA. It does not have any direct use since it has not been calibrated and may have saturated the 14-bit resolution. Fitting of the data into the chosen resolution, including calibration and saturation management, is achieved in the function ADC_RawToResult16(), which returns the desired result in a signed 16-bit integer variable. ADC_RawToResult16()

can also post-process blocks of data stored in SRAM.

Capacitor values affect source impedance

The capacitor values and the clock rate are used to determine the maximum permissible source resistance. This is communicated in the customizer.

Each input connection to the ADC has an apparent resistive input impedance of approximately 1MΩ (under the default clocking arrangements), terminated at the reference potential.

Mismatches in the source impedance in differential mode can cause degradation of CMRR.

Calibration procedure

ADC_Calibrate() triggers a two-point calibration, which results in two calibration constants that are used to correct the offset and scale factor of the conversion results.

The final output is a valid (i.e. not wrapped) signed 14-bit representation of the input voltage, as long as the input voltage is within the input range of the ADC.

Calibration is done at the base 'unity gain' configuration, using differential input voltage of +reference and -reference. The modulator capacitors are then returned to the target values as set by the user's gain requirement, and the results are scaled appropriately.

This form of calibration does not correct for any error in the reference voltage itself, only in the transfer function that relates digital code to the input voltage as a fraction of the reference voltage.

Industry Standards

This section lists the industry standard compliance of the ADC_UAB Component.

MISRA Compliance

This section describes the MISRA-C:2004 compliance and deviations for the Component. There are two types of deviations defined:

Deviation Type	Description
Project deviations	Deviations that are applicable for all PSoC Creator Components
Specific deviations	Deviations that are applicable only for this Component

This section provides information on the Component-specific deviations. The project deviations are described in the MISRA Compliance section of the System Reference Guide along with information on the MISRA compliance verification environment.

The ADC_UAB Component has the following specific deviations:

Rule	Class	Description	Description of Deviation(s)
3.1	R	All usage of implementation-defined behaviour shall be documented.	Embedded Component, UABPRIM, source code has comments containing one of the following characters: '$', '@', or "".

Rule	Class	Description	Description of Deviation(s)
19.7	A	A function should be used in preference to a function-like macro.	UABPRIM source code uses function-like macros to take generic functions and rename them for specific use cases and use predefined parameters making the API easier to use. Also, there are read-modify-write macros that are in most UABPRIM API functions and are used to improve readability of the code so that the intent is clearly understood. The macros have proper parenthesis shielding the parameters as well as the whole macro.

DC and AC Electrical Characteristics

Characterization data of the ADC Component is pending.

Component Errata

This section lists known problems with the ADC Component.

Cypress ID	Component Version	Problem	Workaround
254437	All	If SYSCLK is divided from HFCLK, the filter will not function as expected.	Do not use a SYSCLK divider value other than “1” in the Configure System Clocks -> High Frequency Clocks tab.

Component Changes

This section lists the changes in the ADC_UAB Component from the previous versions.

Version	Description of Changes	Reason for Changes / Impact
1.10	Update UABPRIM version to 1.20	Update UABPRIM to latest version
1.0.b	Added link to Component web page from Symbol	Link missing in prior versions
1.0.a	Edited datasheet	Updated Component symbol to show as Prototype
1.0	Initial Version	

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