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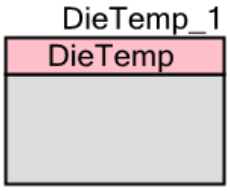
Die Temperature (DieTemp) 2.10

Features



- Accuracy of ±5 °C
- Range –40 °C to +140 °C (0xFFD8 to 0x008C)
- Blocking and non-blocking API

General Description

The Die Temperature (DieTemp) component provides an Application Programming Interface (API) to acquire the temperature of the die. The System Performance Controller (SPC) is used to get the die temperature. The API includes blocking and non-blocking calls.

When to Use a DieTemp

Use a DieTemp component when you want to measure the die temperature of a device.

Input/Output Connections

There are no Input/Output Connections on the DieTemp component. It is a software component only.

Component Parameters

The DieTemp has no configurable parameters other than standard Instance Name and Built-in parameters.

Application Programming Interface

Application Programming Interface (API) routines allow you to configure the component using software. The following table lists and describes the interface to each function. The subsequent sections cover each function in more detail.

By default, PSoC Creator assigns the instance name “DieTemp_1” to the first instance of a component in a given design. You can rename it to any unique value that follows the syntactic rules for identifiers. The instance name becomes the prefix of every global function name, variable, and constant symbol. For readability, the instance name used in the following table is “DieTemp.”

Note that the device should not enter low power modes during temperature measurements. After calling DieTemp_Start() you should wait for the DieTemp_Query() API to report the status of the request that is different from CYRET_STARTED. For more information see the DieTemp_Query() API description.

Functions

Function	Description
DieTemp_Start()	Starts the SPC command to get the die temperature
DieTemp_Stop()	Stops the temperature reading
DieTemp_Query()	Queries the SPC to see if the temperature command is finished
DieTemp_GetTemp()	Sets up the command to get the temperature and blocks until finished

cystatus DieTemp_Start(void)

Description: Sends the command and parameters to the SPC to start a Die Temperature reading. This function returns before the SPC finishes. This function call must always be paired with a call to the DieTemp_Query() API to complete the Die Temperature reading..

Return Value: CYRET_STARTED if the SPC command was started successfully.
CYRET_UNKNOWN if the SPC command failed.
CYRET_LOCKED if the SPC was busy.

void DieTemp_Stop(void)

Description: There is no need to stop or disable this component. This component is naturally a slave that sends request to SPC through SPC API of cy_boot and waits for data to be ready or in case of non-blocking operation it sends request to SPC and user can manually poll the result.

cystatus DieTemp_Query(int16 * temperature)

Description: Checks to see if the SPC command started by DieTemp_Start() has finished. If the command has not finished, the temperature value is not read and returned. The caller will need to poll this function while the return status remains CYRET_STARTED.

This can be used only in conjunction with the DieTemp_Start() API to successfully get the correct Die Temperature.

The Die Temperature reading returned on the first sequence of DieTemp_Start() followed by DieTemp_Query() can be unreliable, so you must do this sequence twice and use the value returned from the second sequence.

Parameters: int16 * temperature: Address to store the temperature in degrees Celsius

Return Value: CYRET_SUCCESS if the temperature command completed successfully.

CYRET_UNKNOWN if the there was an SPC failure.

CYRET_STARTED if the temperature command has not completed.

CYRET_TIMEOUT if waited too long before reading data.

cystatus DieTemp_GetTemp(int16 * temperature)

Description: Sends the command and parameters to the SPC to start a Die Temperature reading and waits until it fails or completes. This is a blocking API.

This function reads the Die Temperature twice and returns the second value to work around an issue in the silicon that causes the first value read to be unreliable.

Parameters: int16 * temperature: Address to store the temperature in degree of Celsius

Return Value: CYRET_SUCCESS if the command was completed successfully.

Along with additional status codes from DieTemp_Start() or DieTemp_Query().

Sample Firmware Source Code

PSoC Creator provides many code examples that include schematics and example code in the Find Code Example dialog. For component-specific examples, open the dialog from the Component Catalog or an instance of the component in a schematic. For general examples, open the dialog from the Start Page or **File** menu. As needed, use the **Filter Options** in the dialog to narrow the list of projects available to select.

Refer to the “Find Code Example” topic in the PSoC Creator Help for more information.

MISRA Compliance

This section describes the MISRA-C:2004 compliance and deviations for the component. There are two types of deviations defined:

- project deviations – deviations that are applicable for all PSoC Creator components
- specific deviations – deviations that are applicable only for this component

This section provides information on component-specific deviations. Project deviations are described in the MISRA Compliance section of the *System Reference Guide* along with information on the MISRA compliance verification environment.

The DieTemp component does not have any specific deviations.

API Memory Usage

The component memory usage varies significantly, depending on the compiler, device, number of APIs used and component configuration. The following table provides the memory usage for all APIs available in the given component configuration.

The measurements have been done with the associated compiler configured in Release mode with optimization set for Size. For a specific design the map file generated by the compiler can be analyzed to determine the memory usage.

Configuration	PSoC 3 (Keil_PK51)		PSoC 5LP (GCC)	
	Flash Bytes	SRAM Bytes	Flash Bytes	SRAM Bytes
Default	339	0	210	0

Resources

The DieTemp uses the on-chip Temperature sensor to measure the internal die temperature.

DC and AC Electrical Characteristics

Specifications are valid for –40 °C ≤ T_A ≤ 85 °C and T_J ≤ 100 °C, except where noted. Specifications are valid for 1.71 V to 5.5 V, except where noted.

Parameter	Description	Conditions	Min	Typical	Max	Units
	Temp sensor accuracy	Range: –40 °C to +85 °C	–	±5	–	°C

Component Changes

This section lists the major changes in the component from the previous version.

Version	Description of Changes	Reason for Changes / Impact
2.10	New Component version to remove the long delay (> 6 ms) in the DieTemp_Query() function.	The DieTemp_Query() function had a long delay (> 6 ms) when the result was not ready.
2.0.c	Minor datasheet edits.	
2.0.b	Datasheet update.	Added a note to not enter low power modes during temperature measurements.
2.0.a	Minor datasheets edits and updates. Removed PSoC 5 references.	 PSoC 5 replaced by PSoC 5LP.
2.0	Removed CySpcStop() API call from DieTemp_Stop().	SPC cannot be stopped from runtime chip operation.
	Added MISRA Compliance section.	The component does not have any specific deviations.
1.80	Added PSoC 5LP support.	
	DieTemp APIs were updated.	Due to changes to the cyboot SPC APIs.
1.70	The DieTemp_GetTemp() API was changed to read the DieTemp twice to fix an issue.	The DieTemp_GetTemp() API was returning the temperature even if the first read was unsuccessful.
1.60	The *DieTemp.c* file GetTemp API was edited to fix the power-on reset error output.	The DieTemp output on power-on reset was erroneous.
1.50.a	Added characterization data to datasheet	
	Added information to the component that advertises its compatibility with silicon revisions.	The tool returns an error/warns if the component is used on incompatible silicon. This component is not compatible with PSoC 3 ES2 or PSoC 5.
	Minor datasheet edits and updates	
1.50	Switch from *cydevice.h* to *cydevice_trm.h*.	The *cydevice.h* file has been made obsolete, so the APIs and generated code provided with PSoC Creator are not included with *cydevice_trm.h*.

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