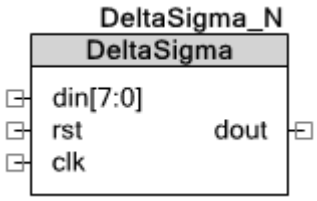
Digital Delta Sigma Modulator

JLS1_DeltaSigma v1.0 **JLS1**

Features



- Delta Sigma Modulator of a byte wide input signal
- Generates a single bit modulated output
- Asynchronous reset

General Description

This component takes a byte wide input and modulates that signal using a Delta Sigma modulator resulting in a single bit output.

Pin Description

Pin	Type	Function
din[7:0]	Input	8-bit Signal
rst	Input	Asynchronous reset
clk	Input	Clock
dout	Output	Modulated output

Support

PSoC Creator Community Components are developed and supported by the Cypress Developer Community. Go to www.cypress.com/CommunityComponents to discuss this and other Community Components.