Logic High/Logic Low

N/A

Features



- Constant digital high or low signal

General Description

The Logic High and Logic Low components provide constant digital values and are used to hard code digital inputs. Hard coding of static inputs results in optimized resource use and is the preferred method of providing a constant input state.

When to use a Logic High or Logic Low

Use the Logic High and Logic Low to provide a digital value for a component input when the value does not need to change. For example, a Logic High could be connected to the enable terminal of a timer component so that the timer is always enabled.

Input/Output Connections

This section describes the various input and output connections for the Logic High and Logic Low.

0 – Output (Logic Low)

Provides a digital value that is always false.

1 – Output (Logic High)

Provides a digital value that is always true.

Resources

All digital logic gates are converted to a sum of products and placed into a Universal Digital Block (UDB) programmable logic. This process results in digital logic gates being automatically optimized and placed into the PSoC device. Resource use depends on the specific logic created and cannot be determined before project compilation in PSoC Creator.

Component Changes

This section lists the major changes in the component from the previous version.

Version	Description of Changes	Reason for Changes / Impact
	Minor datasheet edits	
	Removed Preliminary label from datasheet	Production release
	Changed version number in datasheet to N/A	

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