

TRENCHSTOP™ 5 Advanced Isolation

High speed 5 IGBT in TRENCHSTOP™ 5 technology copacked with RAPID 1 fast and soft antiparallel diode in fully isolated package

Features and Benefits:

TRENCHSTOP™ 5 technology offering

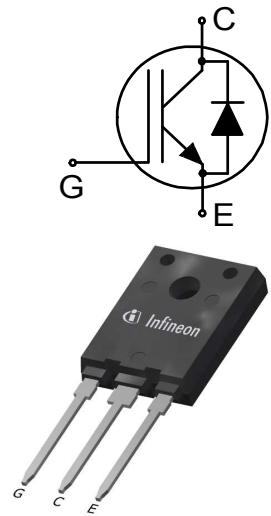
- Best-in-Class efficiency in hard switching and resonant topologies
- Plug and play replacement of previous generation IGBTs
- 650V breakdown voltage
- Low gate charge Q_G
- Very soft, fast recovery antiparallel diode
- Maximum junction temperature 175°C
- 2500V_{RMS} electrical isolation, 50/60Hz, $t=1\text{min}$
- 100% tested isolated mounting surface
- Pb-free lead plating; RoHS compliant
- Complete product spectrum and PSpice Models:
<http://www.infineon.com/igbt/>

Potential Applications:

- Residential and Commercial Aircon PFC
- Welding converters
- Mid to high range switching frequency converters

Product Validation:

Qualified for industrial applications according to the relevant tests of JEDEC47/20/22



Fully isolated package TO-247

**Key Performance and Package Parameters**

Type	V_{CE}	I_C	$V_{CEsat}, T_{vj}=25^\circ\text{C}$	T_{vjmax}	Marking	Package
IKFW50N65DH5	650V	50A	1.8V	175°C	K50EDH5	PG-HSIP247-3-2



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Maximum Ratings

For optimum lifetime and reliability, Infineon recommends operating conditions that do not exceed 80% of the maximum ratings stated in this datasheet.

Parameter	Symbol	Value	Unit
Collector-emitter voltage, $T_{vj} \geq 25^{\circ}\text{C}$	V_{CE}	650	V
DC collector current, limited by T_{vjmax} $T_h = 25^{\circ}\text{C}$ $T_h = 65^{\circ}\text{C}$ $T_h = 65^{\circ}\text{C}$	I_C	59.0 49.0 69.0 ¹⁾	A
Pulsed collector current, t_p limited by T_{vjmax}	I_{Cpuls}	160.0	A
Turn off safe operating area $V_{CE} \leq 650\text{V}$, $T_{vj} \leq 175^{\circ}\text{C}$, $t_p = 1\mu\text{s}$	-	160.0	A
Diode forward current, limited by T_{vjmax} $T_h = 25^{\circ}\text{C}$ $T_h = 65^{\circ}\text{C}$	I_F	40.0 32.0	A
Diode pulsed current, t_p limited by T_{vjmax}	I_{Fpuls}	160.0	A
Gate-emitter voltage Transient Gate-emitter voltage ($t_p \leq 10\mu\text{s}$, $D < 0.010$)	V_{GE}	± 20 ± 30	V
Power dissipation $T_h = 25^{\circ}\text{C}$ Power dissipation $T_h = 65^{\circ}\text{C}$	P_{tot}	124.0 91.0	W
Operating junction temperature	T_{vj}	-40...+175	$^{\circ}\text{C}$
Storage temperature	T_{stg}	-55...+150	$^{\circ}\text{C}$
Soldering temperature, wave soldering 1.6mm (0.063in.) from case for 10s		260	$^{\circ}\text{C}$
Mounting torque, M3 screw Maximum of mounting processes: 3	M	0.6	Nm
Isolation voltage RMS, $f = 50/60\text{Hz}$, $t = 1\text{min}^{2)}$	V_{isol}	2500	V

Thermal Resistance

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
R _{th} Characteristics						
IGBT thermal resistance, ³⁾ junction - heatsink	R _{th(j-h)}		-	1.03	1.21	K/W
Diode thermal resistance, ³⁾ junction - heatsink	R _{th(j-h)}		-	1.64	1.93	K/W
Thermal resistance junction - ambient	R _{th(j-a)}		-	-	65	K/W

¹⁾ Equivalent current rating in TO-247-3 at $T_h = 65^{\circ}\text{C}$ using reference insulation material: 152 μm , 0.9 W/mK, standard polyimide based reinforced carrier insulator

²⁾ For a proper handling and assembly of the advanced isolation device in the application refer to the note at the package drawing.

³⁾ At force on body $F = 500\text{N}$, $T_h = 25^{\circ}\text{C}$

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Electrical Characteristic, at $T_{vj} = 25^{\circ}\text{C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Static Characteristic						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{GE} = 0V, I_C = 0.50mA$	650	-	-	V
Collector-emitter saturation voltage	V_{CEsat}	$V_{GE} = 15.0V, I_C = 50.0A$ $T_{vj} = 25^{\circ}C$ $T_{vj} = 175^{\circ}C$	- -	1.80 2.25	2.25 -	V
Diode forward voltage	V_F	$V_{GE} = 0V, I_F = 25.0A$ $T_{vj} = 25^{\circ}C$ $T_{vj} = 175^{\circ}C$	- -	1.50 1.45	1.90 -	V
Gate-emitter threshold voltage	$V_{GE(th)}$	$I_C = 0.50mA, V_{CE} = V_{GE}$	3.2	4.0	4.8	V
Zero gate voltage collector current	I_{CES}	$V_{CE} = 650V, V_{GE} = 0V$ $T_{vj} = 25^{\circ}C$ $T_{vj} = 175^{\circ}C$	- -	- -	40 4000	μA
Gate-emitter leakage current	I_{GES}	$V_{CE} = 0V, V_{GE} = 20V$	-	-	100	nA
Transconductance	g_{fs}	$V_{CE} = 20V, I_C = 50.0A$	-	50.0	-	S

Electrical Characteristic, at $T_{vj} = 25^{\circ}\text{C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Dynamic Characteristic						
Input capacitance	C _{ies}	V _{CE} = 25V, V _{GE} = 0V, f = 1MHz	-	2347	-	pF
Output capacitance	C _{oes}		-	57	-	
Reverse transfer capacitance	C _{res}		-	9	-	
Gate charge	Q _G	V _{CC} = 520V, I _C = 50.0A, V _{GE} = 15V	-	95.0	-	nC
Internal emitter inductance measured 5mm (0.197 in.) from case	L _E		-	13.0	-	nH

Switching Characteristic, Inductive Load

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
IGBT Characteristic, at $T_{vj} = 25^{\circ}\text{C}$						
Turn-on delay time	$t_{d(\text{on})}$	$T_{vj} = 25^{\circ}\text{C},$ $V_{CC} = 400\text{V}, I_C = 50.0\text{A},$ $V_{GE} = 0.0/15.0\text{V},$ $R_{G(\text{on})} = 12.2\Omega, R_{G(\text{off})} = 12.2\Omega,$ $L\sigma = 30\text{nH}, C\sigma = 30\text{pF}$ $L\sigma, C\sigma$ from Fig. E Energy losses include “tail” and diode reverse recovery.	-	23	-	ns
Rise time	t_r		-	36	-	ns
Turn-off delay time	$t_{d(\text{off})}$		-	131	-	ns
Fall time	t_f		-	20	-	ns
Turn-on energy	E_{on}		-	1.46	-	mJ
Turn-off energy	E_{off}		-	0.63	-	mJ
Total switching energy	E_{ts}		-	2.09	-	mJ

Diode Characteristic, at $T_{vj} = 25^{\circ}\text{C}$

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Diode reverse recovery time	t_{rr}	$T_{vj} = 25^{\circ}\text{C},$ $V_R = 400\text{V},$ $I_F = 25.0\text{A},$ $di_F/dt = 1000\text{A}/\mu\text{s}$	-	68	-	ns
Diode reverse recovery charge	Q_{rr}		-	0.55	-	μC
Diode peak reverse recovery current	I_{rrm}		-	12.0	-	A
Diode peak rate of fall of reverse recovery current during t_b	di_{rr}/dt		-	-834	-	$\text{A}/\mu\text{s}$

Switching Characteristic, Inductive Load

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	

IGBT Characteristic, at $T_{vj} = 150^{\circ}\text{C}$

Turn-on delay time	$t_{d(on)}$	$T_{vj} = 150^{\circ}\text{C},$ $V_{CC} = 400\text{V}, I_C = 50.0\text{A},$ $V_{GE} = 0.0/15.0\text{V},$ $R_{G(on)} = 12.2\Omega, R_{G(off)} = 12.2\Omega,$ $L\sigma = 30\text{nH}, C\sigma = 30\text{pF}$ $L\sigma, C\sigma$ from Fig. E Energy losses include "tail" and diode reverse recovery.	-	21	-	ns
Rise time	t_r		-	36	-	ns
Turn-off delay time	$t_{d(off)}$		-	155	-	ns
Fall time	t_f		-	22	-	ns
Turn-on energy	E_{on}		-	1.74	-	mJ
Turn-off energy	E_{off}		-	0.71	-	mJ
Total switching energy	E_{ts}		-	2.45	-	mJ

Diode Characteristic, at $T_{vj} = 150^{\circ}\text{C}$

Diode reverse recovery time	t_{rr}	$T_{vj} = 150^{\circ}\text{C},$ $V_R = 400\text{V},$ $I_F = 25.0\text{A},$ $di_F/dt = 1000\text{A}/\mu\text{s}$	-	104	-	ns
Diode reverse recovery charge	Q_{rr}		-	1.43	-	μC
Diode peak reverse recovery current	I_{rrm}		-	19.8	-	A
Diode peak rate of fall of reverse recovery current during t_b	di_{rr}/dt		-	-637	-	$\text{A}/\mu\text{s}$

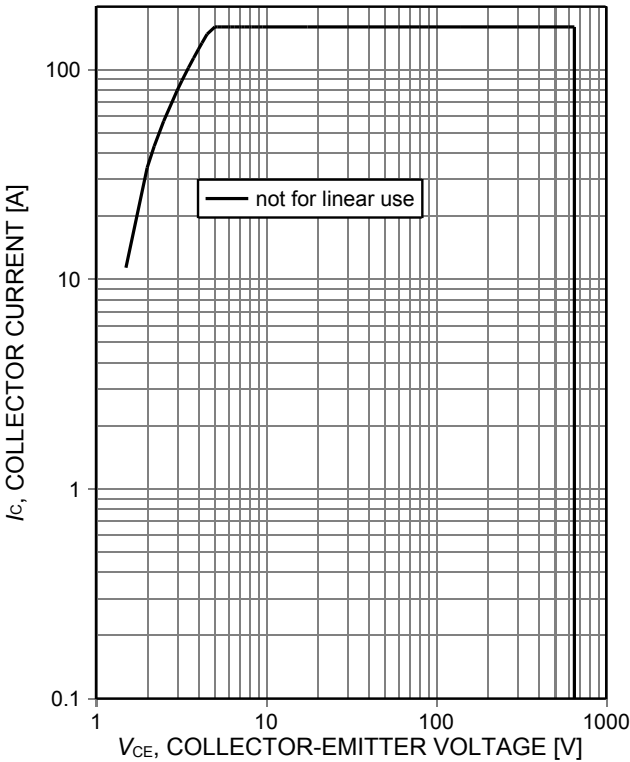


Figure 1. **Forward bias safe operating area**
($D=0$, $T_h=25^{\circ}\text{C}$, $T_j\leq 175^{\circ}\text{C}$, $V_{GE}=15\text{V}$, $t_p\leq 1\mu\text{s}$)

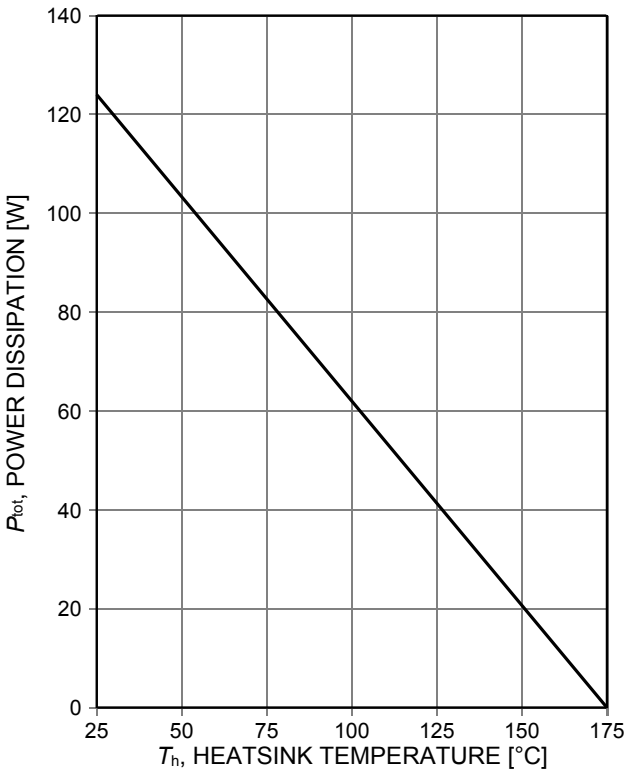


Figure 2. **Power dissipation as a function of heatsink temperature**
($T_j\leq 175^{\circ}\text{C}$)

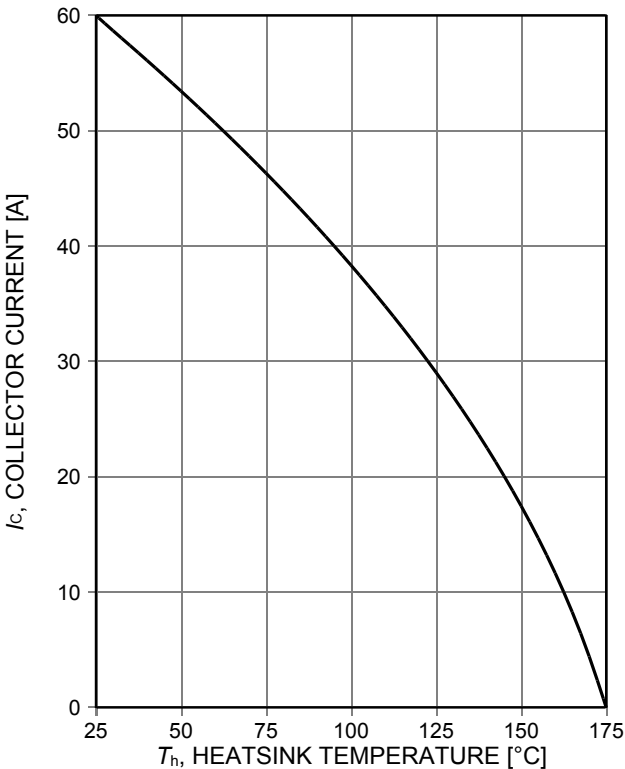


Figure 3. **Collector current as a function of heatsink temperature**
($V_{GE}\geq 15\text{V}$, $T_j\leq 175^{\circ}\text{C}$)

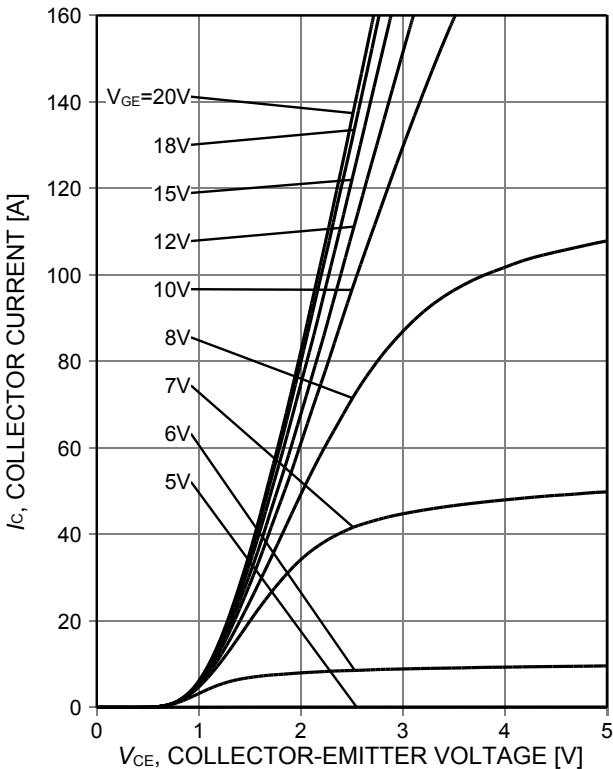


Figure 4. **Typical output characteristic**
($T_j=25^{\circ}\text{C}$)

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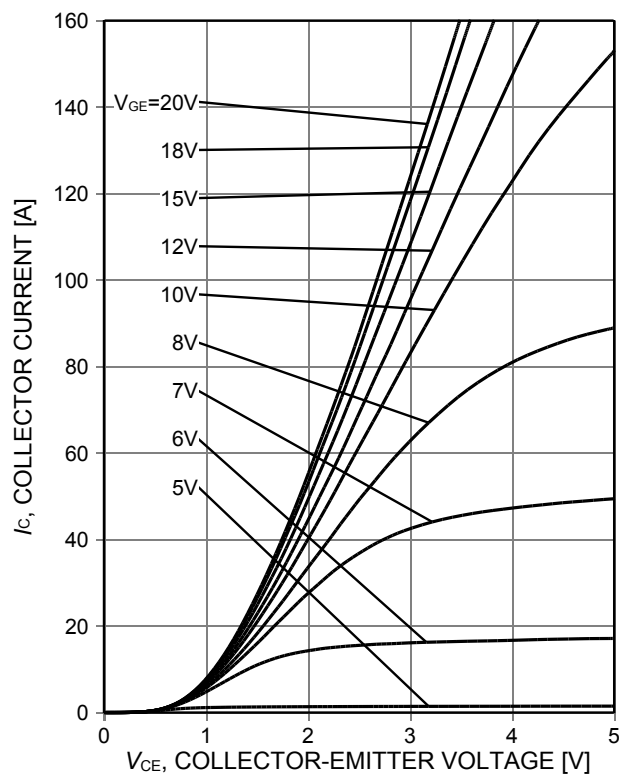


Figure 5. **Typical output characteristic**
($T_j=150^\circ\text{C}$)

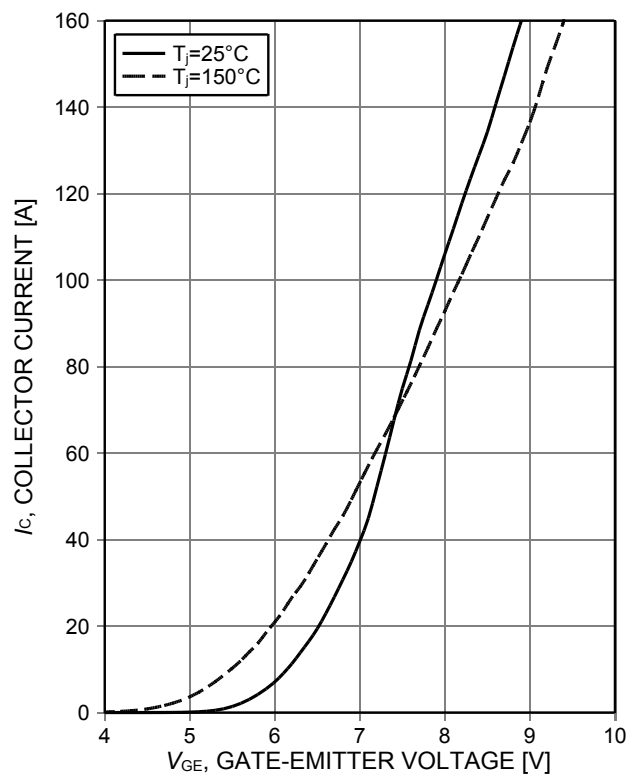


Figure 6. **Typical transfer characteristic**
($V_{CE}=20\text{V}$)

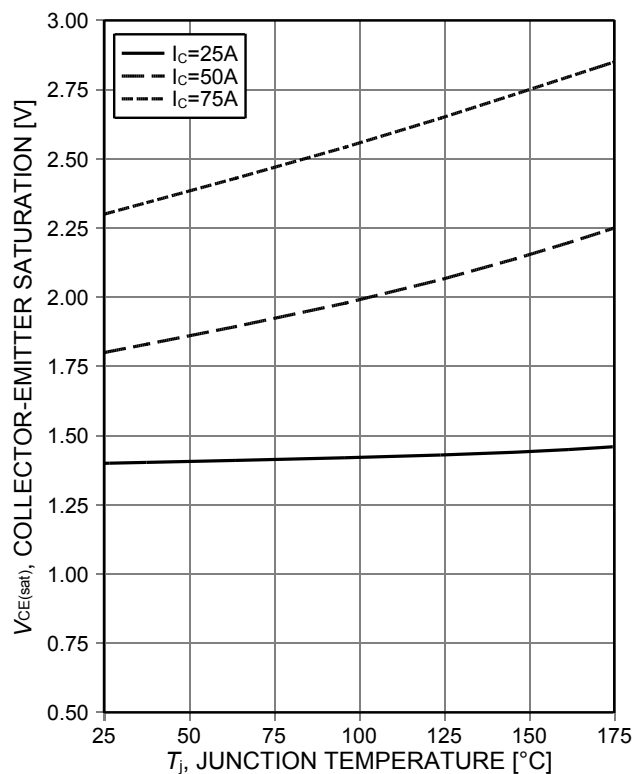


Figure 7. **Typical collector-emitter saturation voltage as a function of junction temperature**
($V_{GE}=15\text{V}$)

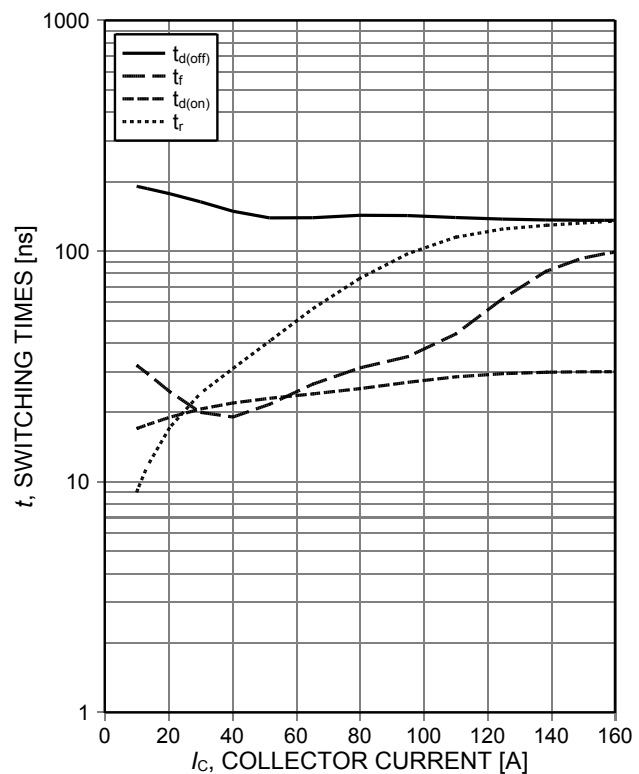


Figure 8. **Typical switching times as a function of collector current**
(ind. load, $T_j=150^\circ\text{C}$, $V_{CE}=400\text{V}$, $V_{GE}=0/15\text{V}$, $R_G=12.2\Omega$, test circuit in Fig. E)

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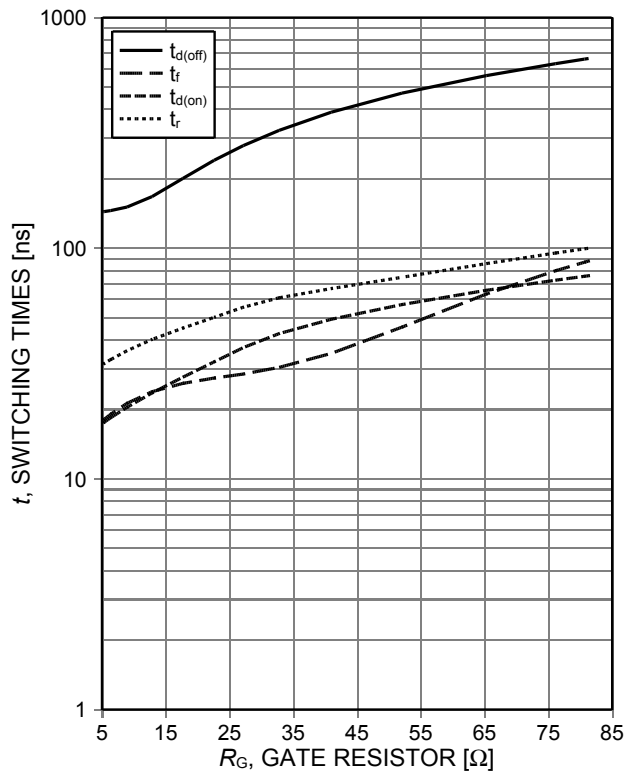


Figure 9. **Typical switching times as a function of gate resistor**
(ind. load, $T_j=150^\circ\text{C}$, $V_{CE}=400\text{V}$, $V_{GE}=0/15\text{V}$, $I_C=50\text{A}$, test circuit in Fig. E)

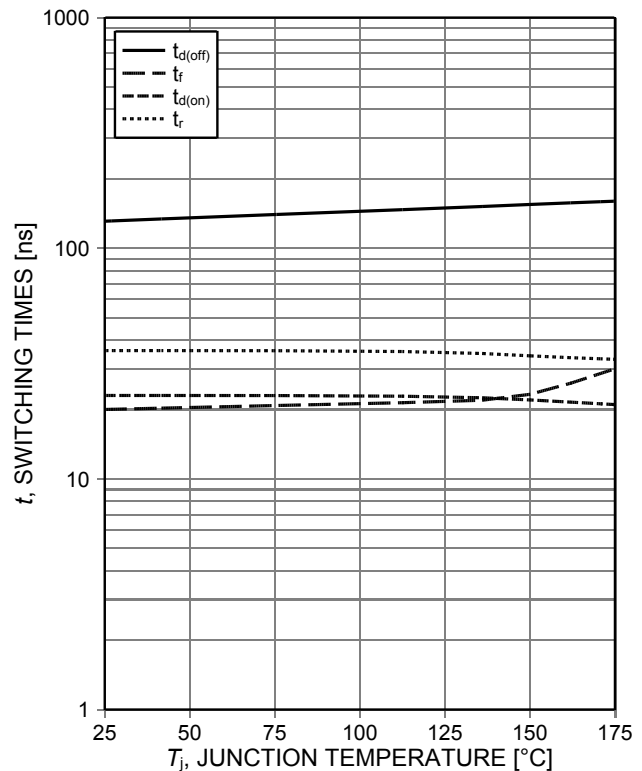


Figure 10. **Typical switching times as a function of junction temperature**
(ind. load, $V_{CE}=400\text{V}$, $V_{GE}=0/15\text{V}$, $I_C=50\text{A}$, $r_G=12.2\Omega$, test circuit in Fig. E)

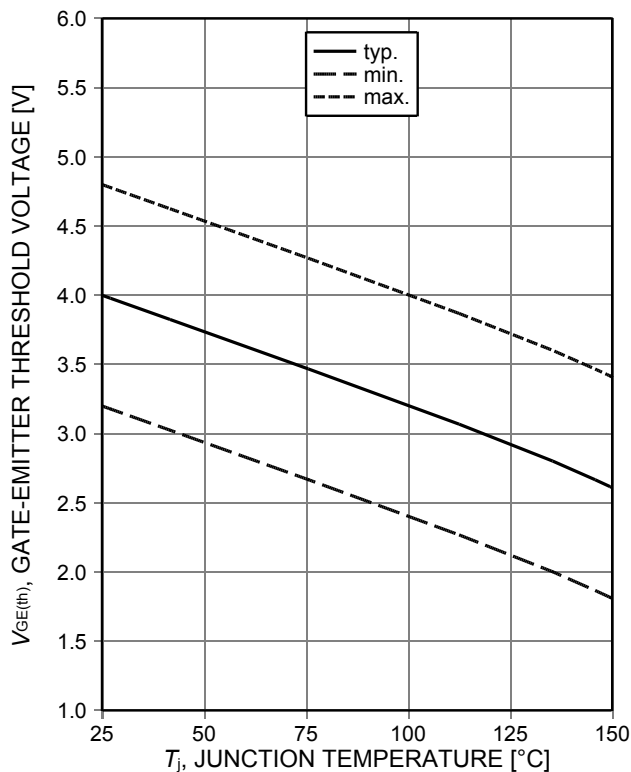


Figure 11. **Gate-emitter threshold voltage as a function of junction temperature**
($I_C=0.5\text{mA}$)

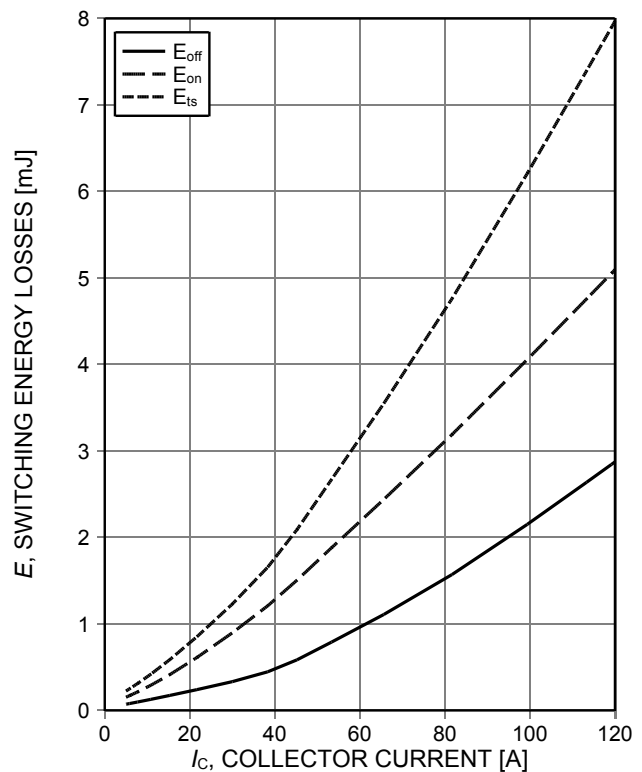


Figure 12. **Typical switching energy losses as a function of collector current**
(ind. load, $T_j=150^\circ\text{C}$, $V_{CE}=400\text{V}$, $V_{GE}=0/15\text{V}$, $R_G=12.2\Omega$, test circuit in Fig. E)

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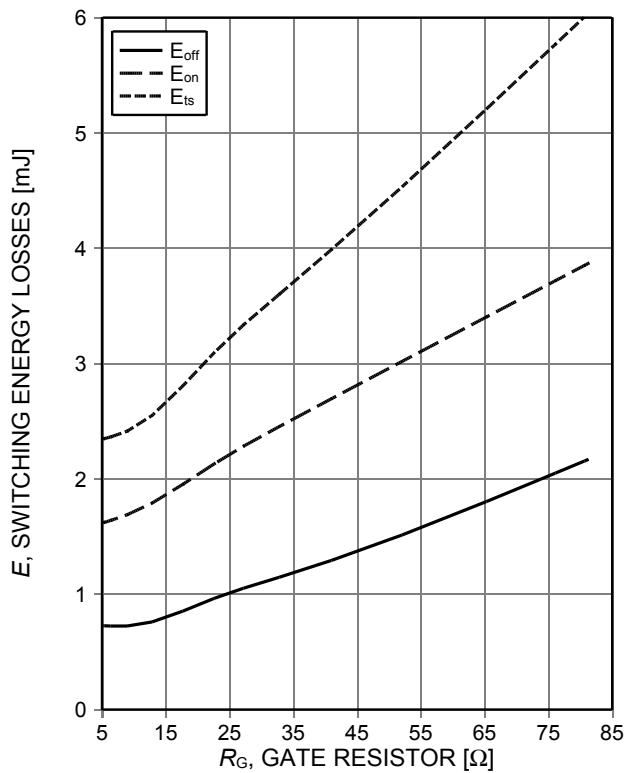


Figure 13. **Typical switching energy losses as a function of gate resistor**
(ind. load, $T_J=150^\circ\text{C}$, $V_{CE}=400\text{V}$, $V_{GE}=0/15\text{V}$, $I_C=50\text{A}$, test circuit in Fig. E)

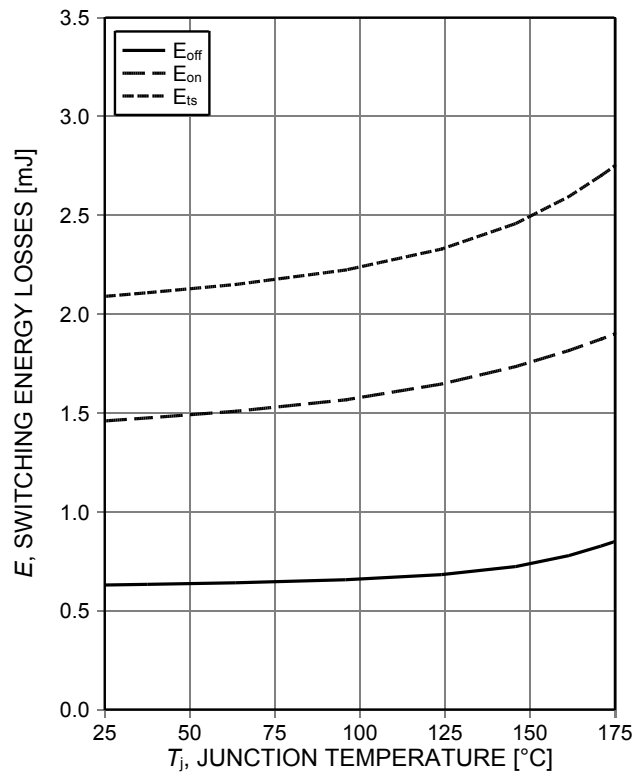


Figure 14. **Typical switching energy losses as a function of junction temperature**
(ind. load, $V_{CE}=400\text{V}$, $V_{GE}=0/15\text{V}$, $I_C=50\text{A}$, $R_G=12.2\Omega$, test circuit in Fig. E)

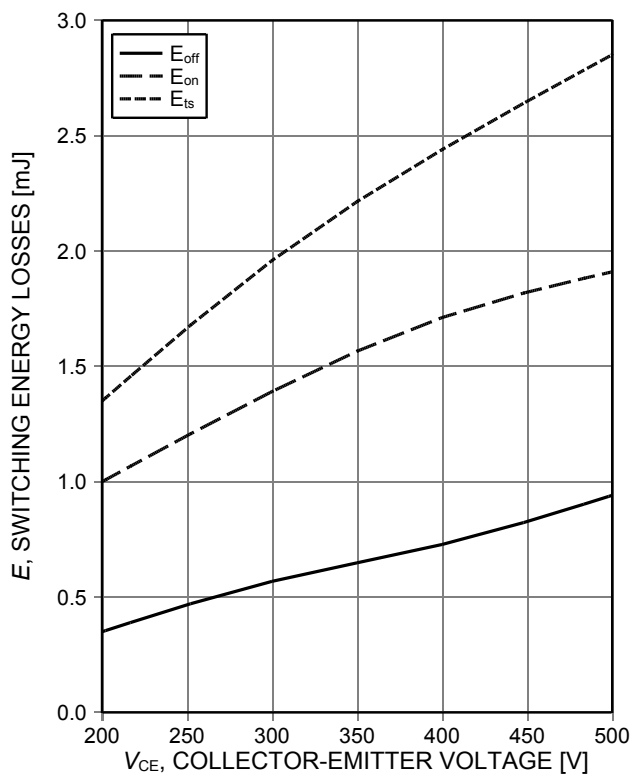


Figure 15. **Typical switching energy losses as a function of collector emitter voltage**
(ind. load, $T_J=150^\circ\text{C}$, $V_{GE}=0/15\text{V}$, $I_C=50\text{A}$, $R_G=12.2\Omega$, test circuit in Fig. E)

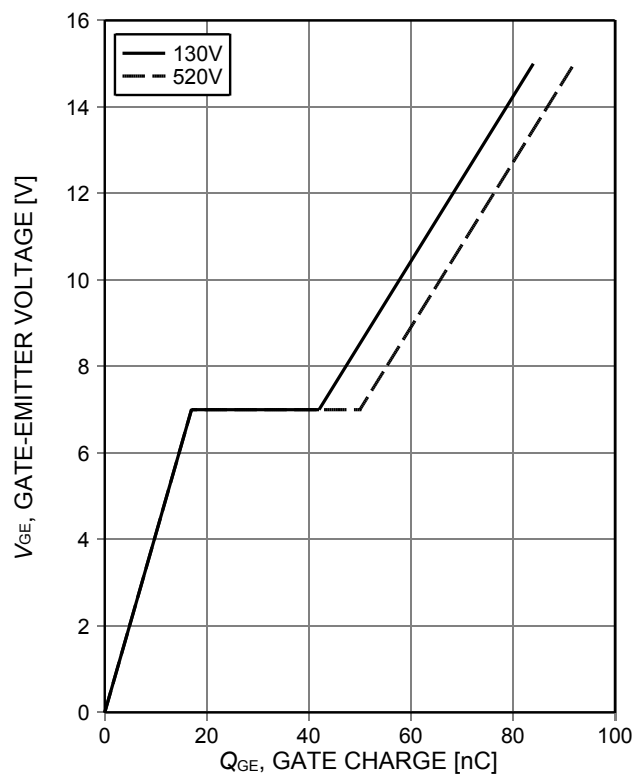


Figure 16. **Typical gate charge**
($I_C=50\text{A}$)

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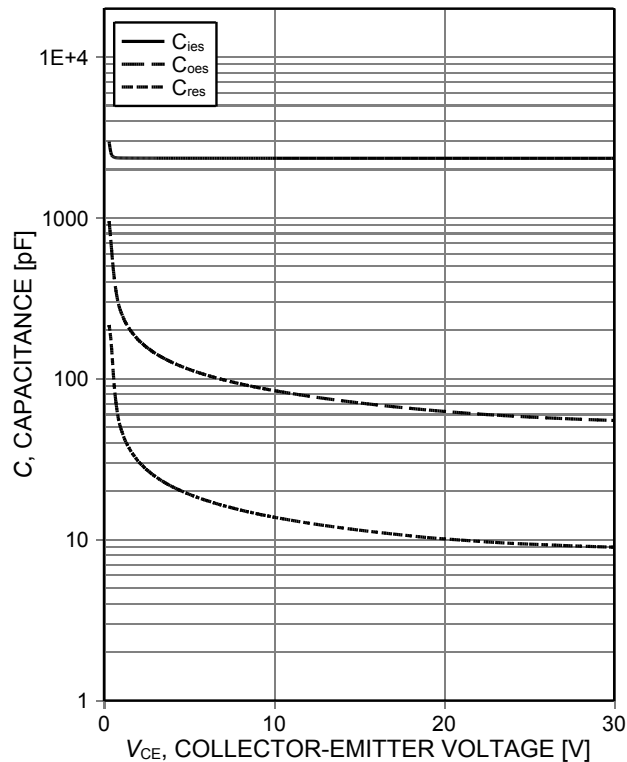


Figure 17. **Typical capacitance as a function of collector-emitter voltage**
($V_{GE}=0V$, $f=1MHz$)

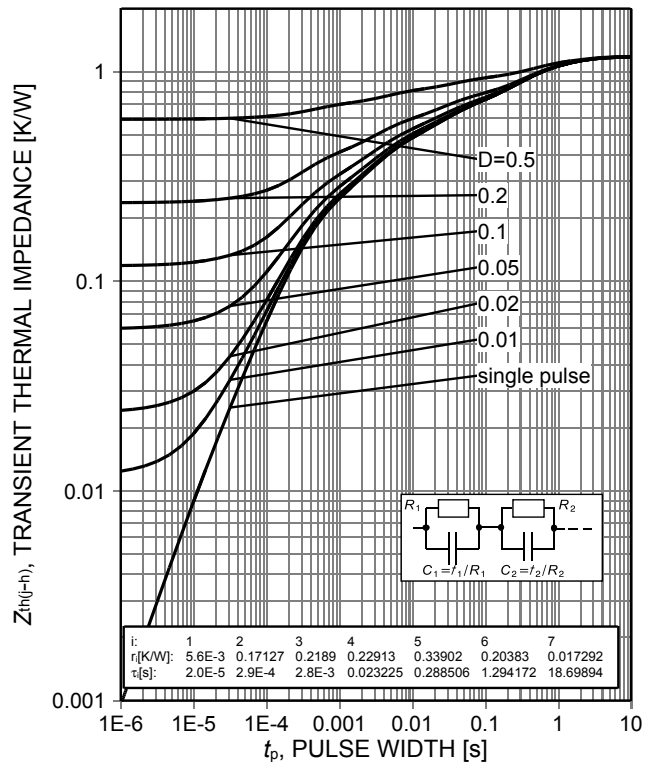


Figure 18. **IGBT transient thermal impedance as a function of pulse width**
($D=t_p/T$)

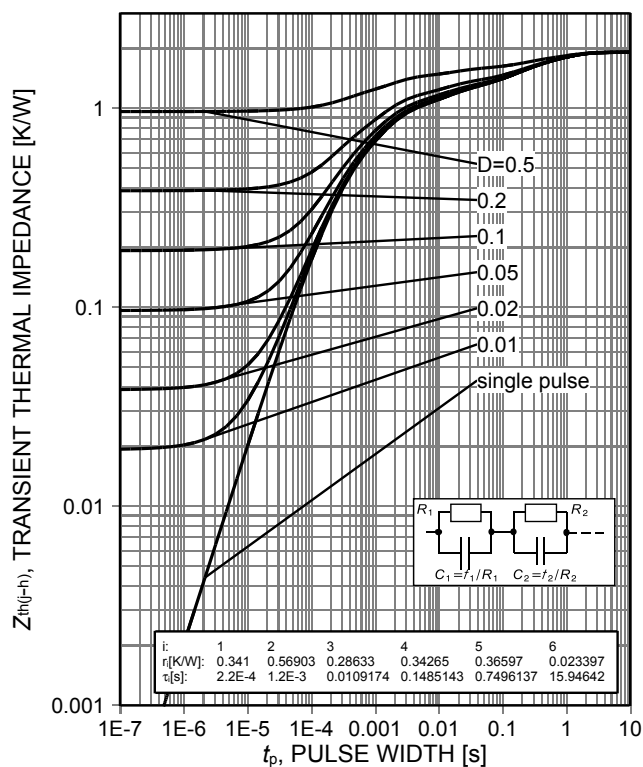


Figure 19. **Diode transient thermal impedance as a function of pulse width**
($D=t_p/T$)

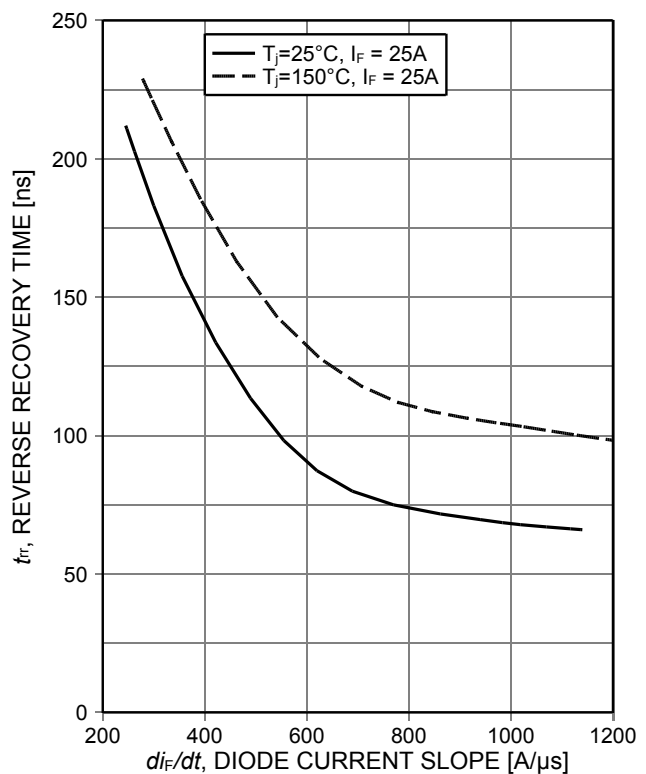


Figure 20. **Typical reverse recovery time as a function of diode current slope**
($V_R=400V$)

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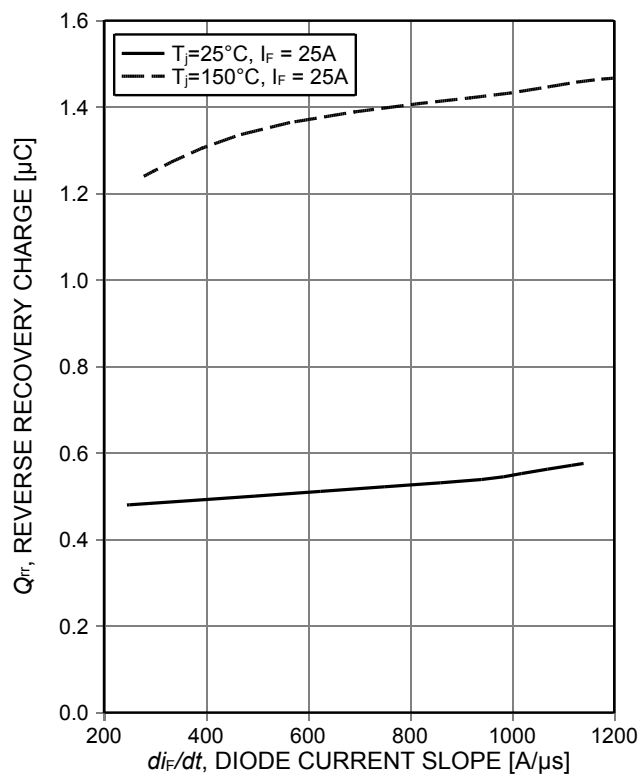


Figure 21. Typical reverse recovery charge as a function of diode current slope ($V_R=400\text{V}$)

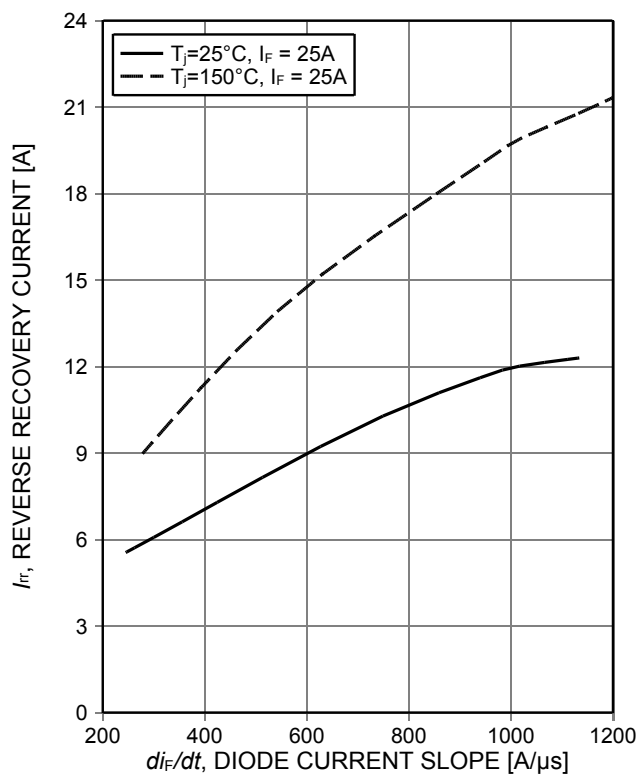


Figure 22. Typical reverse recovery current as a function of diode current slope ($V_R=400\text{V}$)

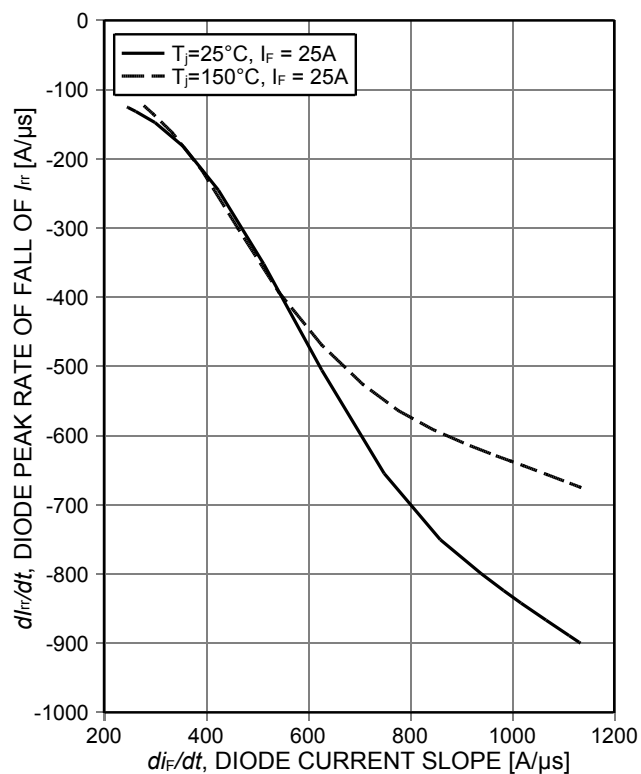


Figure 23. Typical diode peak rate of fall of reverse recovery current as a function of diode current slope ($V_R=400\text{V}$)

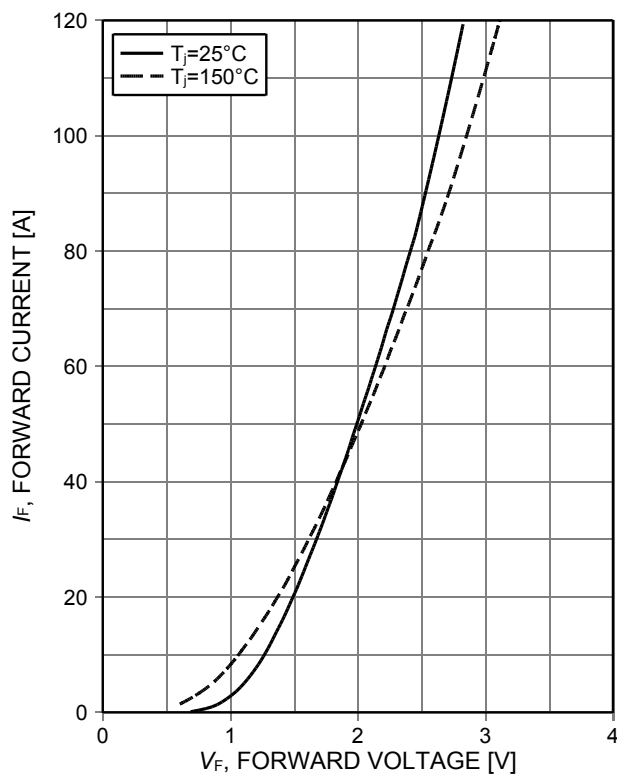
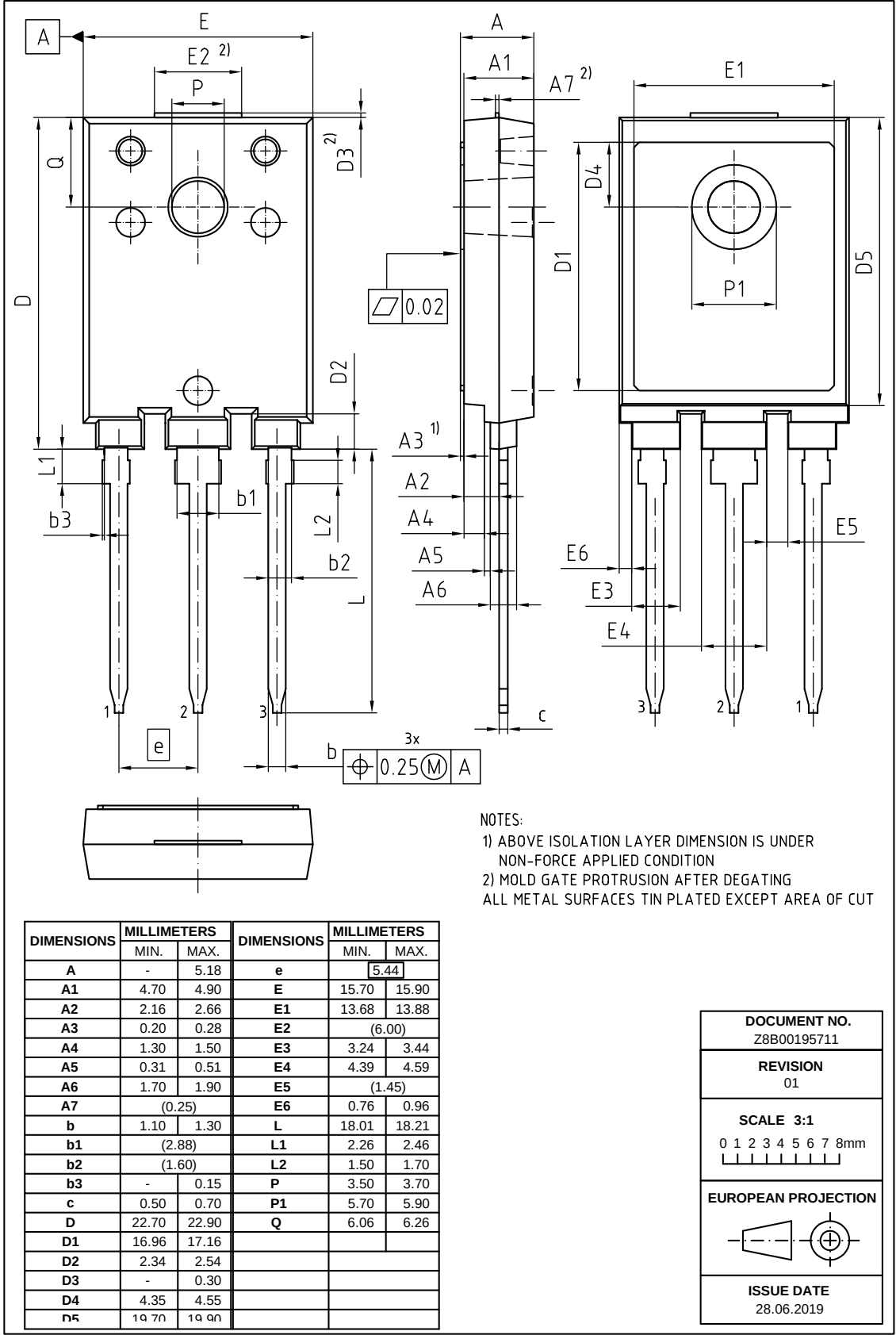


Figure 24. Typical diode forward current as a function of forward voltage

PG-HSIP247-3-2



Testing Conditions

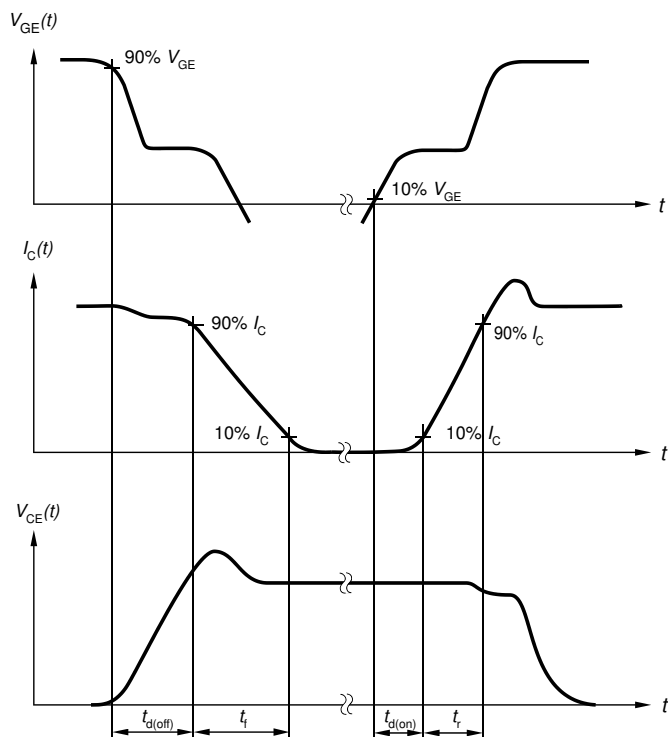


Figure A. Definition of switching times

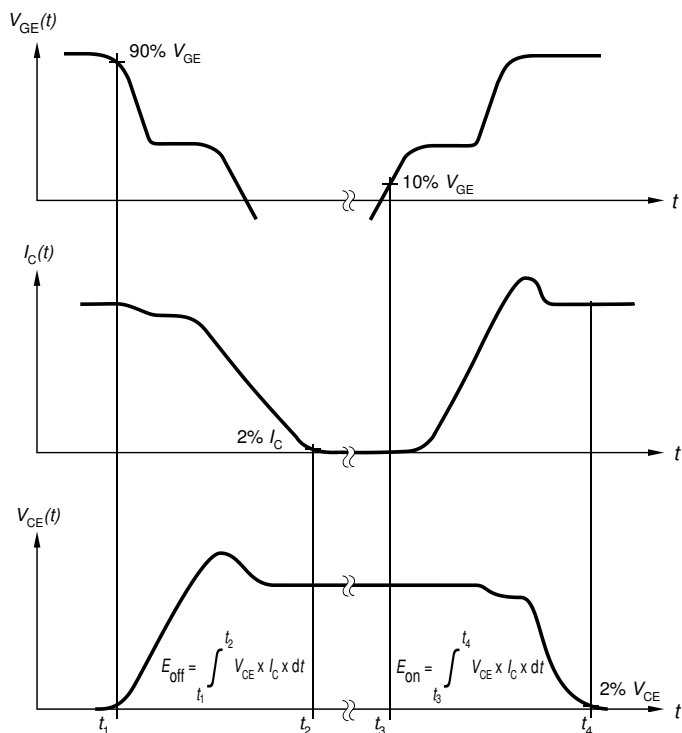


Figure B. Definition of switching losses

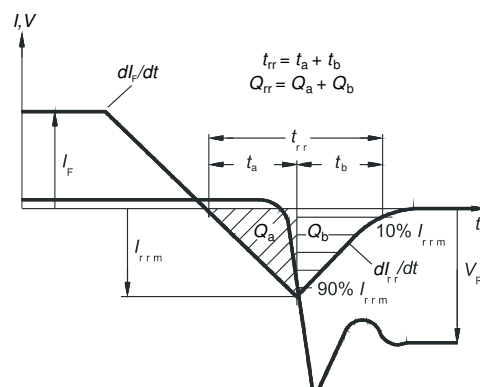


Figure C. Definition of diode switching characteristics

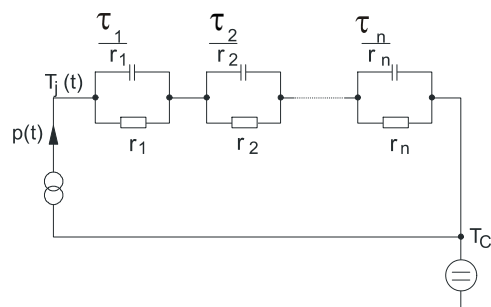


Figure D. Thermal equivalent circuit

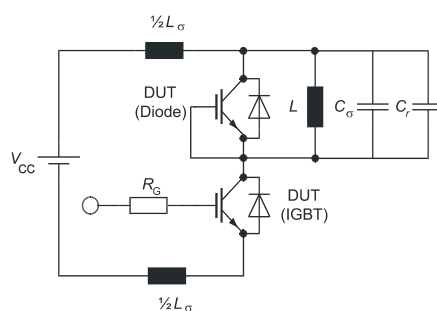


Figure E. **Dynamic test circuit**
Parasitic inductance L_σ ,
parasitic capacitor C_σ ,
relief capacitor C_r ,
(only for ZVT switching)

Revision History

IKFW50N65DH5

Revision: 2019-07-08, Rev. 2.1

Previous Revision

Revision	Date	Subjects (major changes since last revision)
2.1	2019-07-08	Final Data Sheet

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