

Final datasheet
CoolSiC™ 1200 V Schottky diode G5 : Silicon Carbide Diode with top side cooling and .XT technology

Features

- $V_{RRM} = 1200\text{ V}$
- $I_F = 10\text{ A}$
- No reverse recovery current / no forward recovery
- High surge current capability
- Temperature independent switching behavior
- Low forward voltage
- dv/dt ruggedness
- Pb-free lead plating; RoHS compliant
- .XT interconnection technology for best-in-class thermal performance

Potential applications

- EV Charging
- DIN Rail & Rack Mount (Power Supply)
- Commercial Lighting Fixture

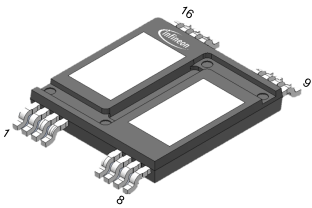
Product validation

- Qualified for industrial applications according to the relevant tests of JEDEC47/20/22

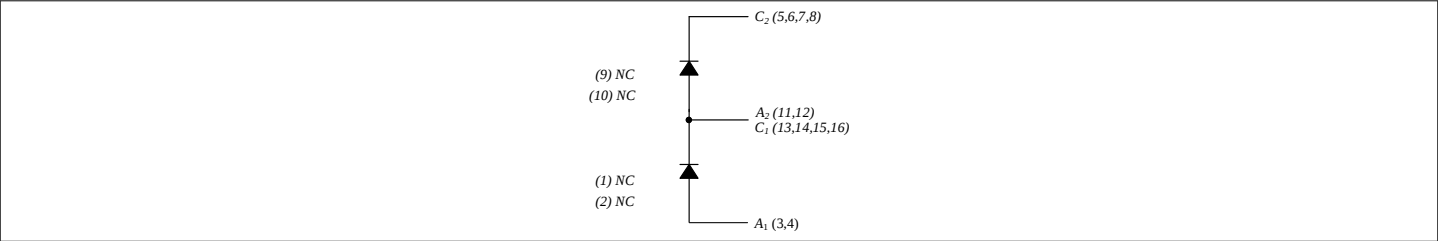
Description

Pin definition:

- 13-16 & Case 1 – Cathode 1 (C_1)
- 1, 2 – not connected (NC)
- 3-4 – Anode 1 (A_1)
- 5-8 & Case 2 – Cathode 2 (C_2)
- 9, 10 – not connected (NC)
- 11-12 – Anode 2 (A_2)



- Lead-free
- Green
- Halogen-free
- RoHS



Type	Package	Marking
IDSQ10G120C5D	PG-HDSOP-16-U03	D1012C5



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1 Package

Table 1 Characteristic values

Parameter	Symbol	Note or test condition	Values			Unit
			Min.	Typ.	Max.	
Storage temperature	T_{stg}		-55		150	°C
Soldering temperature	T_{sold}	Reflow soldering (MSL2 according to JEDEC J-STD-020)			260	°C
Thermal resistance, junction-ambient	$R_{th(j-a)}$				62	K/W
Diode thermal resistance, junction-case	$R_{th(j-c)}$			0.54	0.7	K/W
Comparative Tracking Index	CTI	IEC 60112 (material group 1 according to IEC 60664-1)	600			V

2 SiC Diode

Table 2 Maximum rated values

Parameter	Symbol	Note or test condition		Values	Unit
Repetitive peak reverse voltage	V_{RRM}	$T_{vj} \geq 25\text{ °C}$		1200	V
Continuous forward current for $R_{th(j-c,max)}$	I_F		$T_c = 25\text{ °C}$	39	A
			$T_c = 135\text{ °C}$	19	
			$T_c = 161\text{ °C}$	10	
Surge repetitive forward current, sine halfwave ¹⁾	$I_{F,RM}$	$t_p = 10\text{ ms}$	$T_c = 25\text{ °C}$	40	A
			$T_c = 100\text{ °C}$	30	
Surge non-repetitive forward current, sine halfwave	$I_{F,SM}$	$t_p = 10\text{ ms}$	$T_c = 25\text{ °C}$	108	A
			$T_c = 150\text{ °C}$	91	
Non-repetitive peak forward current	$I_{F,max}$	$T_c = 25\text{ °C}, t_p = 10\text{ }\mu\text{s}$		710	A
I^2t -value	$\int I^2 t$	$t_p = 10\text{ ms}$ ²⁾	$T_c = 25\text{ °C}$	58	A ² s
			$T_c = 150\text{ °C}$	41	
Diode dv/dt ruggedness	dv/dt	$V_R = 0...960\text{ V}$		150	V/ns
Avalanche energy, single pulse	E_{AS}	$I_D = 10\text{ A}, L = 0.28\text{ mH}$		14	mJ
Power dissipation for $R_{th(j-c,max)}$	P_{tot}		$T_c = 25\text{ °C}$	214	W
			$T_c = 100\text{ °C}$	107	

1) Not subject to production test. The test was performed with 20k pulses (half-wave rectified sine with 10 ms period).

2) sine halfwave

Table 3 Characteristic values

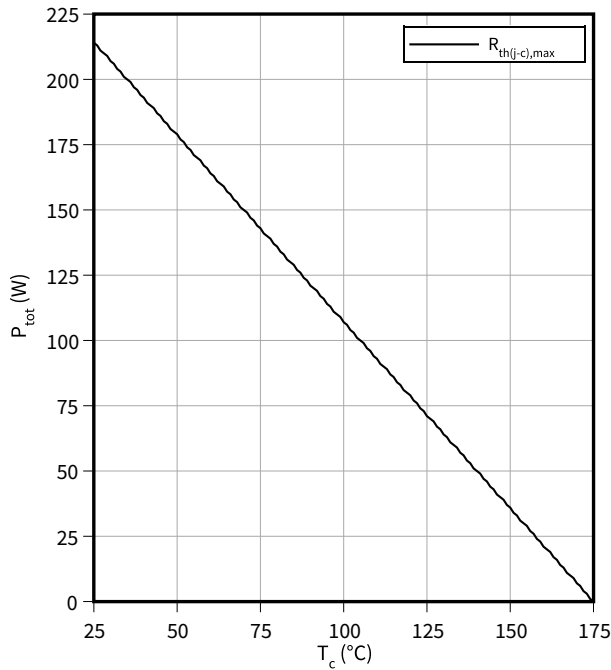
Parameter	Symbol	Note or test condition		Values			Unit
				Min.	Typ.	Max.	
DC blocking voltage	V_{DC}	$I_R = 500 \mu A$		1200			V
Diode forward voltage	V_F	$I_F = 10 A$	$T_{vj} = 25^\circ C$		1.45	1.8	V
			$T_{vj} = 125^\circ C$		1.69		
			$T_{vj} = 150^\circ C$		1.8		
Reverse current	I_R	$V_R = 1200 V$	$T_{vj} = 25^\circ C$		5	80	μA
			$T_{vj} = 150^\circ C$		25		
Total capacitive charge	Q_C	$V_R = 800 V, T_{vj} = 25^\circ C \text{ \& } 150^\circ C,$ $Q_C = \int_0^{V_R} C(V) dV$			52		nC
Total capacitance	C	$f = 100 \text{ kHz}$	$V_R = 1 V$		667		pF
			$V_R = 400 V$		47		
			$V_R = 800 V$		37		
Virtual junction temperature	T_{vj}			-55		175	$^\circ C$

Note: For optimum lifetime and reliability, Infineon recommends operating conditions that do not exceed 80% of the maximum ratings stated in this datasheet.
Characteristic at $T_{vj} = 25^\circ C$, unless otherwise specified.

3 Characteristics diagrams

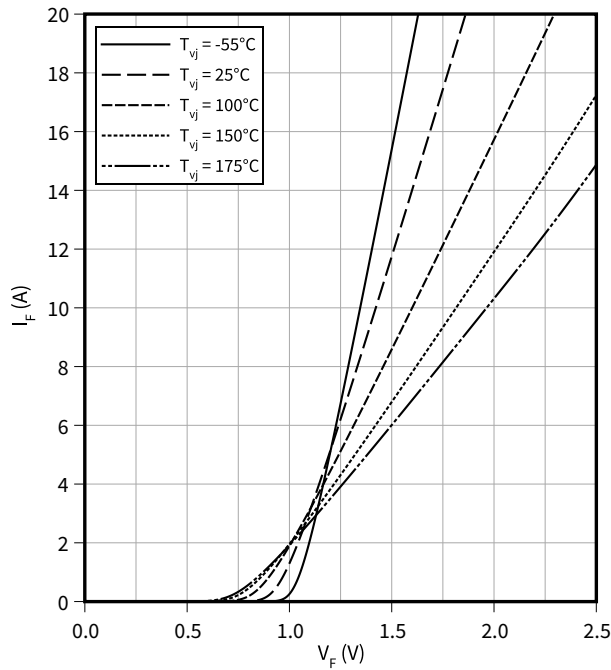
Power dissipation as function of case temperature

$P_{\text{tot}} = f(T_c)$
 $T_{vj} \leq 175\text{ °C}$



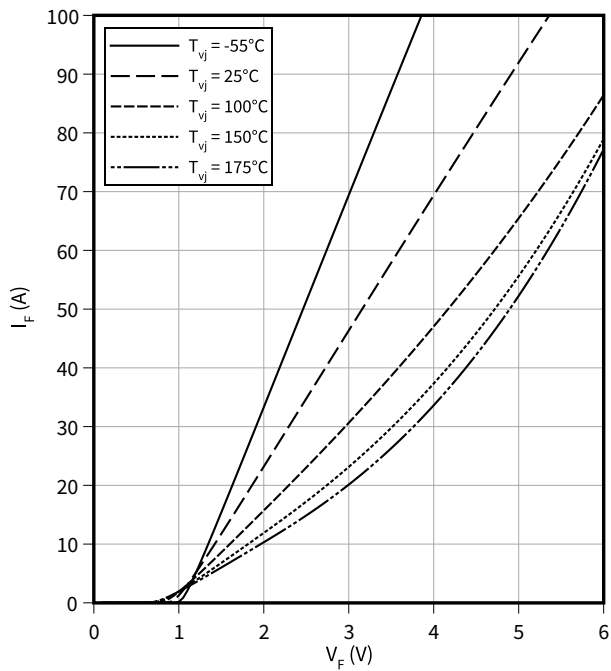
Typical forward characteristics

$I_F = f(V_F)$
 $t_p = 10\text{ }\mu\text{s}$



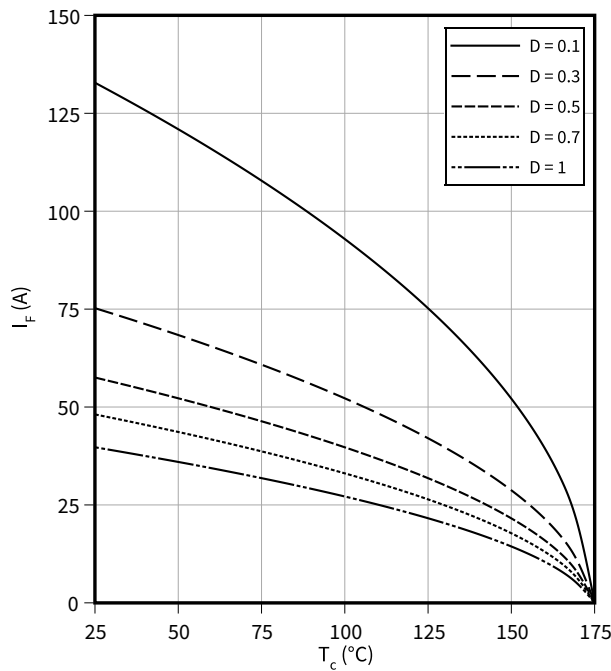
Typical forward characteristics in surge current

$I_F = f(V_F)$
 $t_p = 10\text{ }\mu\text{s}$



Diode forward current as function of temperature

$I_F = f(T_c)$
 $T_{vj} \leq 175\text{ °C}$

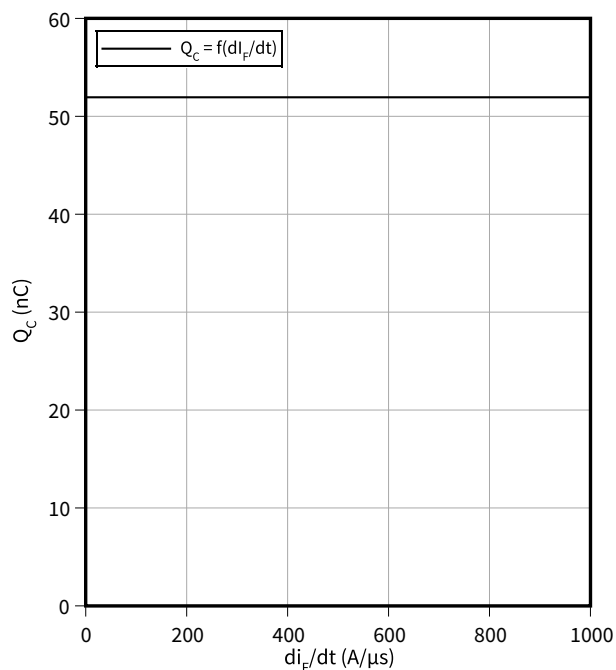


3 Characteristics diagrams

Typical capacitive charge as function of current slope

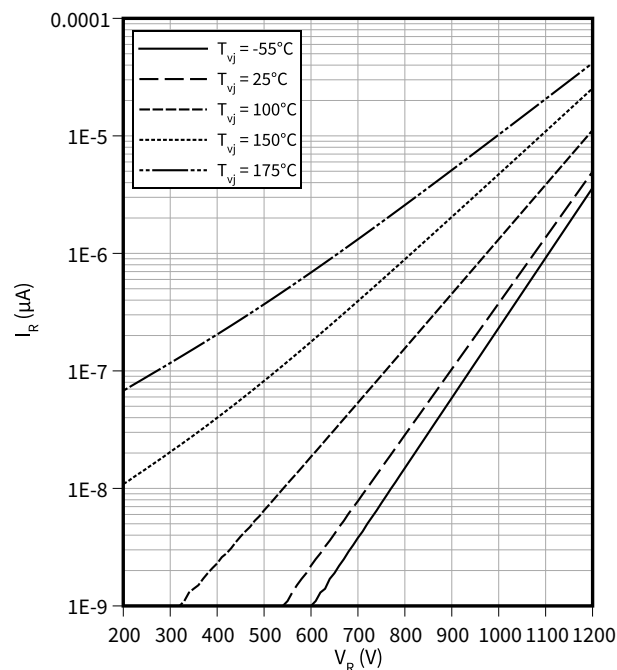
$$Q_C = f(di_F/dt)$$

$T_{vj} = 25\text{ °C}$, $V_R = 800\text{ V}$



Typical reverse characteristics

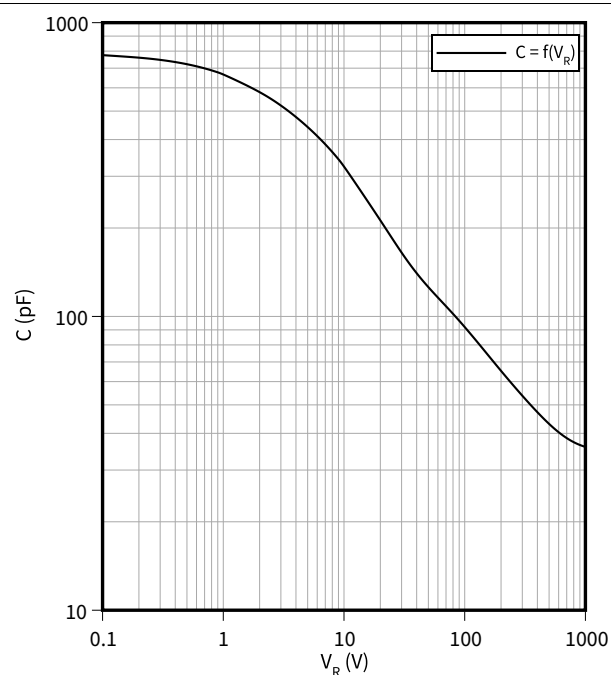
$$I_R = f(V_R)$$



Typical capacitance as function of reverse voltage

$$C = f(V_R)$$

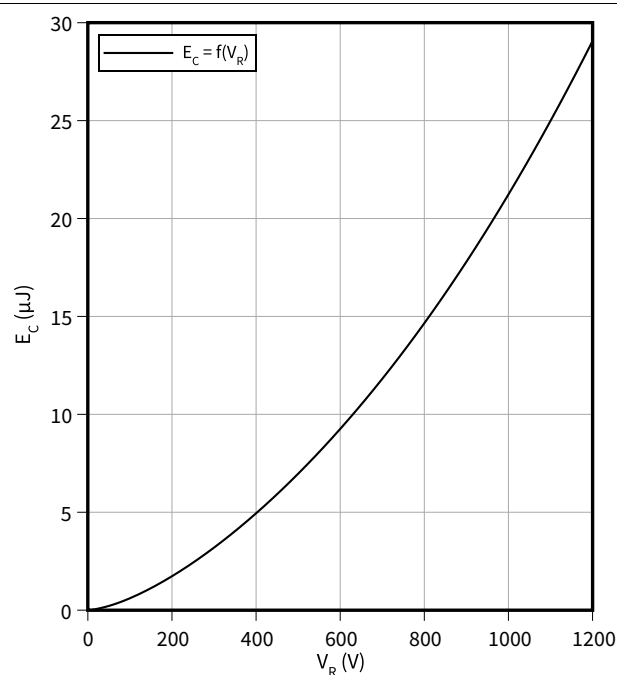
$T_{vj} = 25\text{ °C}$, $f = 100\text{ kHz}$



Typical capacitively stored energy as function of reverse voltage

$$E_C = f(V_R)$$

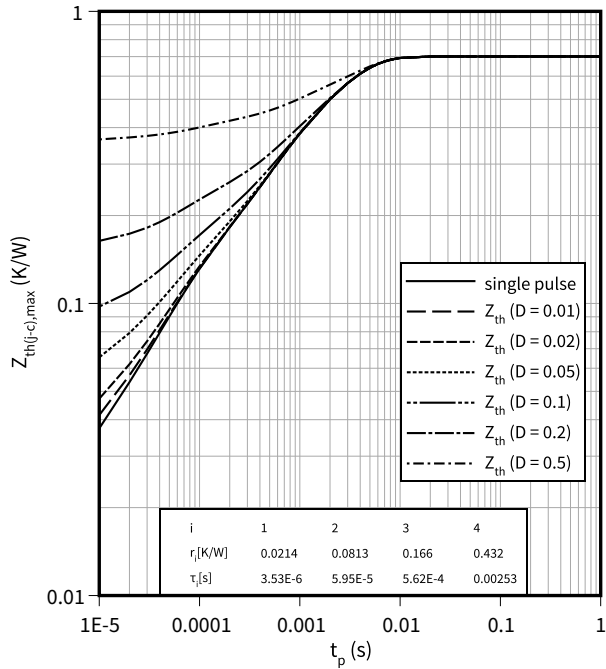
$f = 100\text{ kHz}$



3 Characteristics diagrams

Max. transient thermal impedance

$Z_{th(j-c),max} = f(t_p)$
 $D = t_p/T$



4 Package outlines

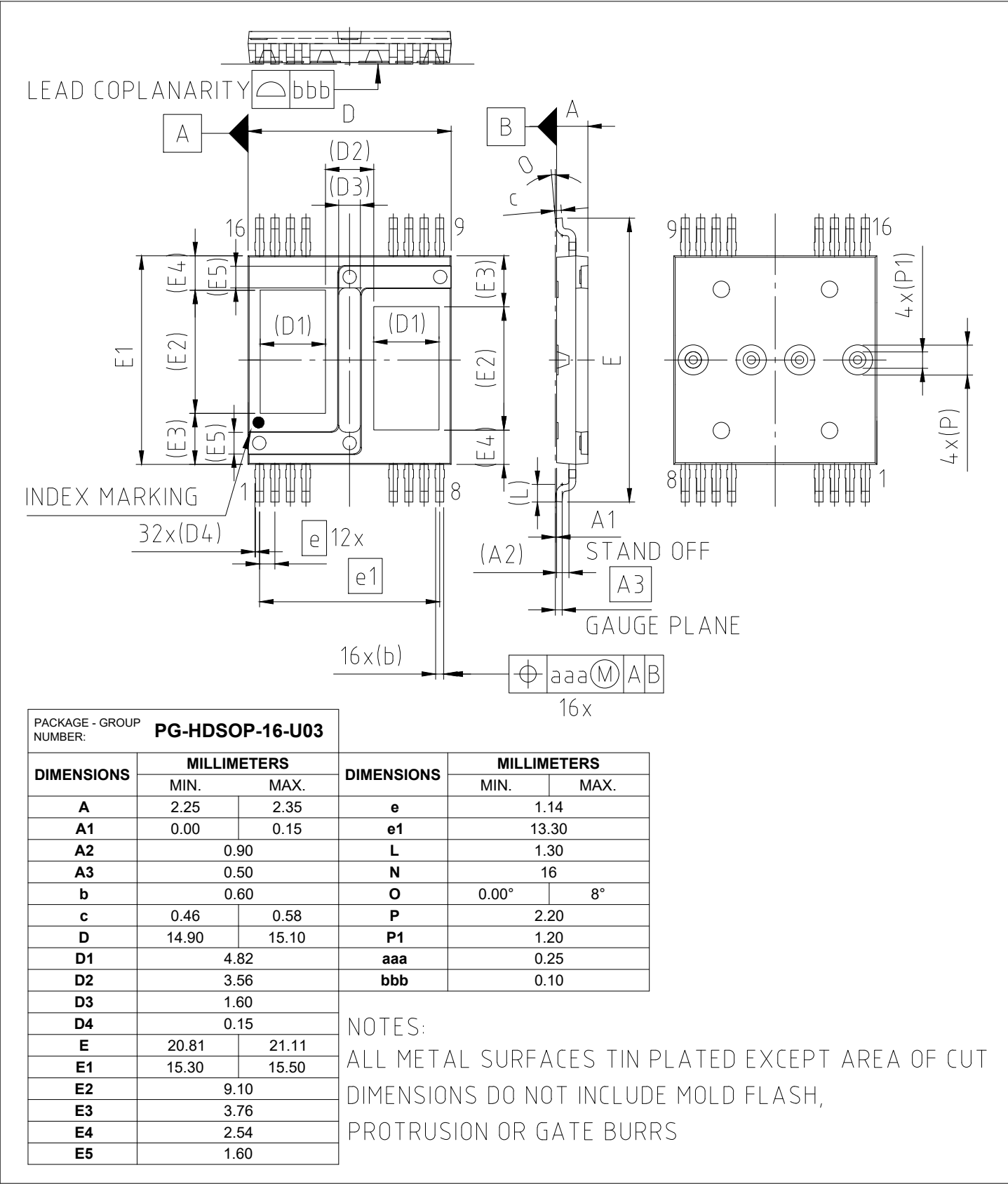


Figure 1



Revision history

Revision history

Document revision	Date of release	Description of changes
0.10	2024-01-24	Target datasheet
0.20	2024-11-14	Updated Avalanche energy, single pulse E_{AS} in Table 2 Editorial changes
0.30	2025-04-03	Preliminary datasheet
1.00	2026-06-25	Final datasheet

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