

How-to- access IPOSIM with SPICE



IPOSIM is getting SPICE **/**



Simulate now your system closer to real-world operating conditions with the New PLECS model generation out of SPICE directly in IPOSIM!



IPOSIM with SPICE now accounts for the complexities of your specific external circuitry setup and particular gate driver used, enabling you to simulate, test and optimize your system design with a higher degree of accuracy, thereby reducing development time and costs.

Main Features:

- Customize your own application environment setup, tailoring for deadtime, stray inductance and gate voltage.
- ✓ Design support with higher system accuracy
- Model generation taking into account your choice of gate driver.

More realistic operating conditions to support their selection decision



Higher system accuracy



Speed-up development



Reduce design iterations

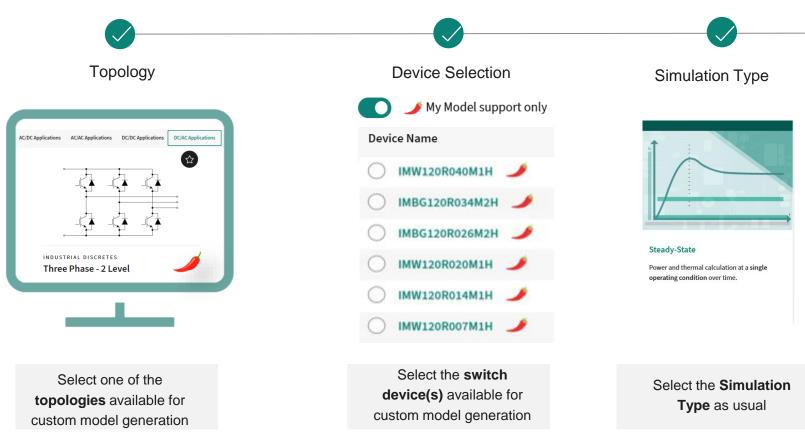


Generate your own custom PLECS model out of SPICE

How to find IPOSIM with SPICE?

Simply follow the / icon in IPOSIM to simulate with higher accuracy.





Note: you can select more than one set-up

Application Data

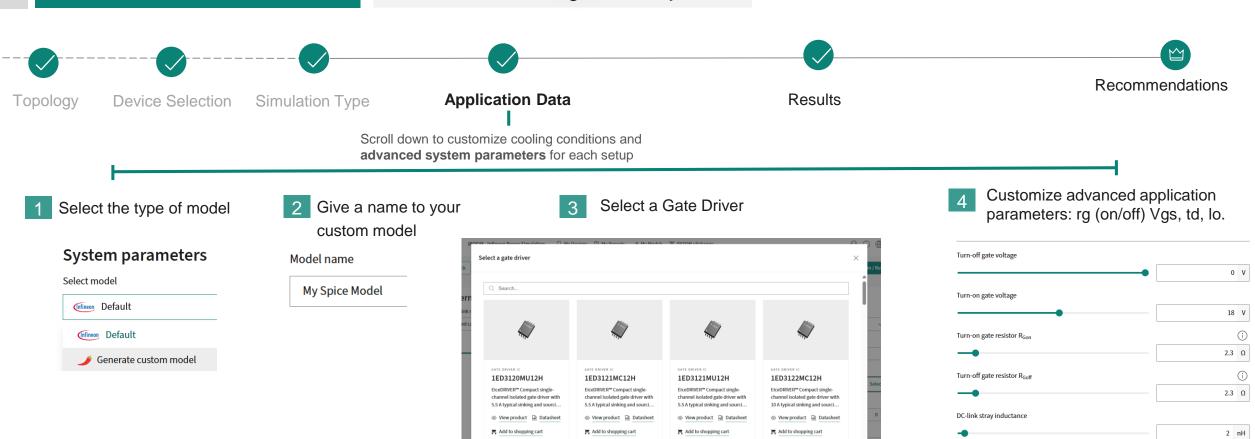
Results



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- Default: standard plecs xml model (based on datasheet)
- Custom model: plecs model generation (powered by SPICE)

50 ns

Dead time

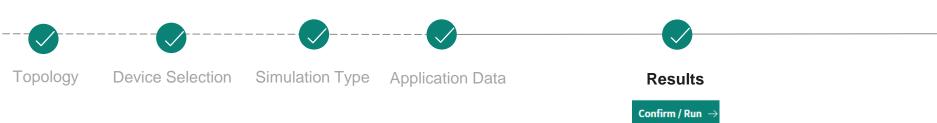


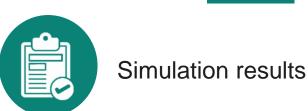
Recommendations

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