ModusToolbox™ run-time software release notes

ModusToolbox™ tools package version 3.9.0 or higher

About this document

Scope and purpose

With the ModusToolbox™ run-time software, you can rapidly develop applications for Infineon MCUs, as well as AIROC™ Wi-Fi and Bluetooth® devices.

This document lists the new and updated features included with this release of ModusToolbox™ run-time software. For details about these and other features and libraries, refer to the ModusToolbox™ run-time software reference guide.

Document conventions

Convention	Explanation
Bold	Emphasizes heading levels, column headings, menus and sub-menus
Italics	Denotes file names and paths
`Courier New`	Denotes APIs, functions, interrupt handlers, events, data types, error handlers, file/folder names, directories, command line inputs, and code snippets
File > New	Indicates that a cascading sub-menu opens when you select a menu item

Reference documents

Refer to the following links for more information:

- ModusToolbox™ run-time software reference guide
- ModusToolbox™ software page on GitHub
- Code examples on GitHub
- ModusToolbox™ community webpage

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1 What's new/changed in this release

The following features and libraries have been added/changed since the previous release:

1.1 Libraries

Library	Version
mtb-dsl-pse8xxgo	1.8.0
mtb-dsl-pse8xxgp	1.8.0
mtb-dsl-pse8x6	1.2.0
ifx-wolfcrypto	5.9.3
cy-mbedtls-acceleration	3.2.0
mtb-template-pse8xxgo	1.5.0
mtb-template-pse8xxgp	1.5.0
mtb-template-pse8x6	1.1.0
cmsis	6.1.1
retarget-io	1.11.0
ml-tflite-micro	3.5.0
ml-middleware	3.5.0
ml-executorch	1.2.0
ifx-tf-m	2.3.500
ifx-tf-m-ns	2.3.500
ifx-tf-m-port	2.3.500
ifx-tf-psa-crypto	1.2.100
ifx-tf-psa-port	1.2.100
wifi-connection-manager	4.3.0
secure-sockets	3.13.0
wifi-core-freertos-lwip-mbedtls	4.0.0
command-console	6.6.0
aws-iot-device-sdk-port	2.8.0
wpa3-external-supplicant	2.0.0
ethernet-core-freertos-lwip-mbedtls	4.0.0
websocket	1.1.0
mtb-tester-psoc-edge-wifi-bluetooth	1.3.0
mbedtls-pal	1.0.0
lwip-network-interface-integration	1.8.0
connectivity-utilities	4.6.0
lwip-freertos-integration	1.4.0
azure-c-sdk-port	1.7.0
http-server	3.4.0
ota-update	4.6.0
ota-bootloader-abstraction	1.9.0
virtual-connectivity-manager	1.3.0
low-power-assistant	5.9.0
audio-sw-codecs	1.0.4

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Library	Version
audio-voice-core	3.1.1
wifi-resources	3.0.6
bt-fw-qtl-cyw55512	1.0.0
display-tft-st7735s	1.0.0

1.2 Code examples

All code examples available can be viewed in the ModusToolbox™ Project Creator tool; refer to the user guide here.

1.3 Application Notes and KBAs

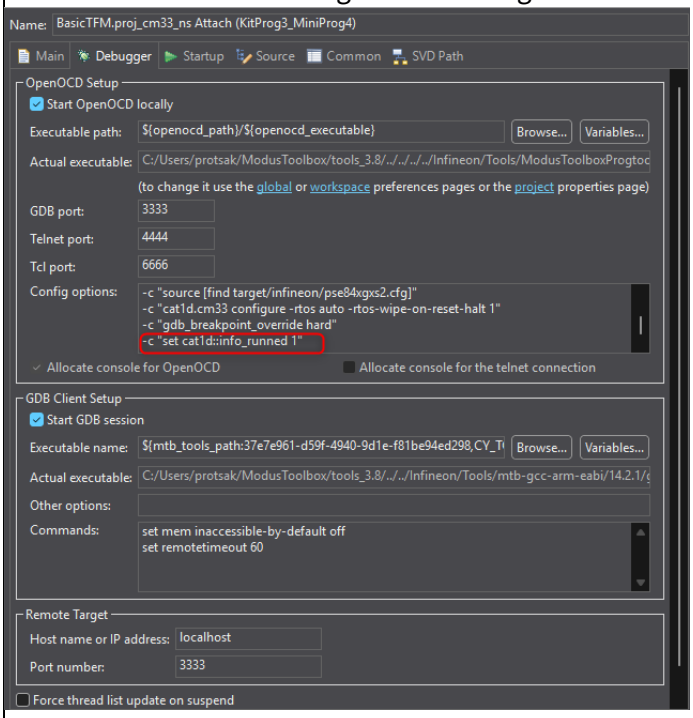
Application Note /KBA Name	Comments
AN243620 - Supporting a new flash device using QSPI Configurator tool in ModusToolbox™ for PSOC™ Edge MCUs	New
AN240096 - Getting started with Trusted Firmware-M (TF-M) on PSOC™ Edge	Updated
AN237857 - Edge Protect Bootloader for PSOC™ Edge MCU	Updated
AN237939 - Developing high-performance graphics with low-power optimization on PSOC™ Edge MCU	Updated

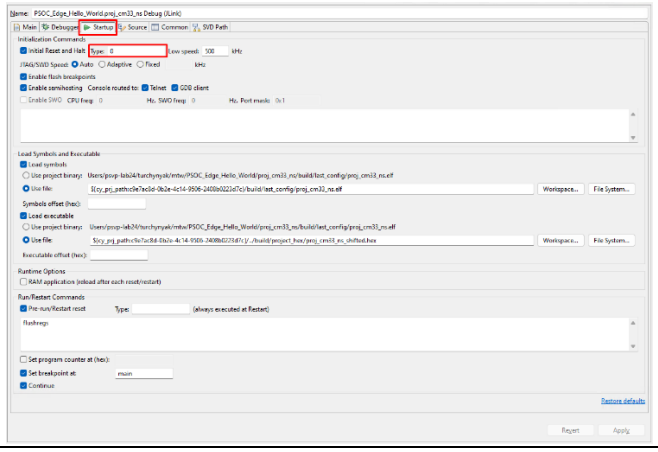
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2 Known issues

Problem	Workaround
The uninstaller for the Machine Learning pack does not uninstall on Mac devices.	All Machine Learning pack files need to be manually removed. The Machine Learning pack is located in two locations: /Applications/ModusToolbox-Machine-Learning-Pack and /Applications/ModusToolbox/packs/ModusToolbox-Machine-Learning-pack.
MCPS could be too high in LLVM Debug Mode.	Add CFLAGS+=-O1 in **project** Makefile
The mtb-ipc library only supports use with channels from IPC instance 1.	Do not use it with channels from IPC instance 0. IPC0 will be implemented in the near future.
There is a defect in the ModusToolbox™ build system that causes any change to any makefile in your application that affects the set of source files and include directories to be ignored.	Any time you make a change to a makefile, run make clean or use the IDE clean function. Then you can build and the changes will be included.
You may see the following error when running a debug session in Keil Uvision with J-Link probe: *g, main* *_{}^ *** error 123, line 2: AGDI: invalid operation*	Remove the "MVE" parameter for the SARMV8M.DLL driver using **Options > Debug** tab.
SE_RT_SERVICES: On EPC4 device the precision of temperature measurement during boot process in out of specification. Therefore, is possible that measured temperature is out of supported range (-40C - +125C). In this case, it will no possible to perform the following operations: - generate key - generate IV, nonce, random - data - sign hash or message - key derivation - generate attestation token.	Update SE_RT_SERVICES to version 1.1.0.3033 using Edge Protect Tools command: *edgeprotecttools -t pse8xs4 se-rt-services-update –image packets/apps/se_rt_services_update/se_rt_services_update-1.1.0.3033.img –key keys/oem_rot_private_key_0.pem*
Deepcraft does not open when sub-project contain multiple .deepcraft files	`DEEPCRAFT™ Studio will only generate the .deepcraft file if no such file already exists in the MTB project.`
Any code example that includes the TF-M library fails to build in Keil/EWARM when exported using the mtbideexport tool. As a result of this error, proj_cm33_s, proj_cm33_ns projects fail to build.	No workaround is available. This will be fixed in a future release of the tools.

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Problem	Workaround
The following warning message is present in the log: Warn: [cat1d.cm55] Failed to clear CPU_WAIT	It is expected while the debugger attempts to initialize the CM55 core at a time when CM33 already did this and changed the protection settings - debugger has no such knowledge.
The Die temperature measurement is wrong on trimmed Si (with LPPASS trim).	Initialize below INFRA trims with "0" value INFRA->AREF.VREF_TRIM0 = 0; INFRA->AREF.VREF_TRIM1 = 0;
The **"Attach cm33_ns"** and **"Attach cm33_s"** launch configurations in TF-M CE start from the **tfm_hal_system_reset** function.	For the "Attach cm33_s" or other secure app, this workaround can be applied, but it is not relevant: CM33 core already jumped into NS world, so you'll won't be able to attach to Secure app anyway, and will observe something like "Break at address "0x4058226" with no debug information available" For the "Attach cm33_ns" configuration would be skipping the system information output into the debug console. This can be achieved by adding *-c "set cat1d::info_runned 1"* To the "***Debugger -> OpenOCD Setup -> Config options***" control in the launch configuration settings: 

Problem	Workaround
The Eclipse launch configurations were using an incorrect reset strategy (#2), which is not supported by SEGGER J-Link for PSOC Edge devices. As a result, the debugger failed to halt before entering the CM33_S application.	Change the reset strategy value to **#0** (default) or simply **remove** the parameter on the **Startup** page, as shown in the image below. 

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