POWER CIRCUIT

NOTE: 5V Adapter input

J6 DC JACK

S_VCC, SELF_PWR, C20 0.1uF, D1 BZX84C5V6/SOT, Q2 S4SCT, R31 1k, Si1433dh Q4, EC7 4.3uF, Q3 FDN338P, R30 4.7K, R29 510K, VBUS

U5 AME8805-SOT89 5V to 3.3V: VCC_5V_REG, C9 1uF, C10 1uF, VCC_3.3

SW1 DPDT: VCC_5V_IN, VCC_5V_REG, VCC_3.3, VCC_5V, VCC_3.3_IN

U3 AIC1526-0: R4 10K, R5 100K, R6 100K, R7 10K, nENP1, nOVRP1, nOVRP2, C19 0.1u, PWR1, PWR2

U4 AIC1526-0: R8 10K, R9 100K, R10 100K, R11 10K, nENP1, nOVRP3, nOVRP4, C18 0.1u, PWR3, PWR4

F1 6V500mA, R19 30K, R20 30K

F2 6V500mA, R21 30K, R22 30K

F3 6V500mA, R23 30K, R24 30K

F4 6V500mA, R25 30K, R26 30K

JP1 Header 3x2: VCC_D, TEST, GANG, VCC_5V, VBUS, GND

Individual Mode: GANG, LED6 RED BUS_LED, R27 100K, R28 680R

LED2 GREEN_LED1, LED5 GREEN_LED4, LED3 GREEN_LED2, LED4 GREEN_LED3, R18 470R TEST, R17 470R nENP1

PORT 1 — J7 USB TYPE A, PART_NUMBER = 73725-0110BLF, Manufacturer = FCI

PORT 2 — J10 USB TYPE A, PART_NUMBER = 73725-0110BLF, Manufacturer = FCI

PORT 3 — J9 USB TYPE A, PART_NUMBER = 73725-0110BLF, Manufacturer = FCI

PORT 4 — J8 USB TYPE A, PART_NUMBER = 73725-0110BLF, Manufacturer = FCI

FB LB2: PWR1_C, C14 0.1uF, EC3 120uF, PWR1

FB LB5: PWR4_C, C17 0.1uF, EC6 120uF, PWR4

FB LB4: PWR3_C, C16 0.1uF, EC5 120uF, PWR3

FB LB3: PWR2_C, C15 0.1uF, EC4 120uF, PWR2

USB Upstream Port

J11 USBB-A: HAND1, HAND2, G1, G2, VCC, D-, D+, GND, VBUS, FB LB7, DM0, DP0, PGB1010603MR ESD1, PGB1010603MR ESD2, C21 10nF 16V 0402

USB Downstream Ports

PORT 1: DM1, DP1, PGB1010603MR ESD3, PGB1010603MR ESD4

PORT 2: DM2, DP2, PGB1010603MR ESD9, PGB1010603MR ESD10

PORT 3: DM3, DP3, PGB1010603MR ESD7, PGB1010603MR ESD8

PORT 4: DM4, DP4, PGB1010603MR ESD5, PGB1010603MR ESD6

J5 I2C_EEPROM_PWR, U2 24LC02B/P, C7 0.1uF, R2 10K, TEST, nENP1

Note: U2 should be place on socket

C11 20P, X1 12MHz, C12 20P, XIN, XOUT

Close to CY7C65632

VBUS, R1 10K, RESET#, C13 1uF, R16 47K, R3 650R 1%, RREF

U6 CY7C65642 QFN28: DM0, DP0, DM1, DP1, VCC_A1, DM2, DP2, DD-0, DD+0, DD-1, DD+1, VCC_A_5, DD-2, DD+2, VREG, VCC, I2C_SDA, OVR#[1], OVR#[2], GANG, SELFPWR, VCC_D, OVR#[3], OVR#[4], TEST, RESET#, DD+4, DD-4, VCC_A_14, GND, DP4, DM4, nOVRP3, nOVRP4, RREF, VCC_A_9, XIN, XOUT, DD-3, DD+3, VCC_A14, VCC_3.3_IN, VCC_5V_IN, nENP1, nOVRP1, nOVRP2, GANG, SELF_PWR

LB8 FB, C22 0.1uF

EC1 close to Pin 28

VCC_D, VCC_A, LB6 FB, EC1 10uF, C1 0.1uF

VCC_A, C2 0.1uF, C3 0.1uF, C4 1uF, C5 1uF, C6 0.1uF

Close to CY7C65632

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Date: Friday, April 27, 2012	Sheet 1 of 1	