

MOD\_PSE84\_SOM PSOC™ Edge E84 SoM

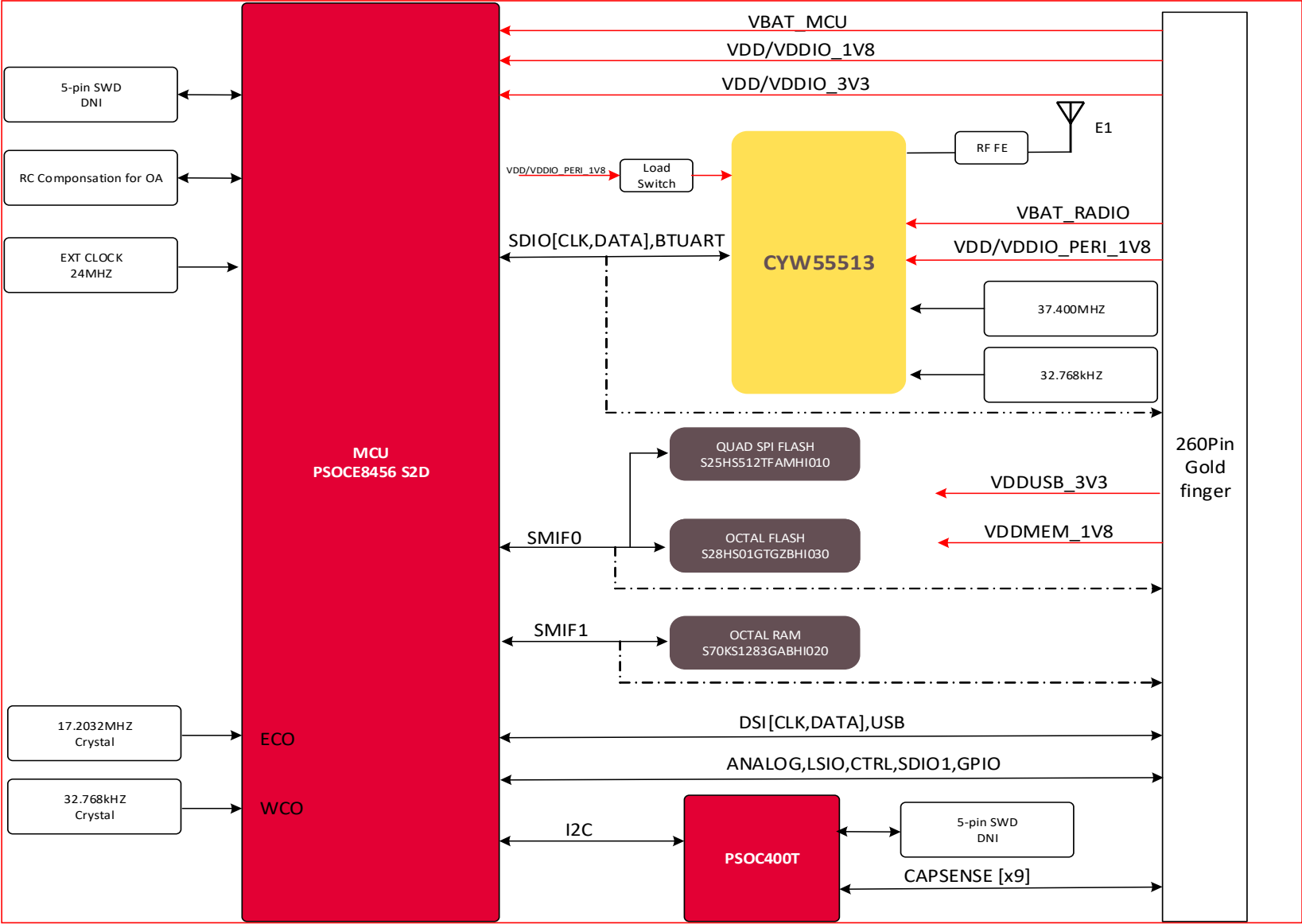
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Drawing Numbers	
PCBA	121-60673-01
PCB	600-60673-01
FAB DRW	610-60673-01
ASSY DRW	620-60673-01
SCH DRW	630-60673-01

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Page Title: Title Page				
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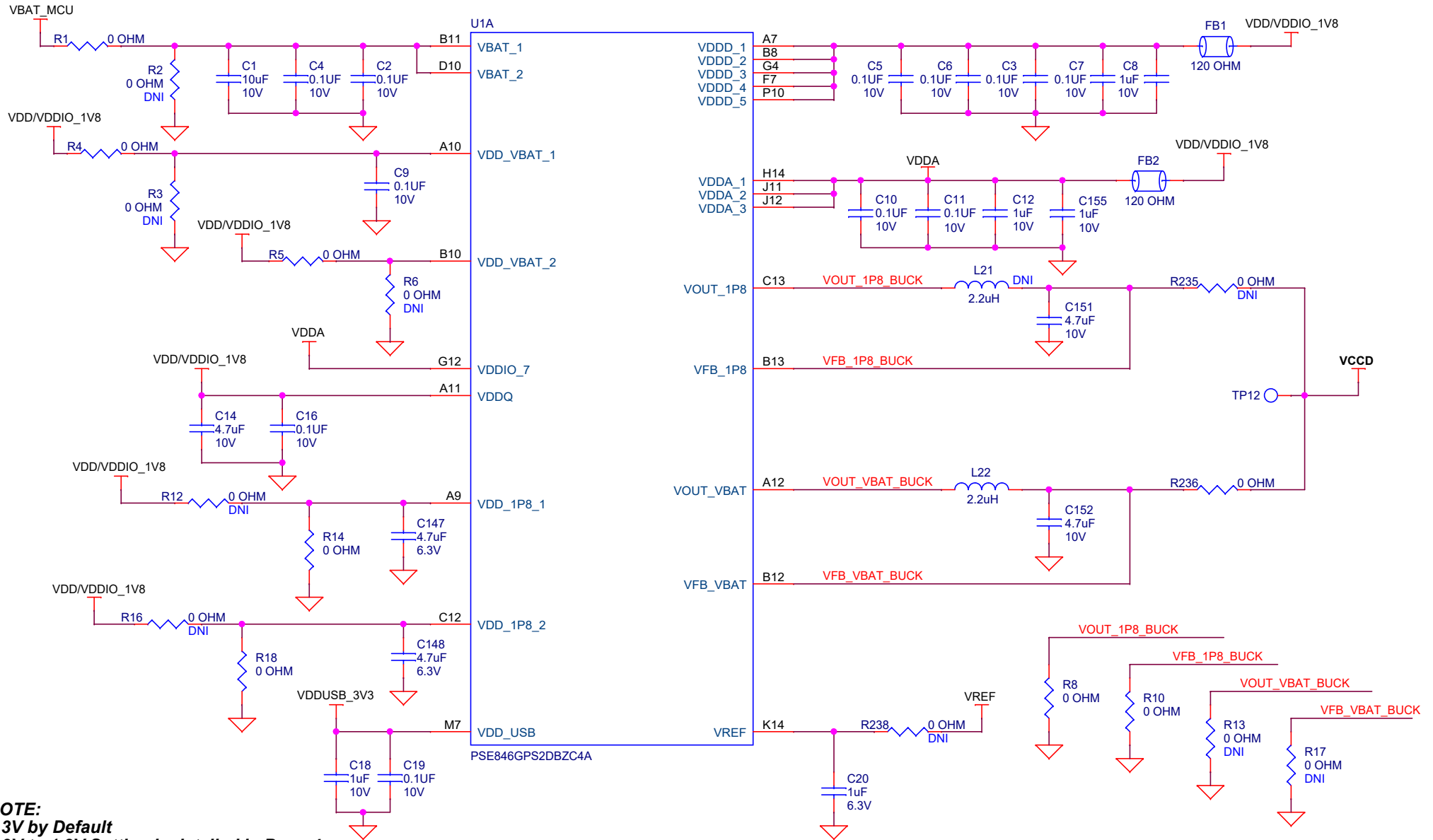


**SCH Title : PSOC™ Edge E84 SoM**

**Page Title: Block Diagram**

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# PSOC™ Edge E84 Power (1)



**NOTE:**  
 3.3V by Default  
 3.3V to 1.8V Setting is detailed in Page-4

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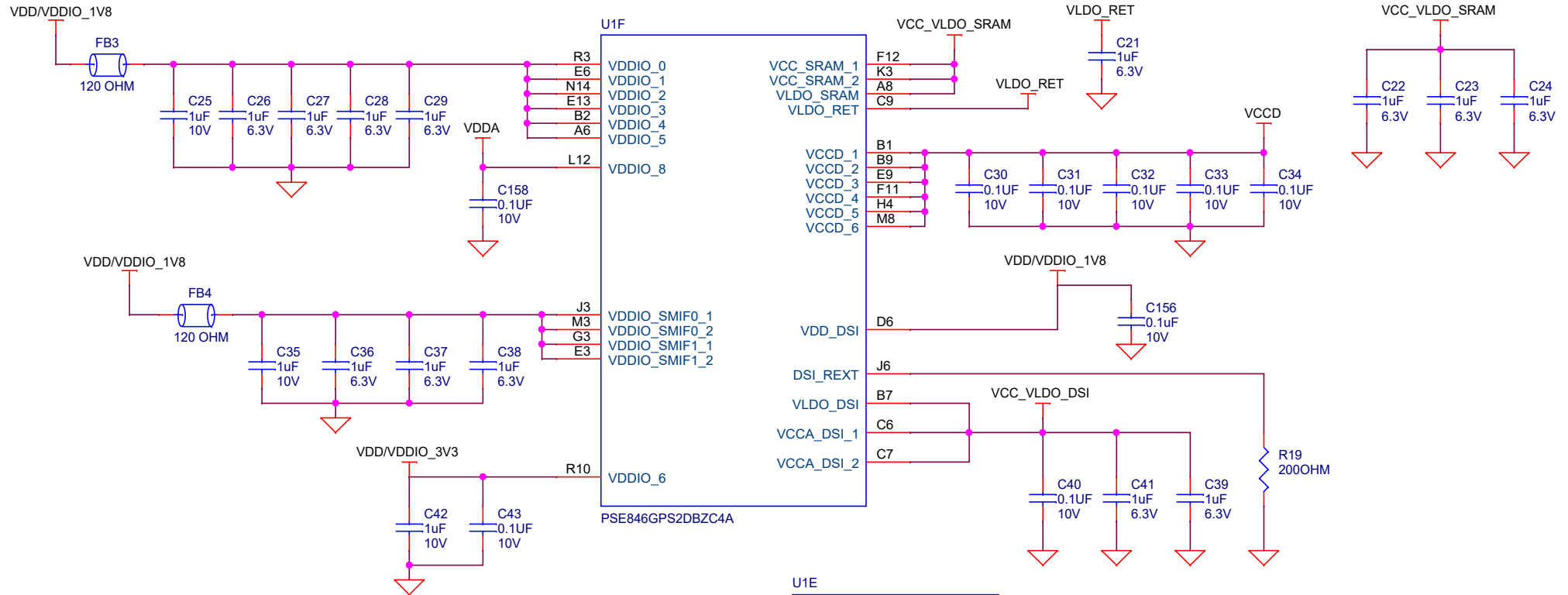


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**Page Title : MCU Power Selection\_1**

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## PSOC™ Edge E84 Power (2)



### 3.3V to 1.8V Setting

1.8V	
STUFF	R2, R3, R6, R12, R13, R16, R17, R235, L21
DNI	R1, R4, R5, R8, R10, R14, R18, R236, L22
3.3V	
STUFF	R1, R4, R5, R8, R10, R14, R18, R236, L22
DNI	R2, R3, R6, R12, R13, R16, R17, R235, L21

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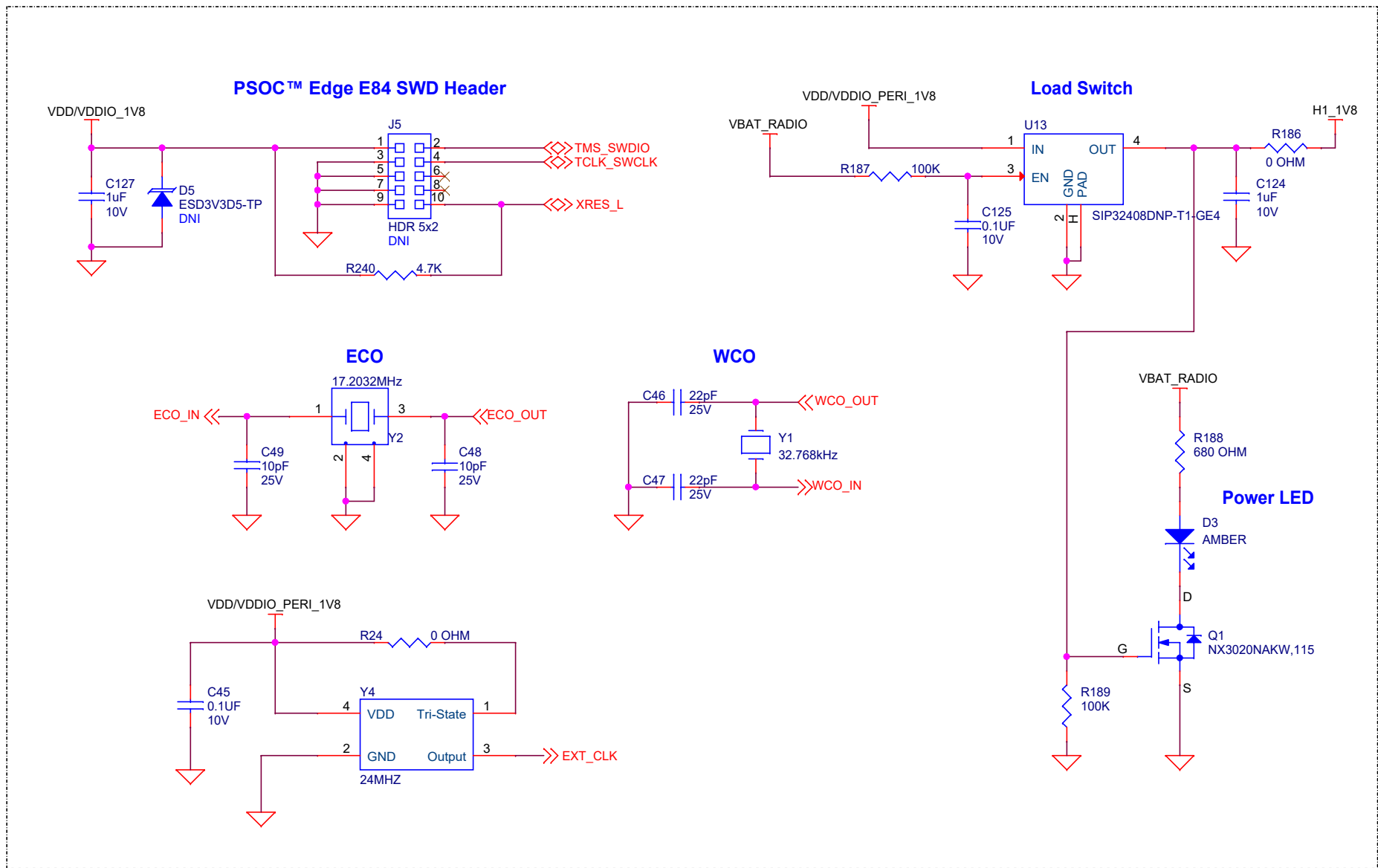
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Page Title : MCU Power selection\_2

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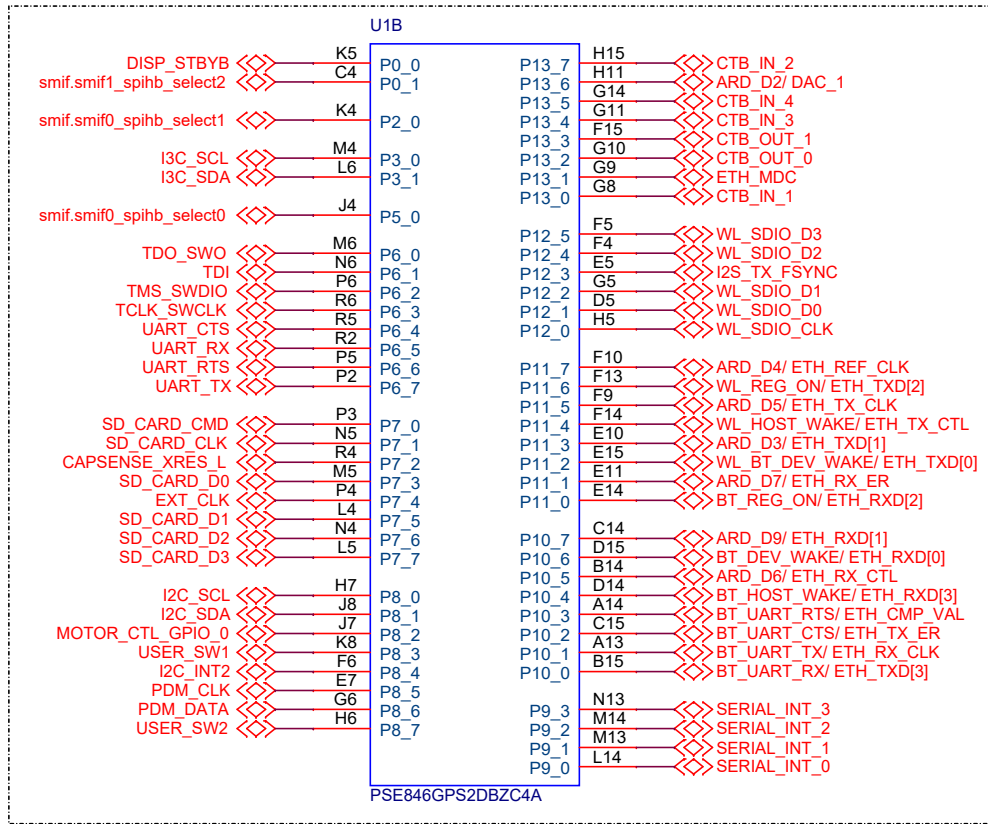


**SCH Title : PSOC™ Edge E84 SoM**

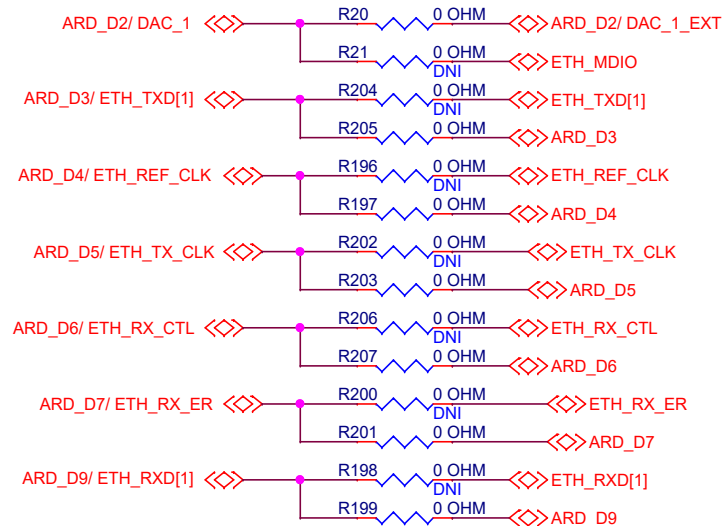
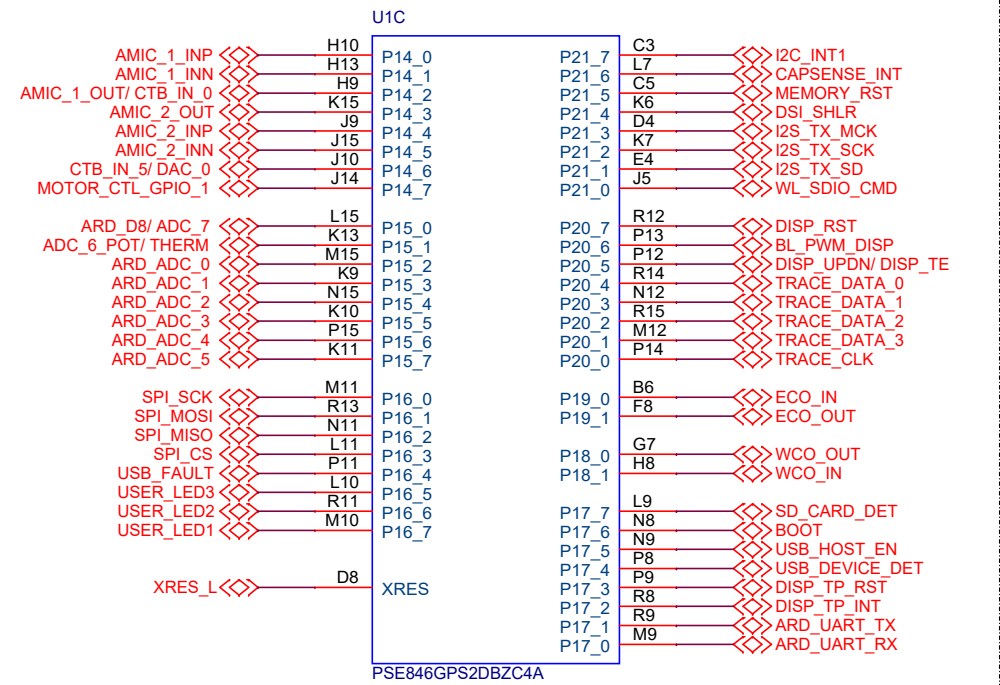
**Page Title: MCU Miscellaneous**

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# PSOC™ Edge E84 GPIO mapping (1)



I2C\_INT2 <<> R23 0 OHM <<> I2C\_INT2\_EXT



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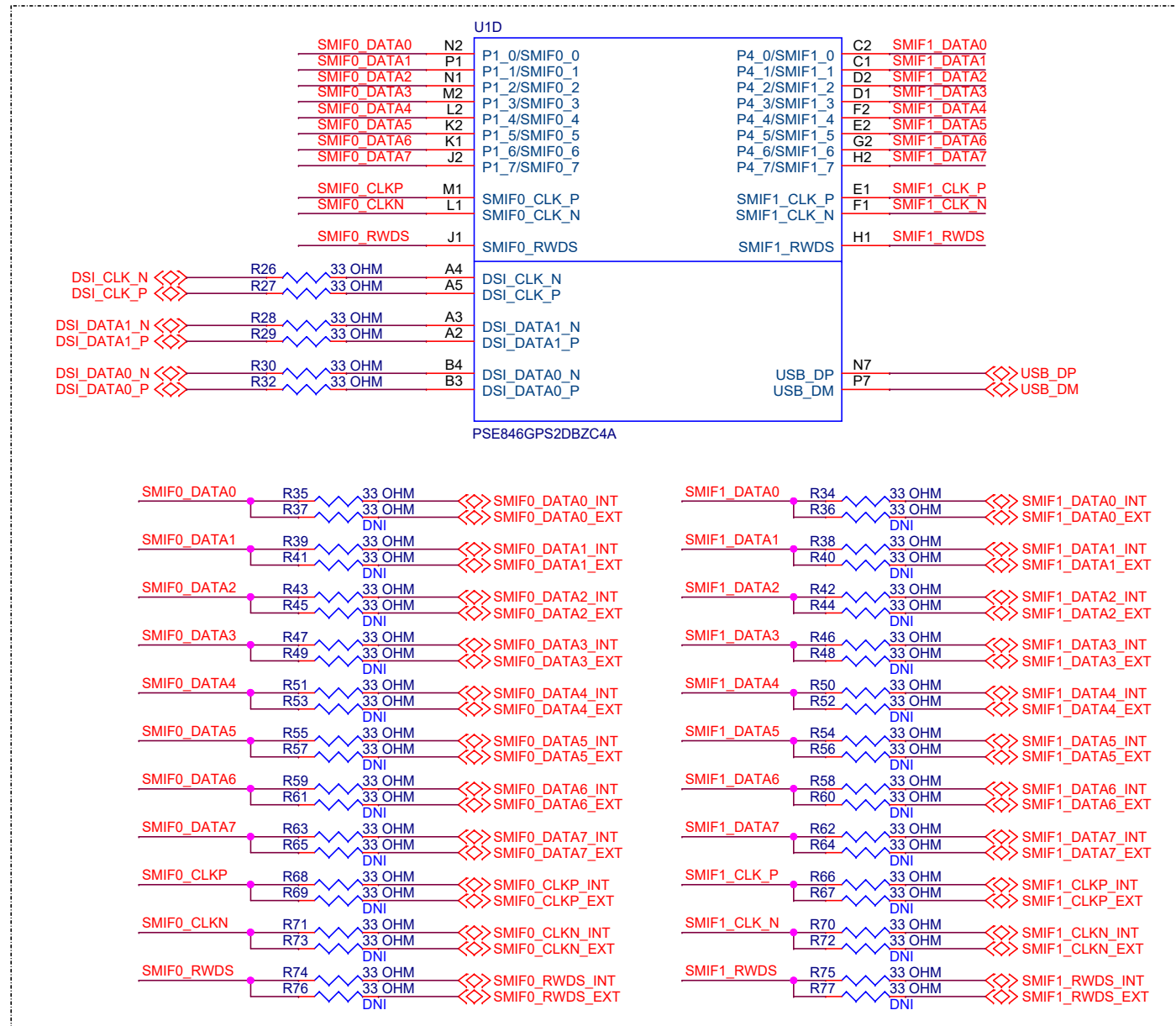


SCH Title : PSOC™ Edge E84 SoM

Page Title: MCU Signals\_1

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# PSOC™ Edge E84 GPIO mapping (2)



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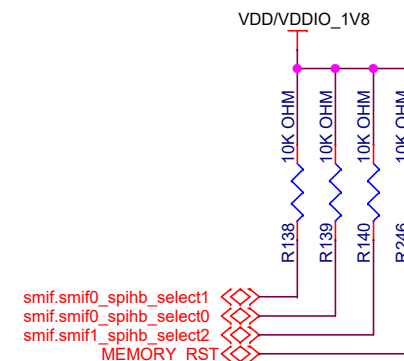
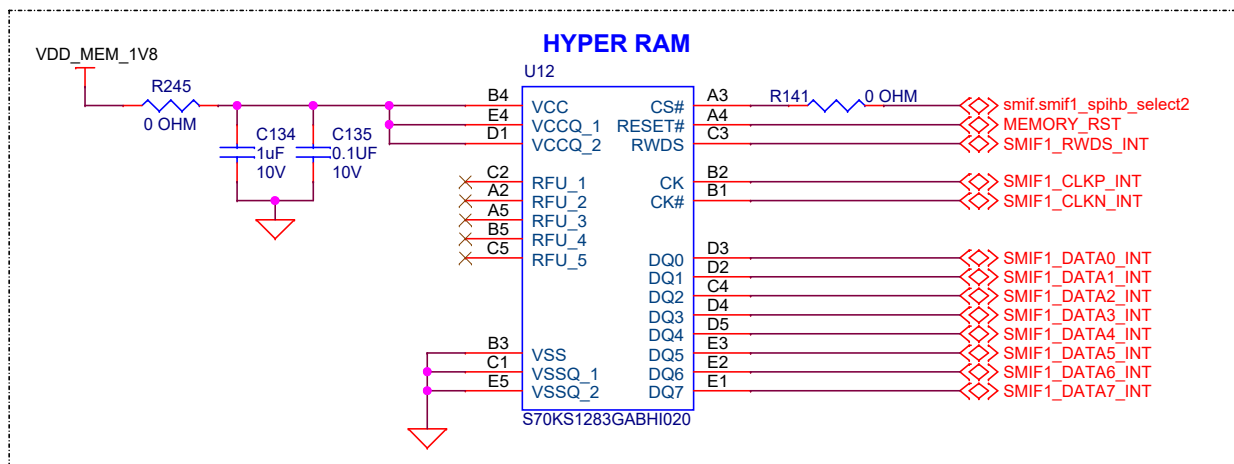
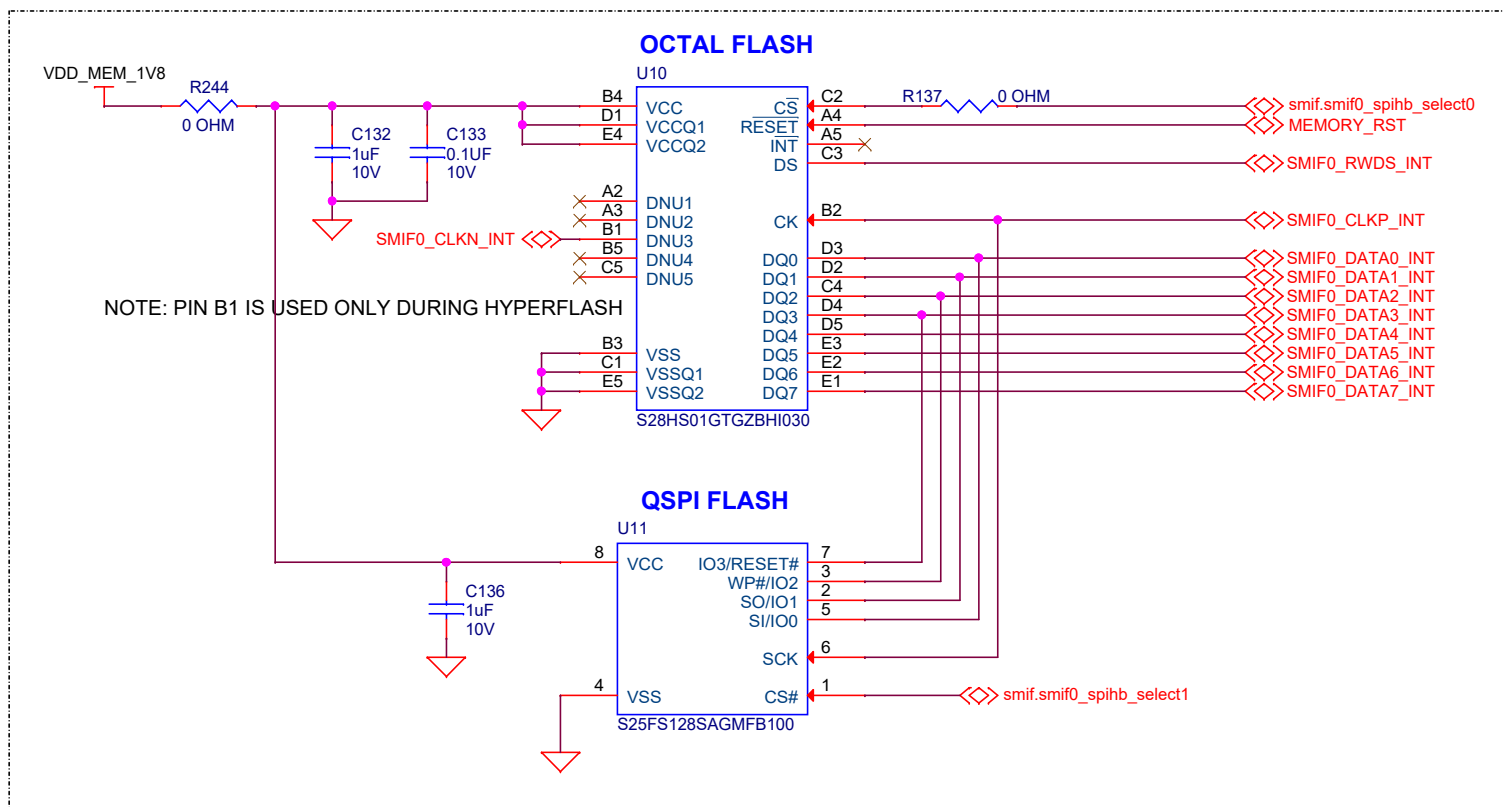
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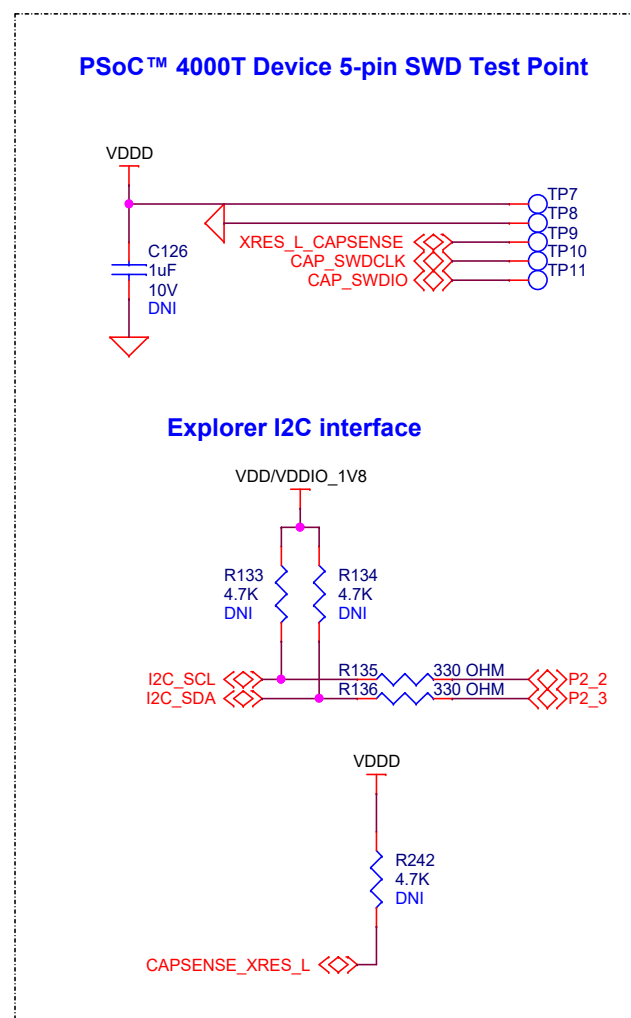
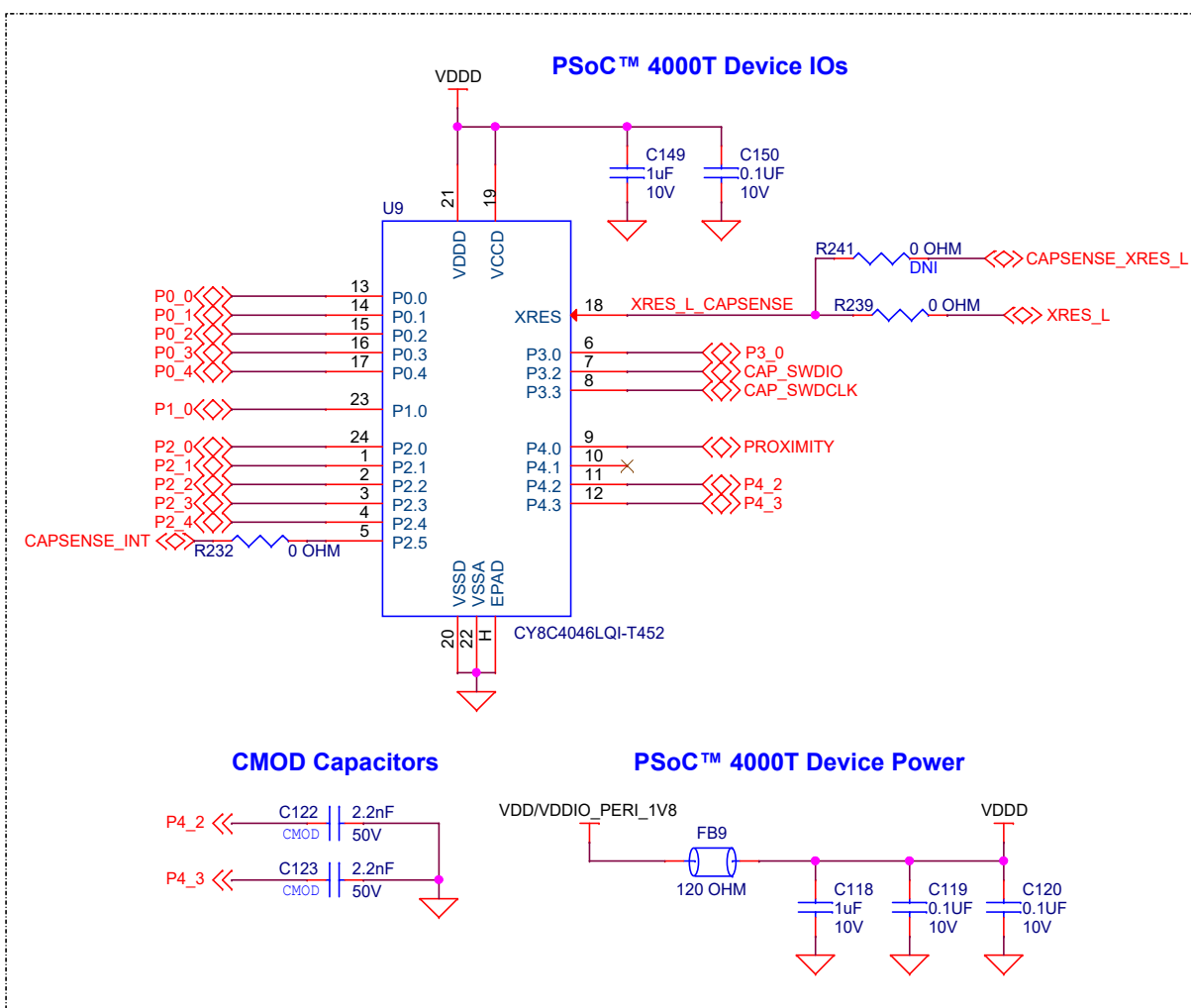


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**Page Title : SMIF Interface**

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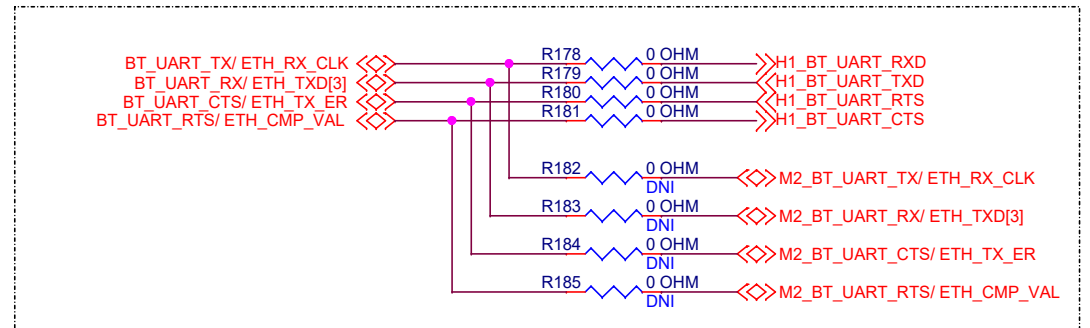
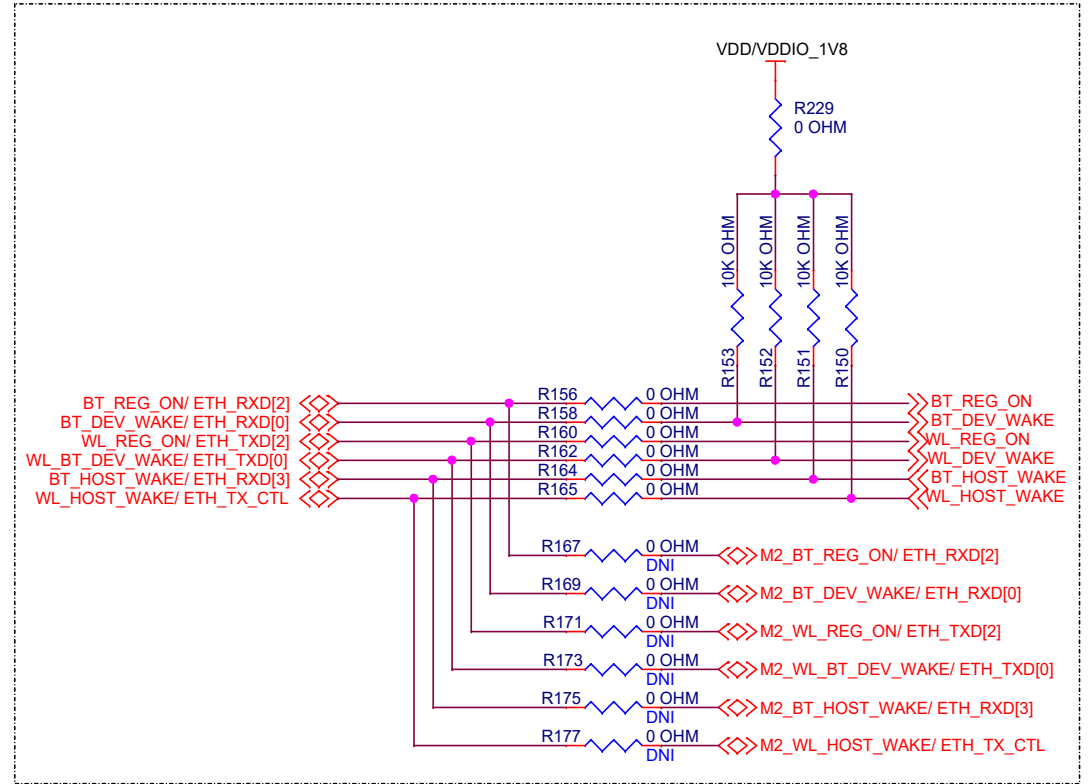
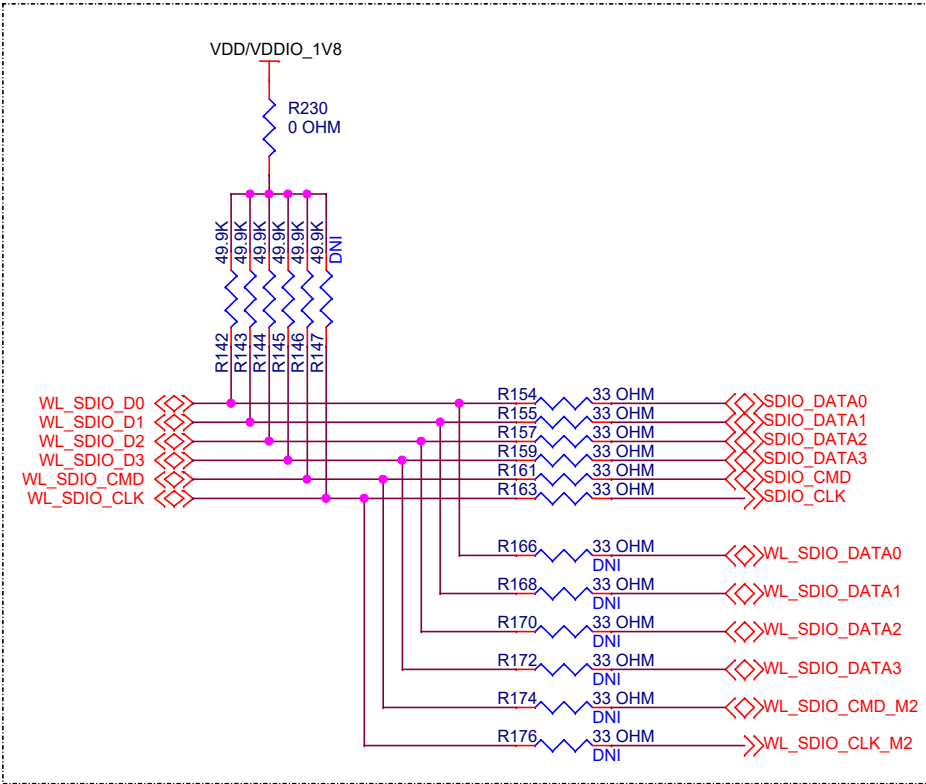


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SCH Title : PSoC™ Edge E84 SoM				
Page Title: Capsense Coprocessor				
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## WiFi/BT multiplexing



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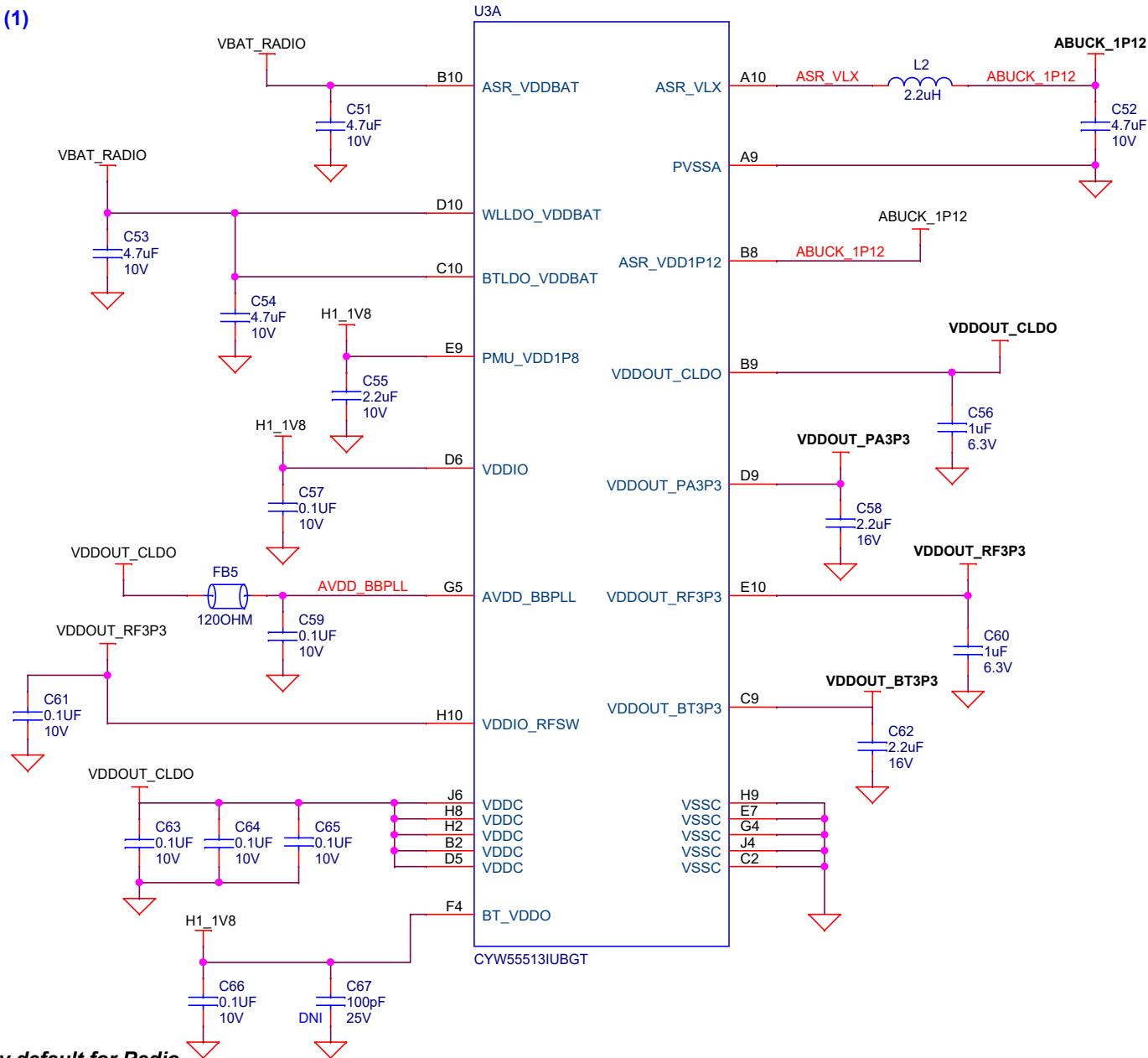
Page Title: Tripad and RC for OA

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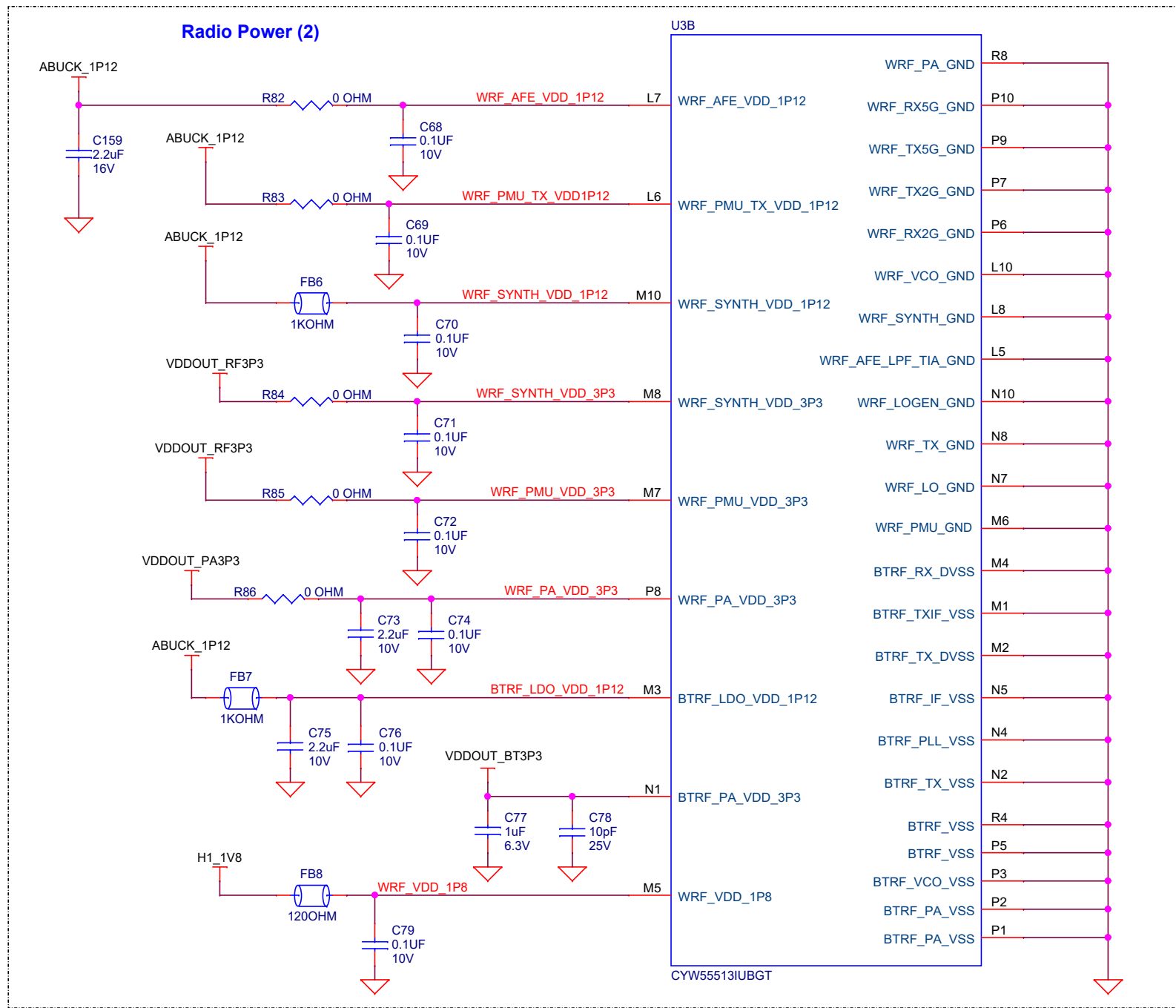
**NOTE:**  
V<sub>BAT</sub> is 3.3V by default for Radio



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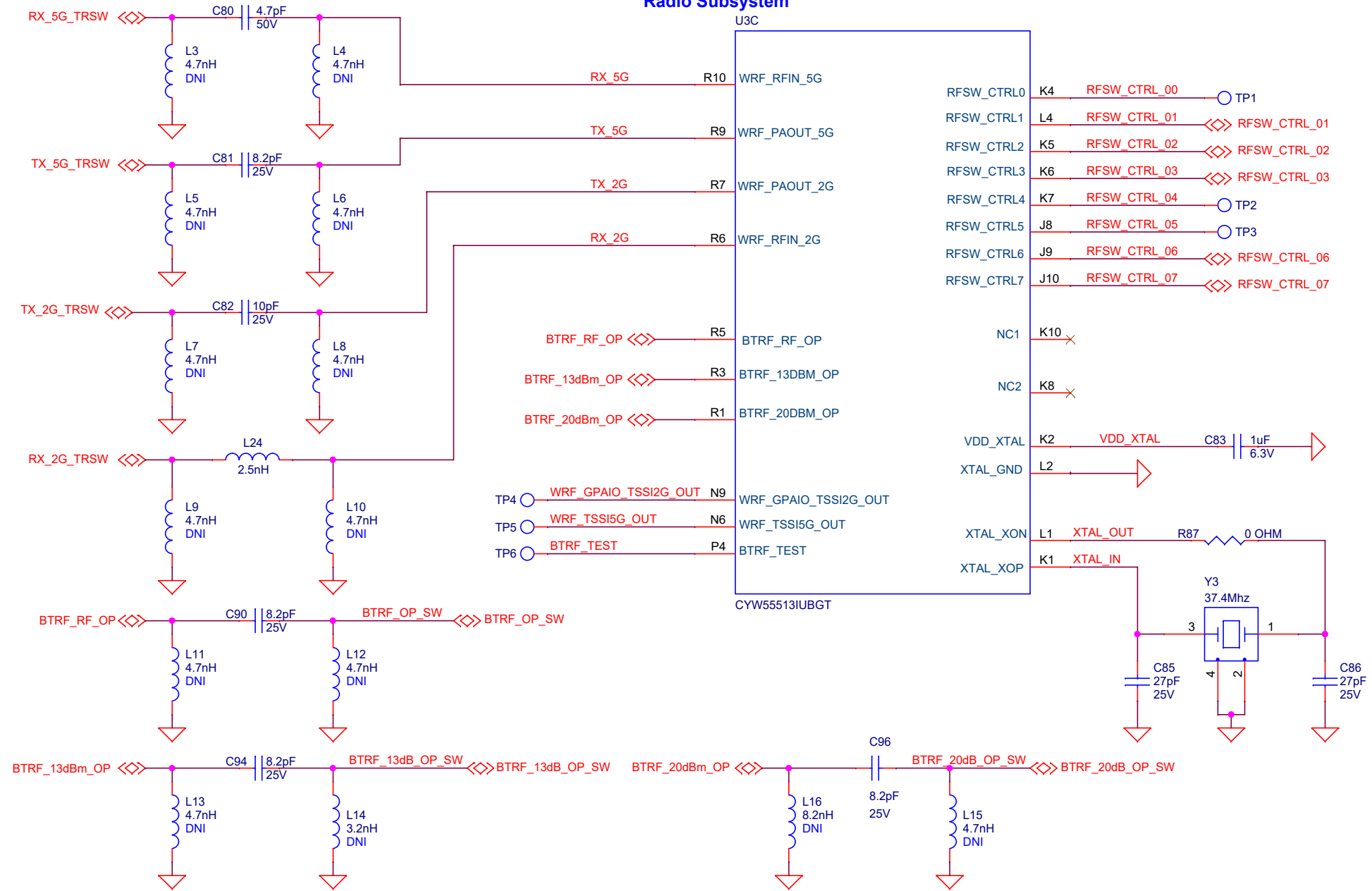
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**Page Title: Radio PMU Topology\_2**

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# Radio Subsystem



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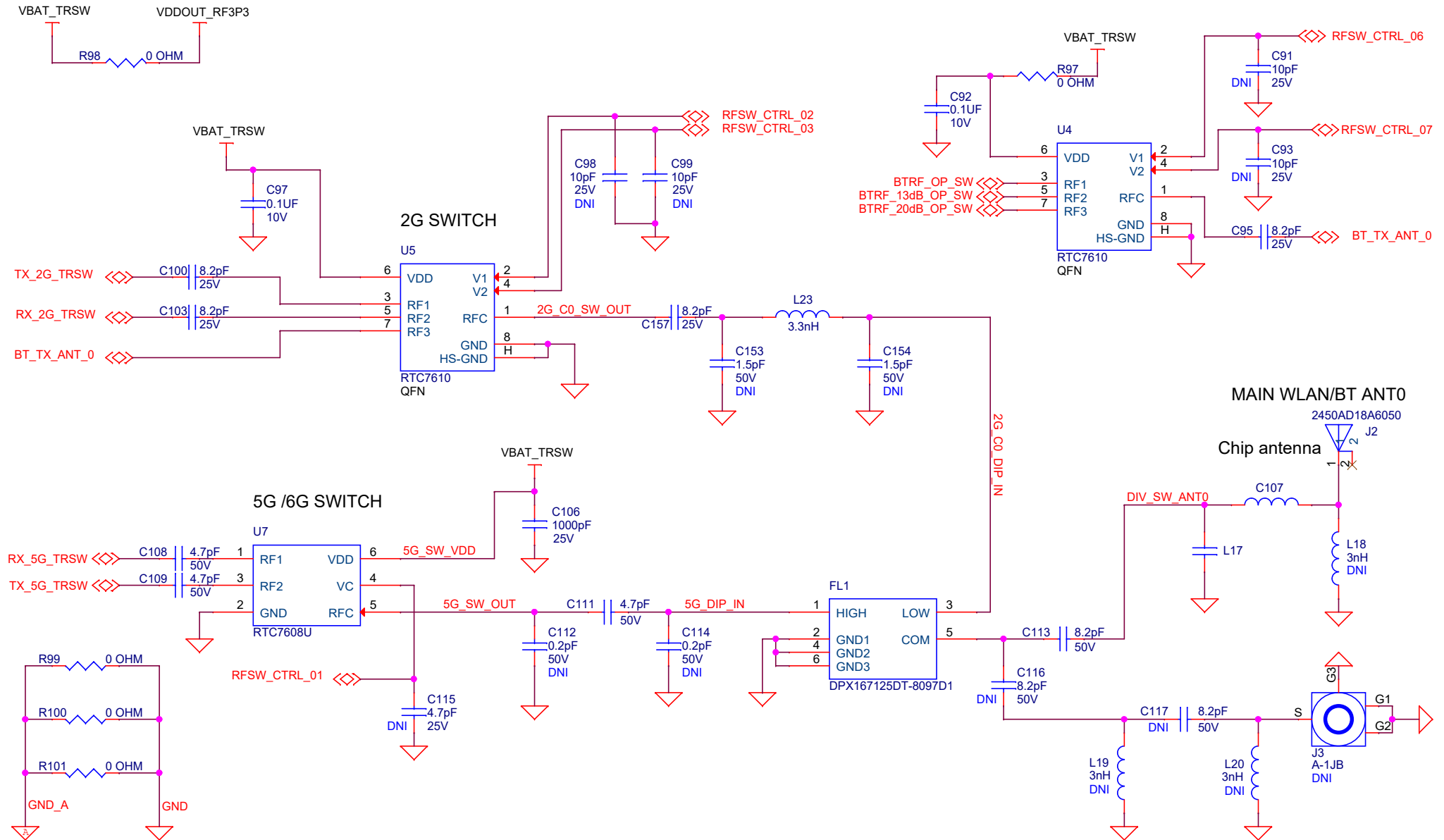


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**Page Title : Radio RF Section**

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## Radio RF Front end



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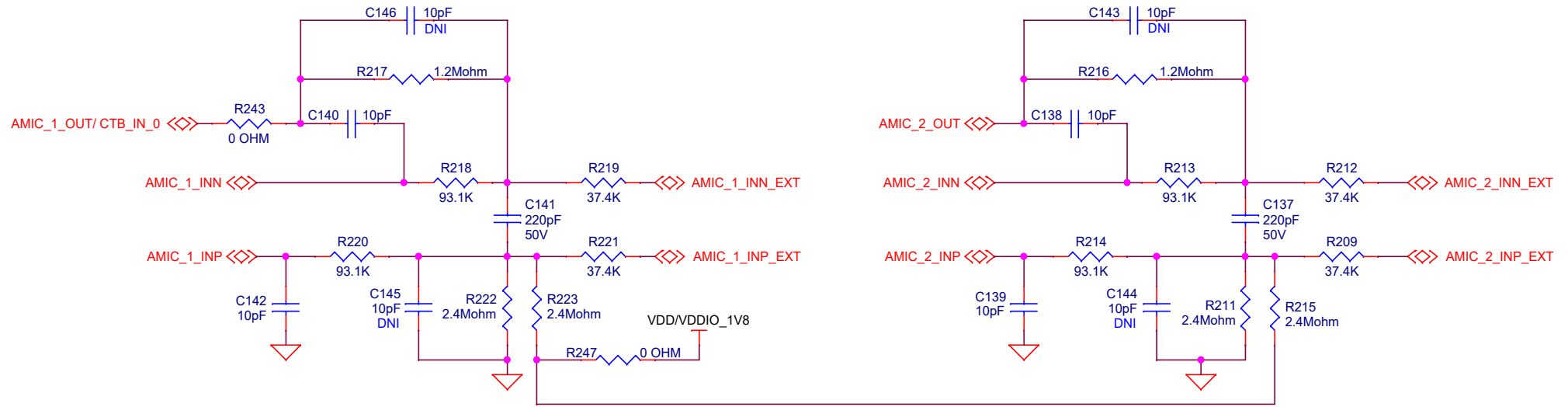


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## AMIC Signal Processing



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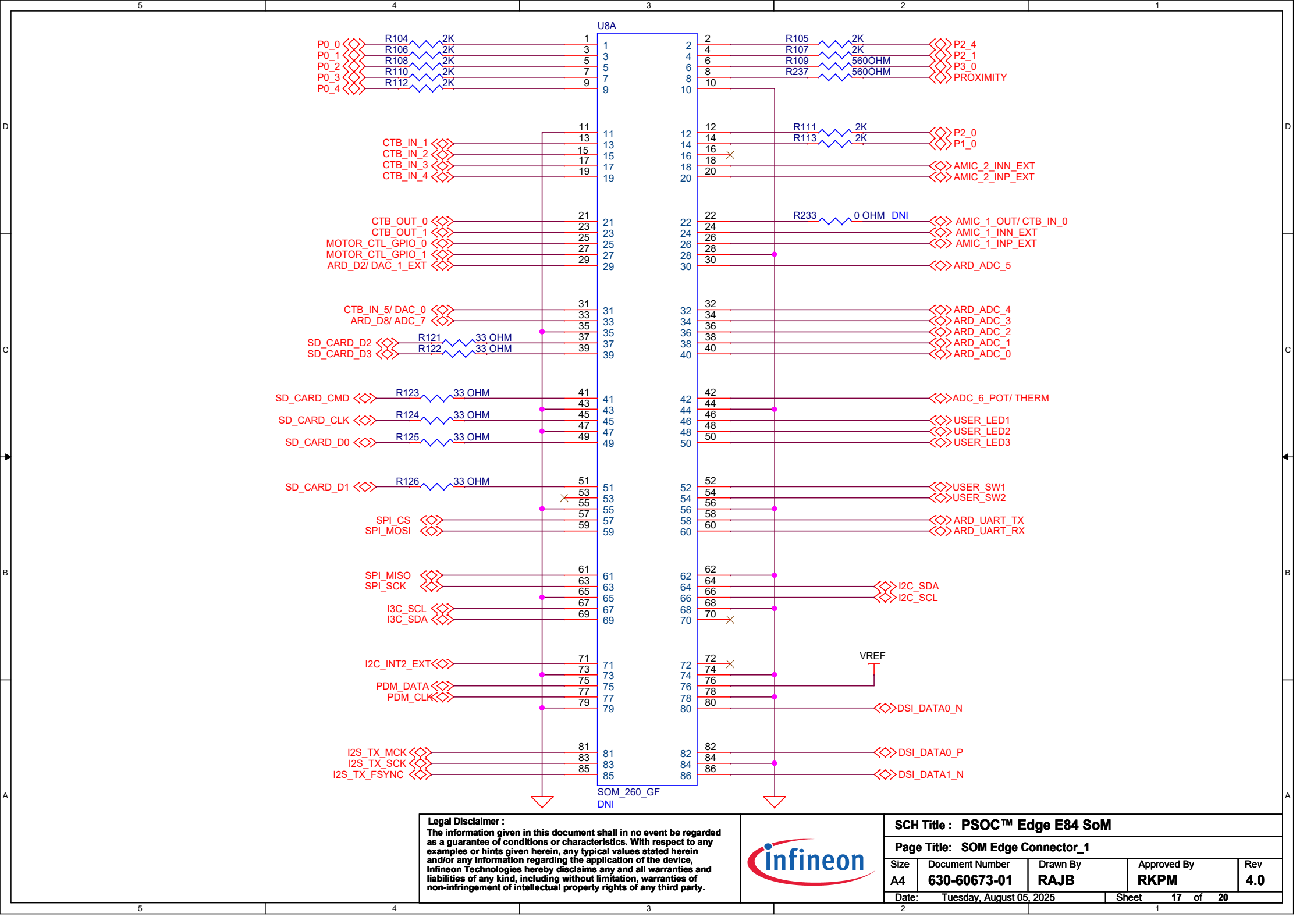


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Page Title: AMIC Signal Processing

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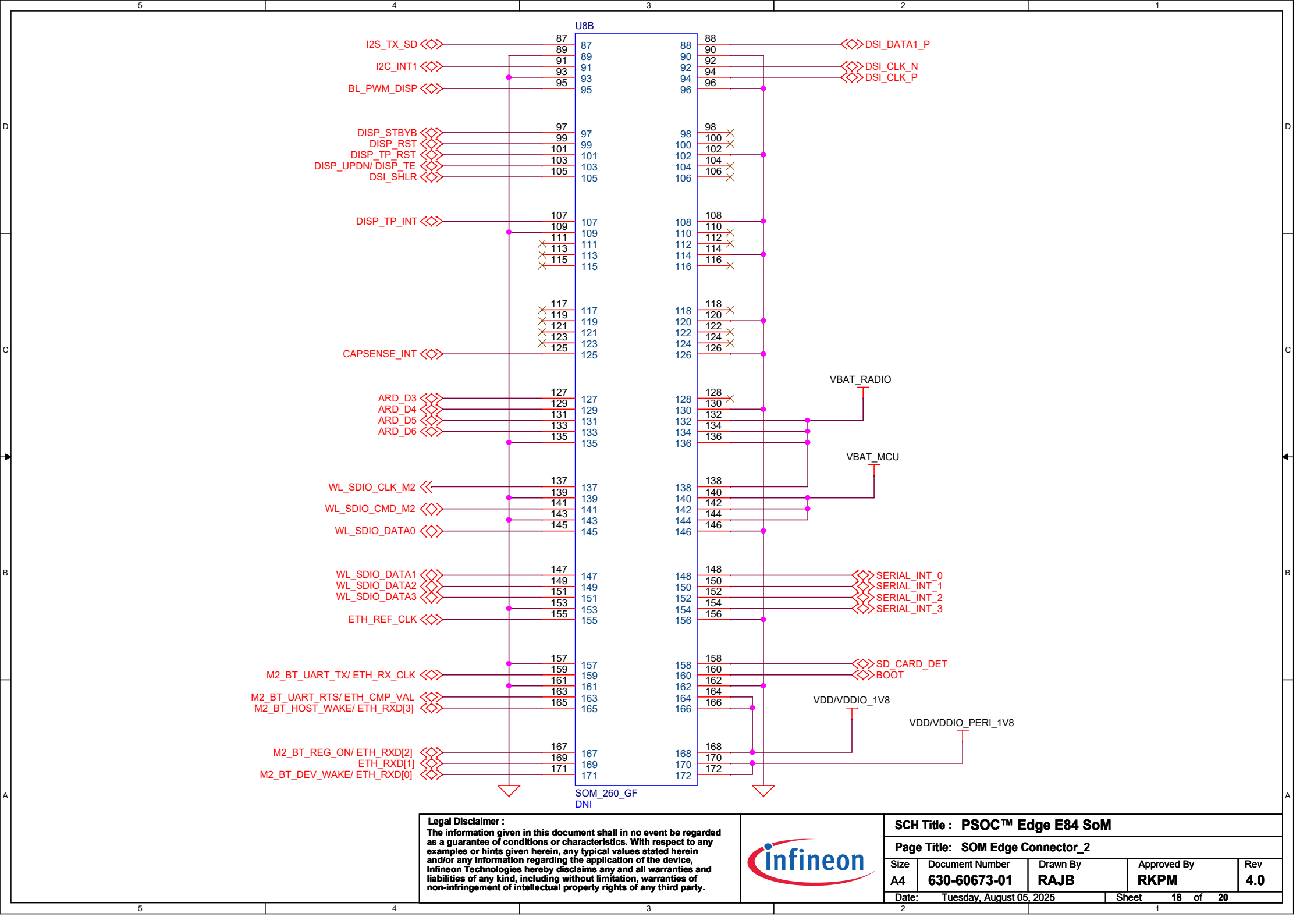




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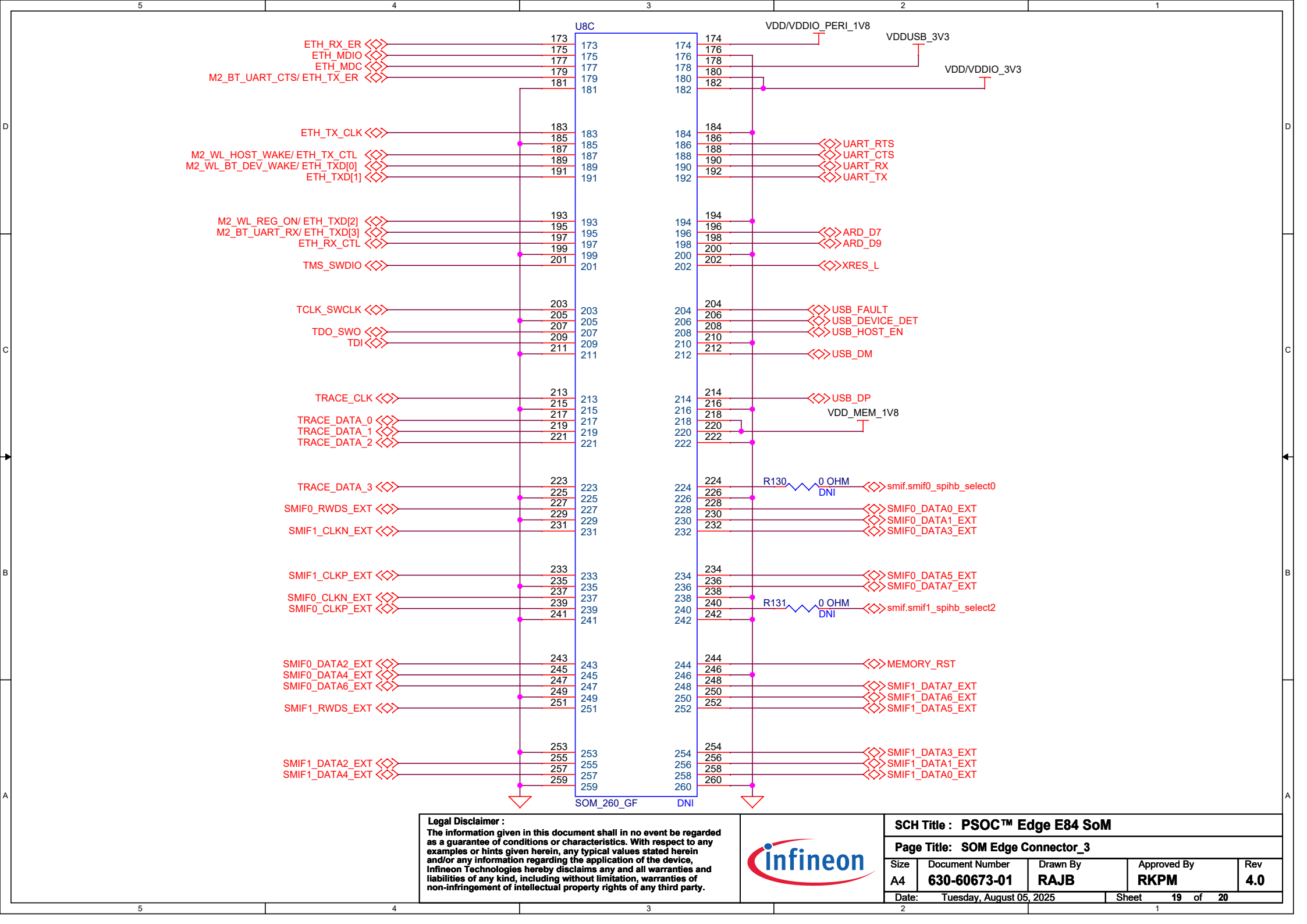
SCH Title : PSOC™ Edge E84 SoM				
Page Title : SOM Edge Connector_1				
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## REVISION HISTORY

REV	DESCRIPTION OF CHANGE	ORIG. OF CHANGE	DATE
1.0	Initial release	RAJB	08/08/2023
2.0	<ol style="list-style-type: none"> <li>P7.2 is connected to CapSense co-processor reset (has default pull up)</li> <li>EXT clk is connected to P7.4</li> <li>XRES pullup added</li> <li>VDDD and VCCD shorted with common supply on PSOC4000T section</li> </ol>	RAJB	31/10/2023
3.0	<ol style="list-style-type: none"> <li>R209, R212-R214, R218-R221, C137, C141 component values changed in AMIC signal processing ckt. section.</li> <li>MCU configured for VBAT domain</li> <li>TP12 added for VCCD</li> <li>R244, R245 added to segregate the memory powers</li> <li>R246 pullup added on Memory reset IO</li> <li>R242 powered from VDDD (instead of VDD/VDDIO_1V8)</li> <li>CapSense section power is taken from VDD/VDDIO_PERI_1V8 (instead of H1_1V8)</li> <li>Made C124 populated</li> <li>Made D5 as DNI</li> <li>R186 removed to bypass U13 and added in series with H1_1V8</li> <li>R247 added to disconnect AMIC section voltage dividers.</li> <li>R243 multiplexing resistor added</li> <li>RF frontend L17 and C107 parts are swapped</li> </ol>	RISR	03/04/2024
4.0	<ol style="list-style-type: none"> <li>Cosmetic changes and clean-up of passive component properties</li> </ol>	RKPM	05/08/2025

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