KIT_PSC3Mx_CC2

TABLE OF CONTENTS

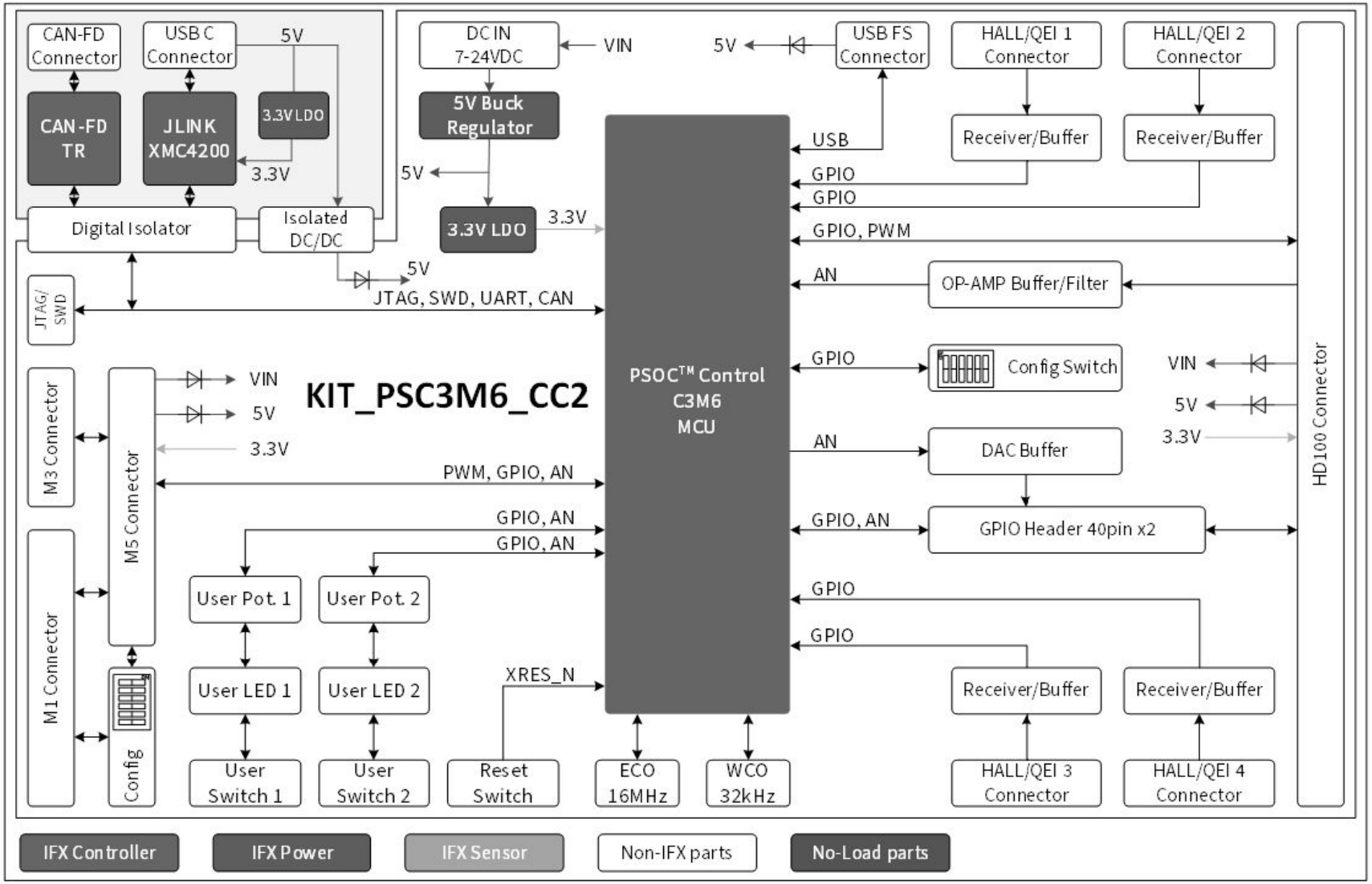
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Project	KIT_PSC3Mx_CC2		
Title	TABLE OF CONTENTS	Variant	PSC3M6_QFP100
Drawn By: M.N.	Reviewed By: --	Approved By: --	
Size: A4	Document Name: KIT_PSC3Mx_CC2.SchDoc	Rel.: 1.0	Rev.: --
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BLOCK DIAGRAM

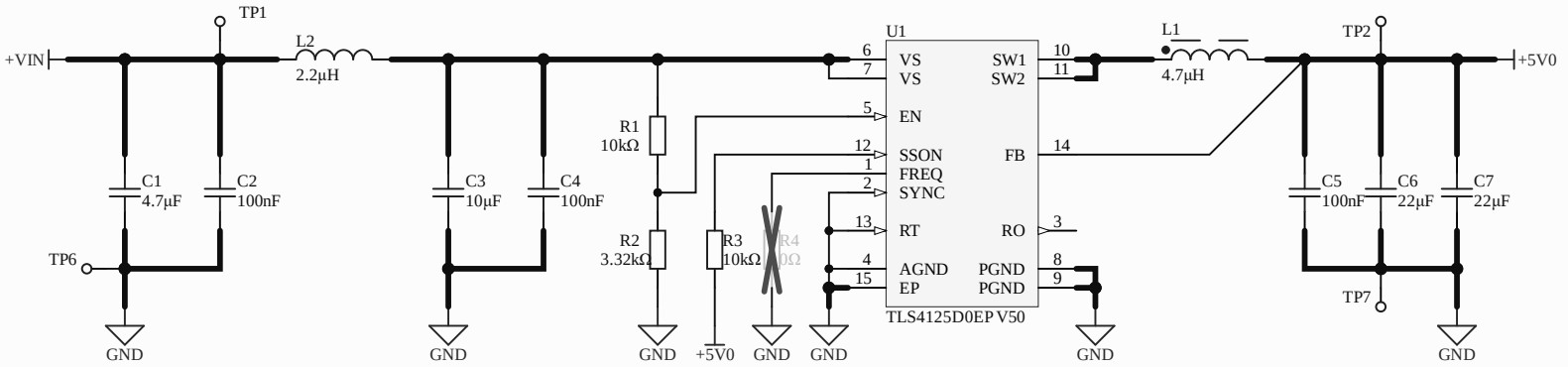


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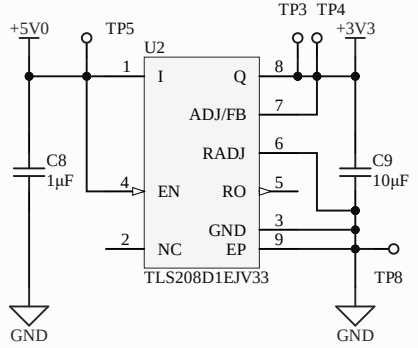
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Project	KIT_PSC3Mx_CC2		
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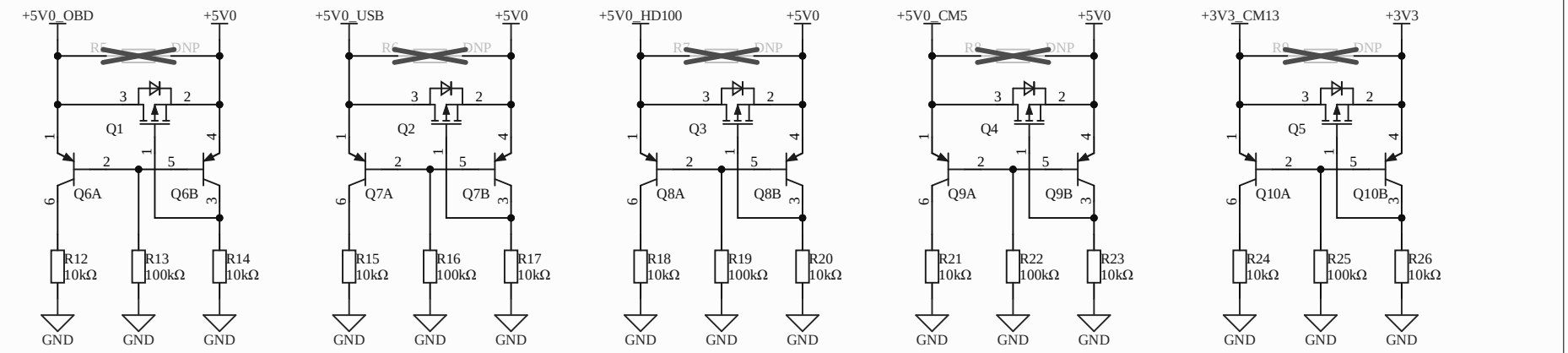
5V SWITCHING REGULATOR



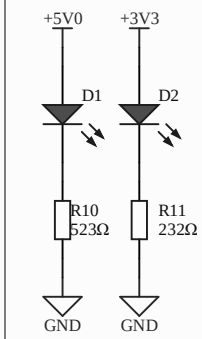
3.3V LDO REGULATOR



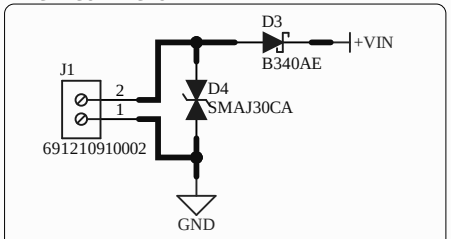
REVERSE VOLTAGE PROTECTION



POWER LEDs



DC IN CONNECTOR



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Project	KIT_PSC3Mx_CC2		
Title	POWER SUPPLY	Variant	PSC3M6_QFP100
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Size: A4	Document Name: KIT_PSC3Mx_CC2_POWER_SUPPLY.SchDoc	Rel.: 1.0	Rev.: --
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COMMON FOR ALL PACKAGES

DBG_TCLK_SWCLK	R27	0Ω	SWCLK_TCLK
DBG_TMS_SWDIO	R28	0Ω	SWDIO_TMS
DBG_TDI	R29	0Ω	USER_LED1_JTAG_TDI
DBG_TDO	R30	0Ω	USER_LED2_JTAG_TDO
UART_RX	R31	0Ω	UART_RX_DFU1
UART_TX	R32	0Ω	UART_TX_DFU2

PSC3M6 LQFP100 - POTENTIOMETERS

USER_POT1	R39	0Ω	MCU_P8.4
USER_POT2	R41	0Ω	MCU_P8.5
USER_POT1	R43	DNP	MCU_AN_B4
USER_POT2	R44	DNP	MCU_AN_B5

PSC3M6 LQFP100 & LQFP80

M3_PWR_EN	R45	0Ω	MCU_P9.6
M2_PWR_EN	R46	0Ω	MCU_P9.7

PSC3M5 LQFP80

M3_PWR_EN	R47	DNP	MCU_P8.4
M2_PWR_EN	R48	DNP	MCU_P8.5

PSC3M5/6 LQFP100, LQFP80, QFP/QFN64 - CAN SHARED

CAN_RX	R49	0Ω	MCU_P6.2
CAN_TX	R51	0Ω	MCU_P6.3
M3_PWM_WH	R54	DNP	MCU_P6.2
M3_PWM_WL	R56	DNP	MCU_P6.3

PSC3Mx QFP/QFN64 - ENCODER SHARED

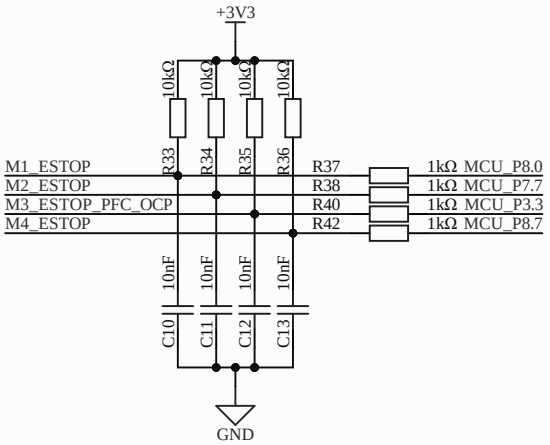
ENC1_HALL1_QEIA	R61	0Ω	MCU_P7.0
ENC1_HALL2_QEIB	R62	0Ω	MCU_P7.1
ENC1_HALL3_QEIZ	R63	0Ω	MCU_P7.2
M3_PWM_UH	R64	DNP	MCU_P7.0
M3_PWM_UL	R65	DNP	MCU_P7.1
M3_PWM_VH	R66	DNP	MCU_P7.2

MOTOR ENABLE PULL-DOWN RESISTORS

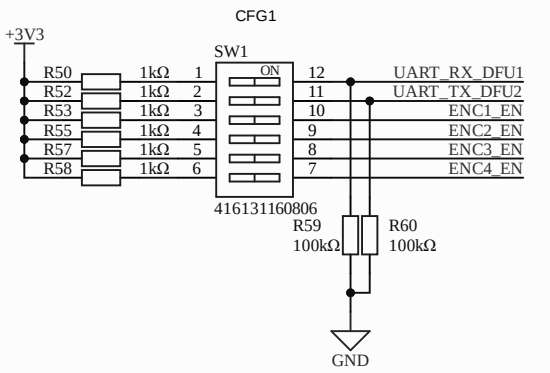
M1_PWR_EN	R67	10kΩ
M2_PWR_EN	R68	10kΩ
M3_PWR_EN	R69	10kΩ
M4_PWR_EN	R70	10kΩ

GND

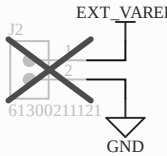
E-STOP SIGNALS



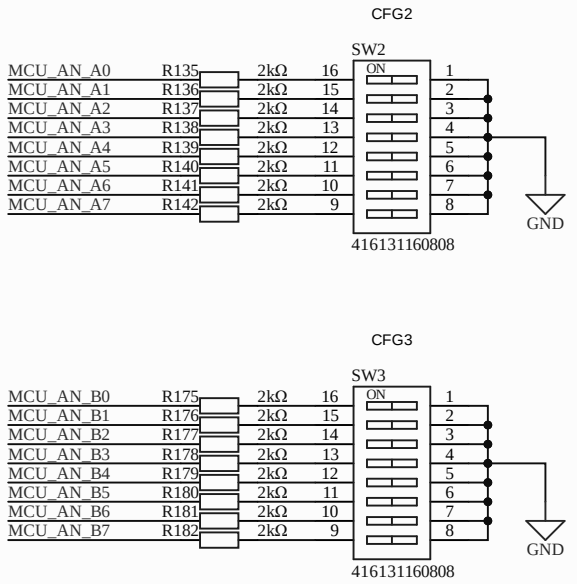
DFU & ENCODER ENABLE



EXTERNAL VAREF CONNECTOR



ANALOG INPUT ATTENUATION

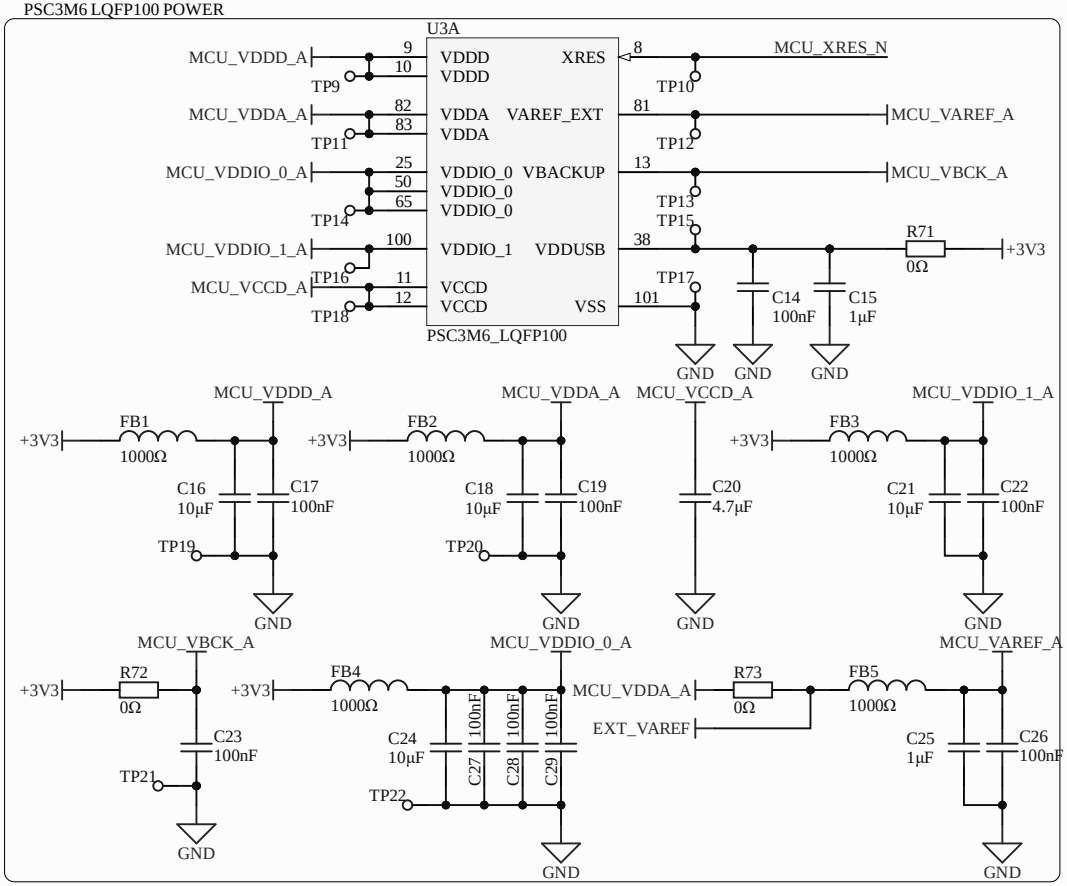


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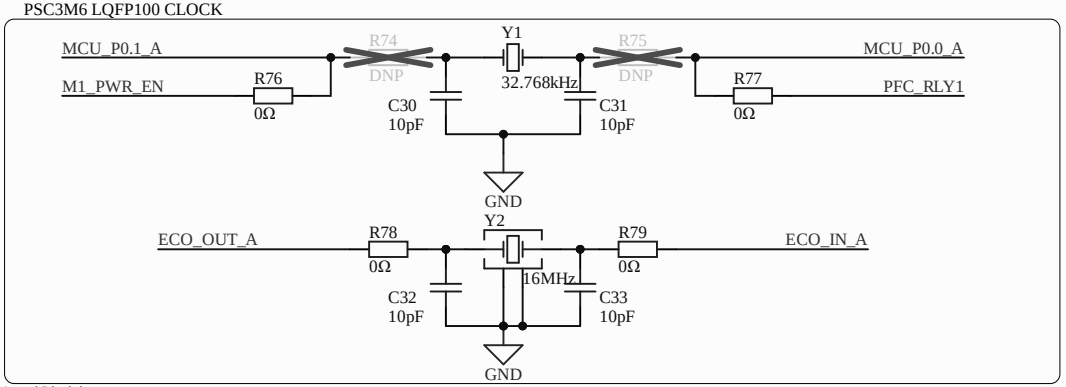
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Project	KIT_PSC3Mx_CC2		
Title	PSC3Mx COMMON	Variant	PSC3M6_QFP100
Drawn By: M.N.	Reviewed By: --	Approved By: --	
Size: A4	Document Name: KIT_PSC3Mx_CC2_PSC3Mx_COMMON.SchDoc	Rel.: 1.0	Rev.: --
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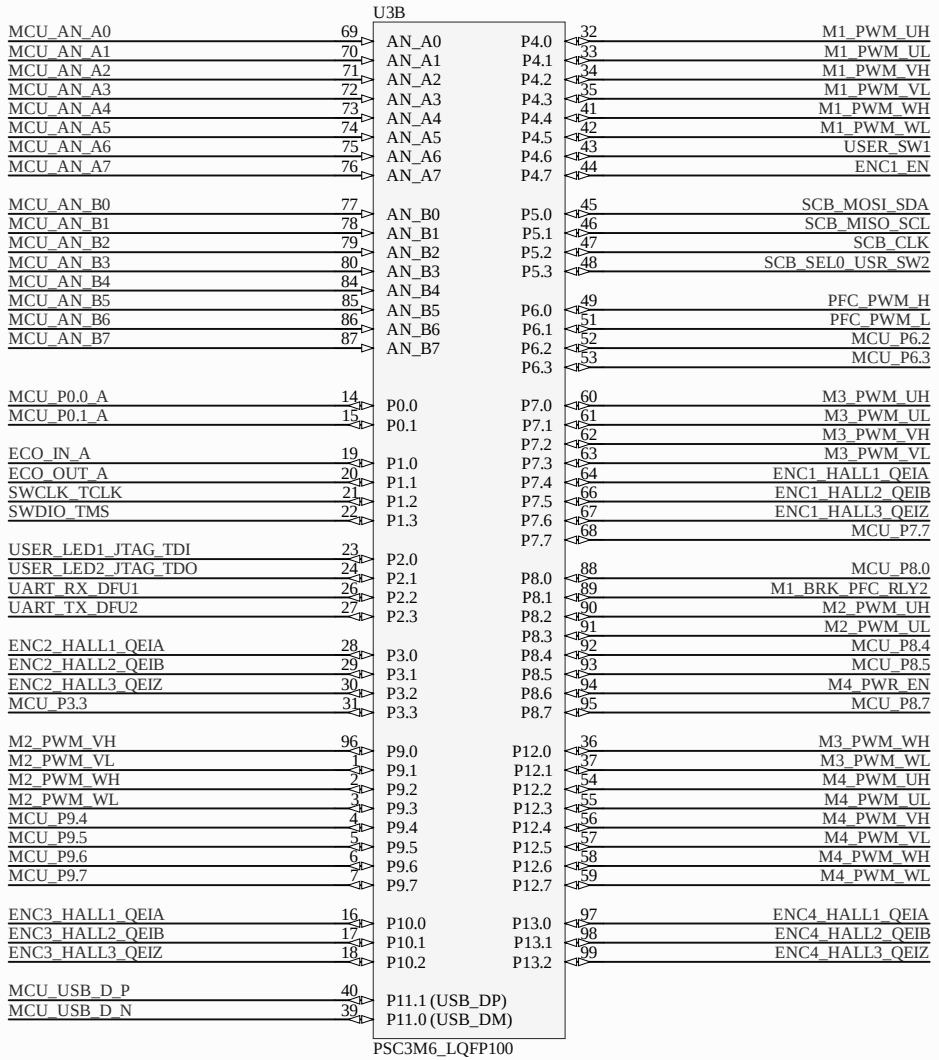
PSC3M6 LQFP100 POWER



PSC3M6 LQFP100 CLOCK



PSC3M6 LQFP100 IOs

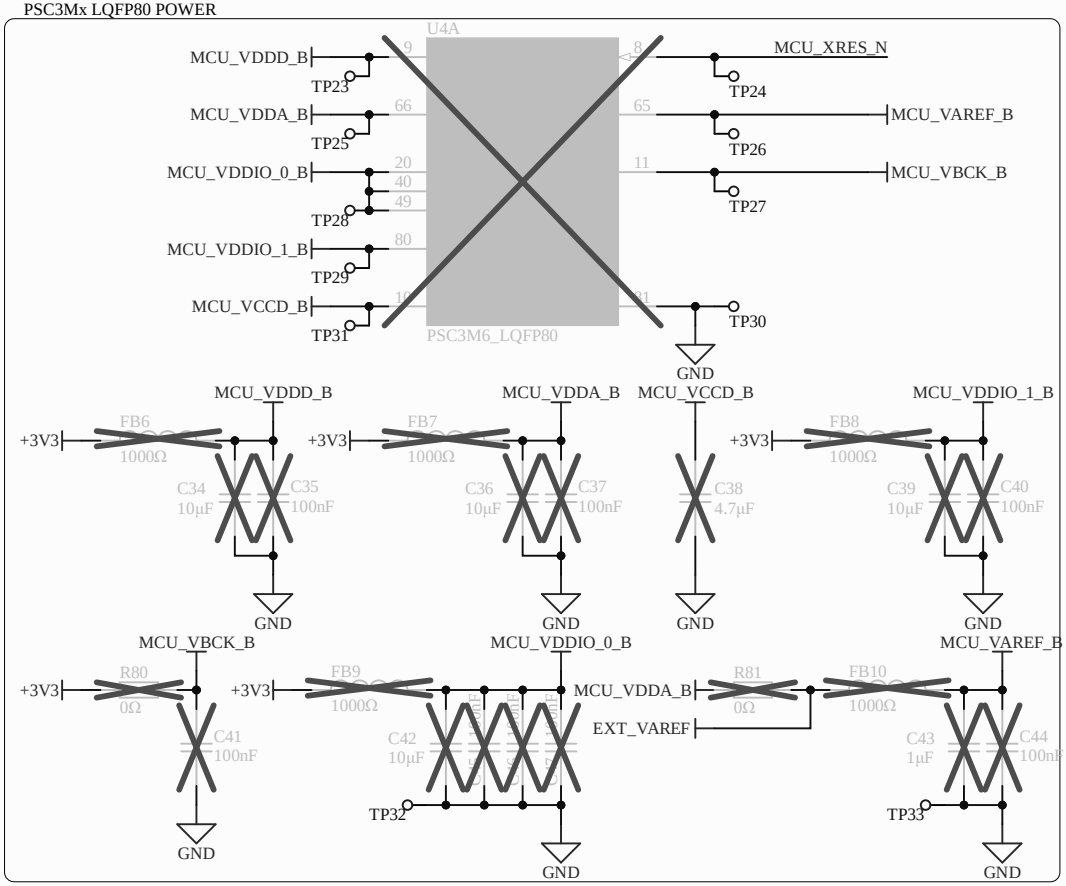


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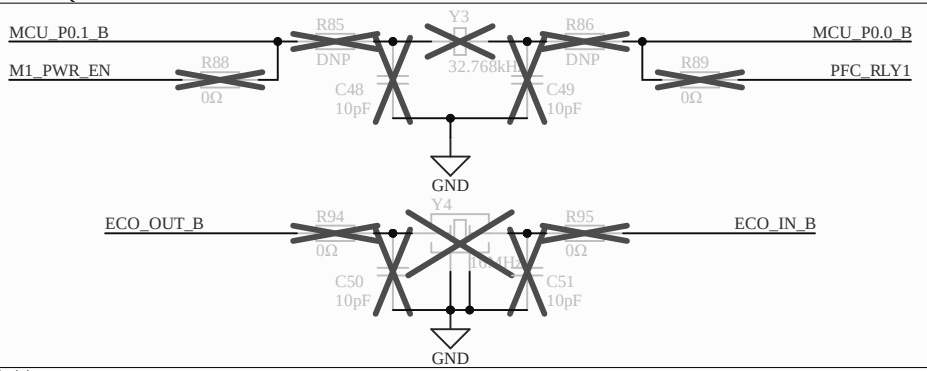
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Project	KIT_PSC3Mx_CC2		
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Size: A4	Document Name: KIT_PSC3Mx_CC2_PSC3Mx_LQFP100.SchDoc	Rel.: 1.0	Rev.: --
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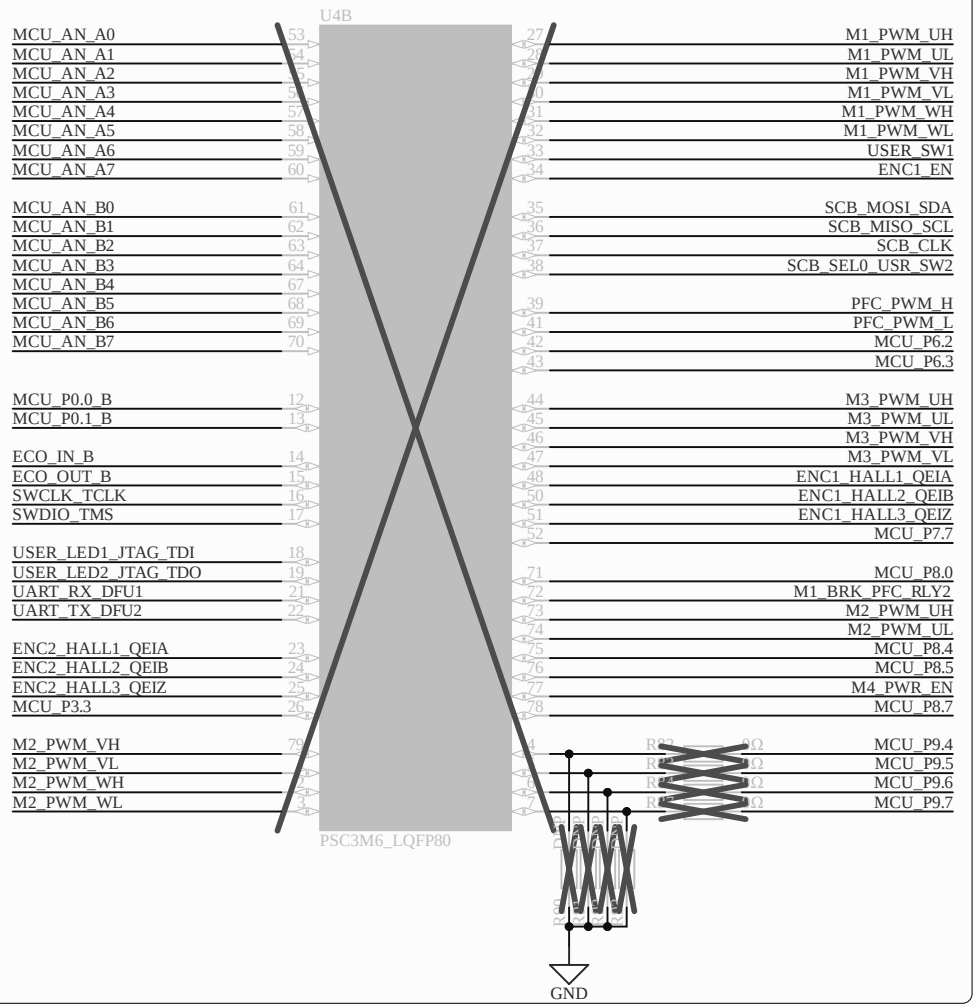
PSC3Mx LQFP80 POWER



PSC3M5/6 LQFP80 CLOCK



PSC3Mx LQFP80 IOs

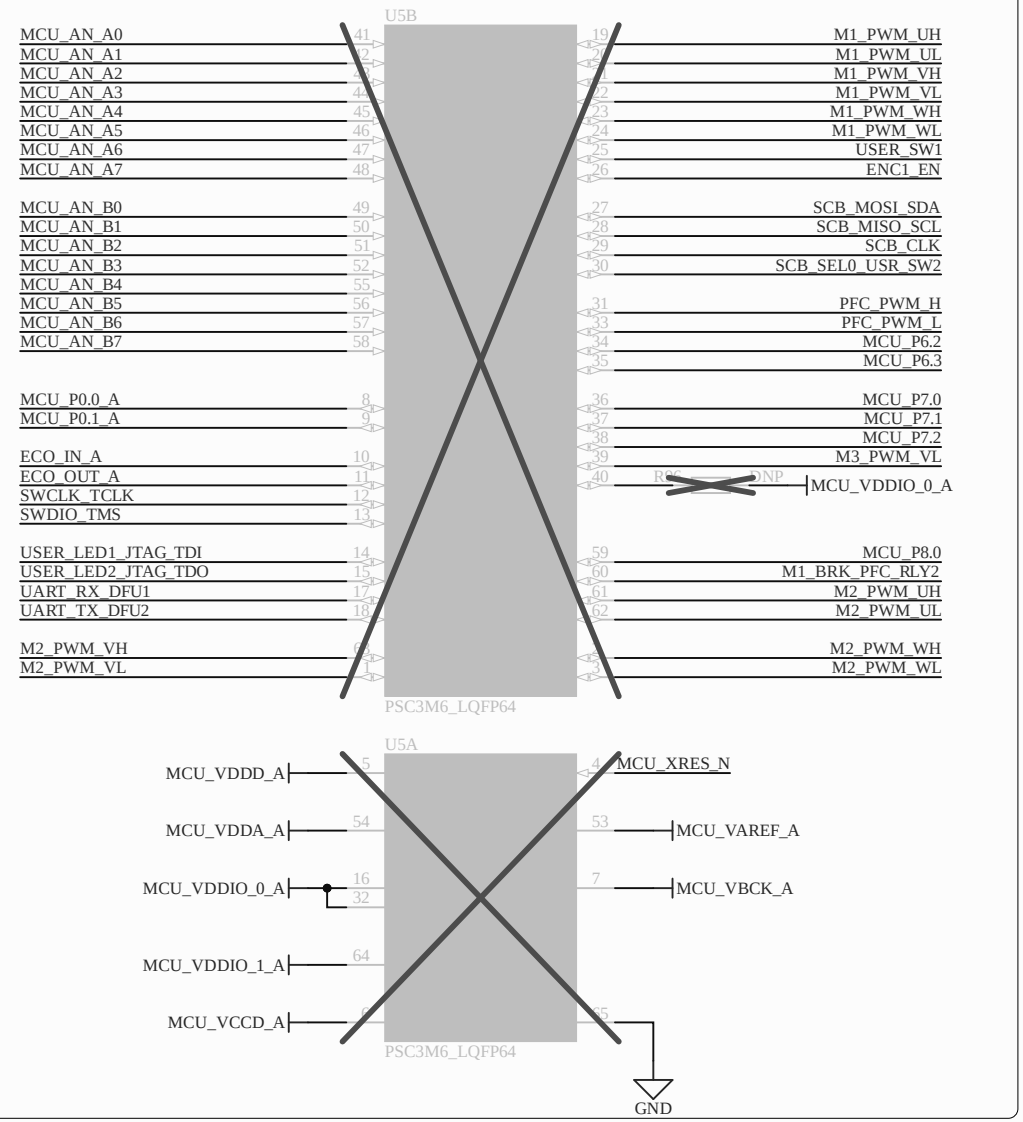


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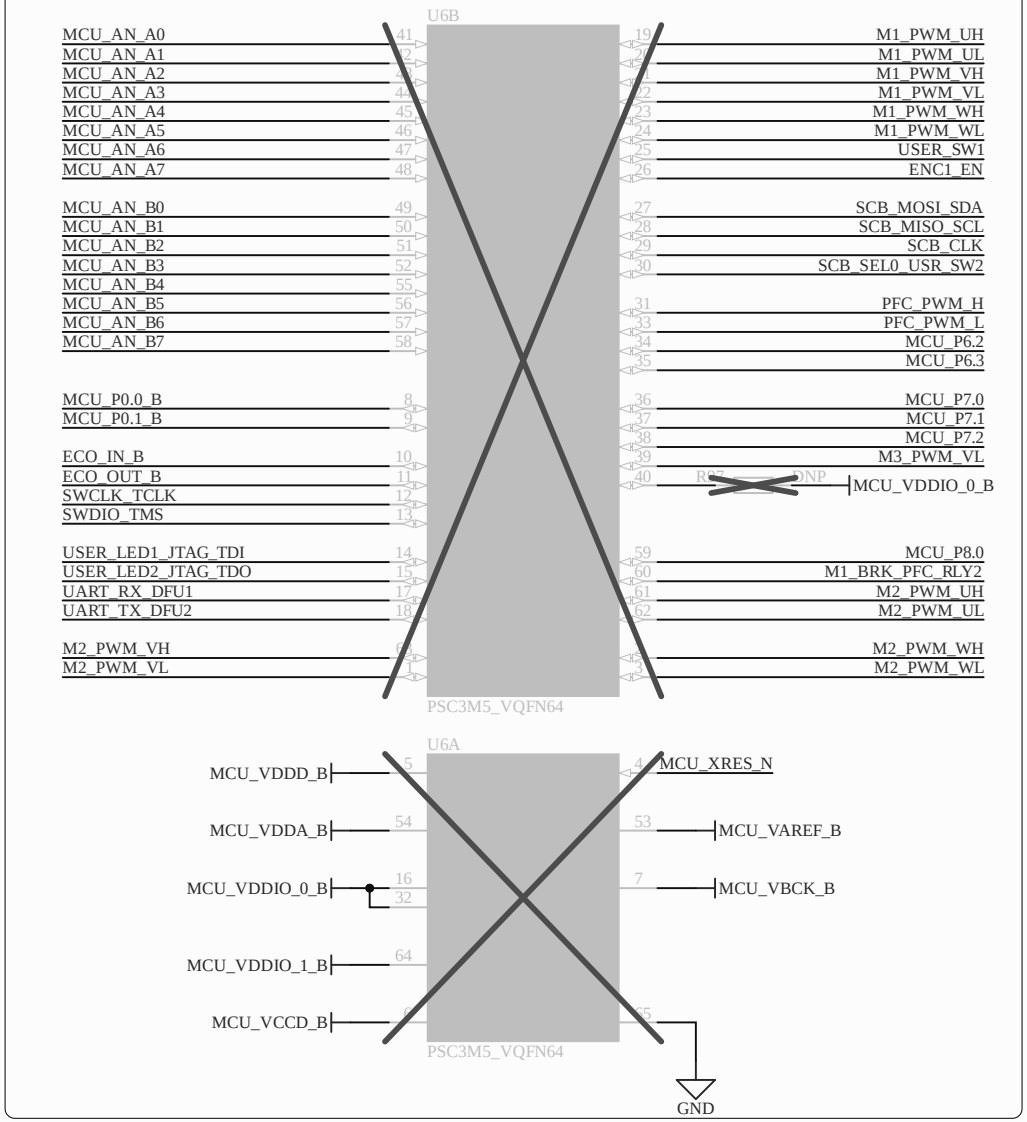
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Project	KIT_PSC3Mx_CC2		
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Size: A4	Document Name: KIT_PSC3Mx_CC2_PSC3Mx_LQFP80.SchDoc	Rel.: 1.0	Rev.: --
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PSC3Mx LQFP64 POWER & IOs



PSC3Mx VQFN64 POWER & IOs

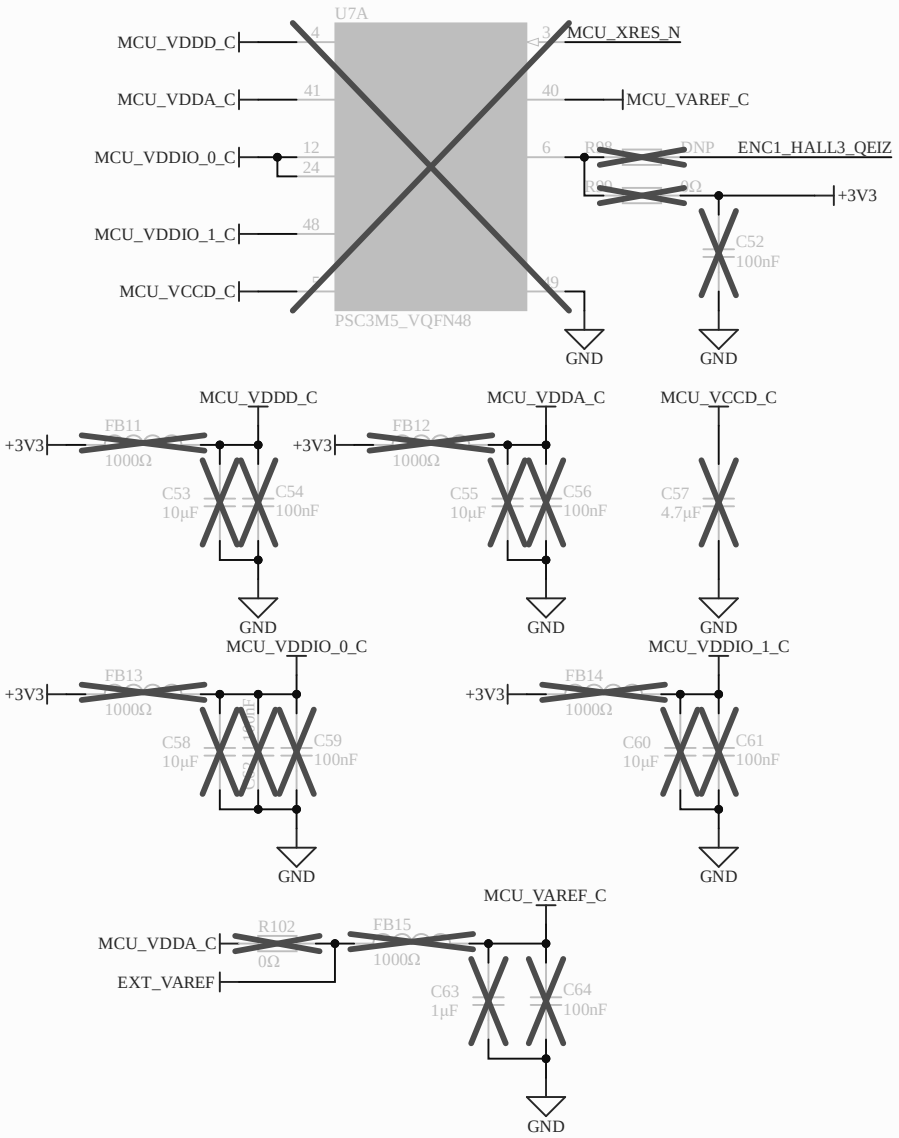


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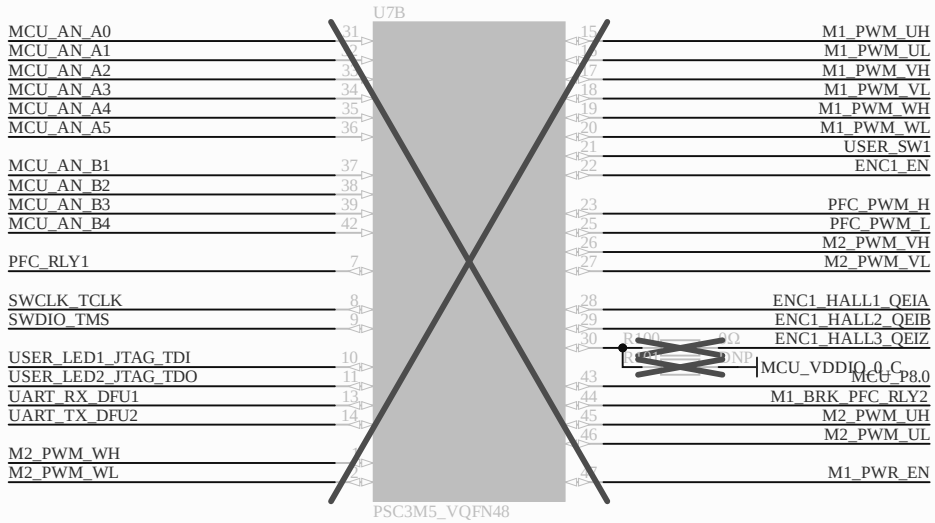
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PSC3Mx VQFN48 POWER



PSC3Mx VQFN48 IOs



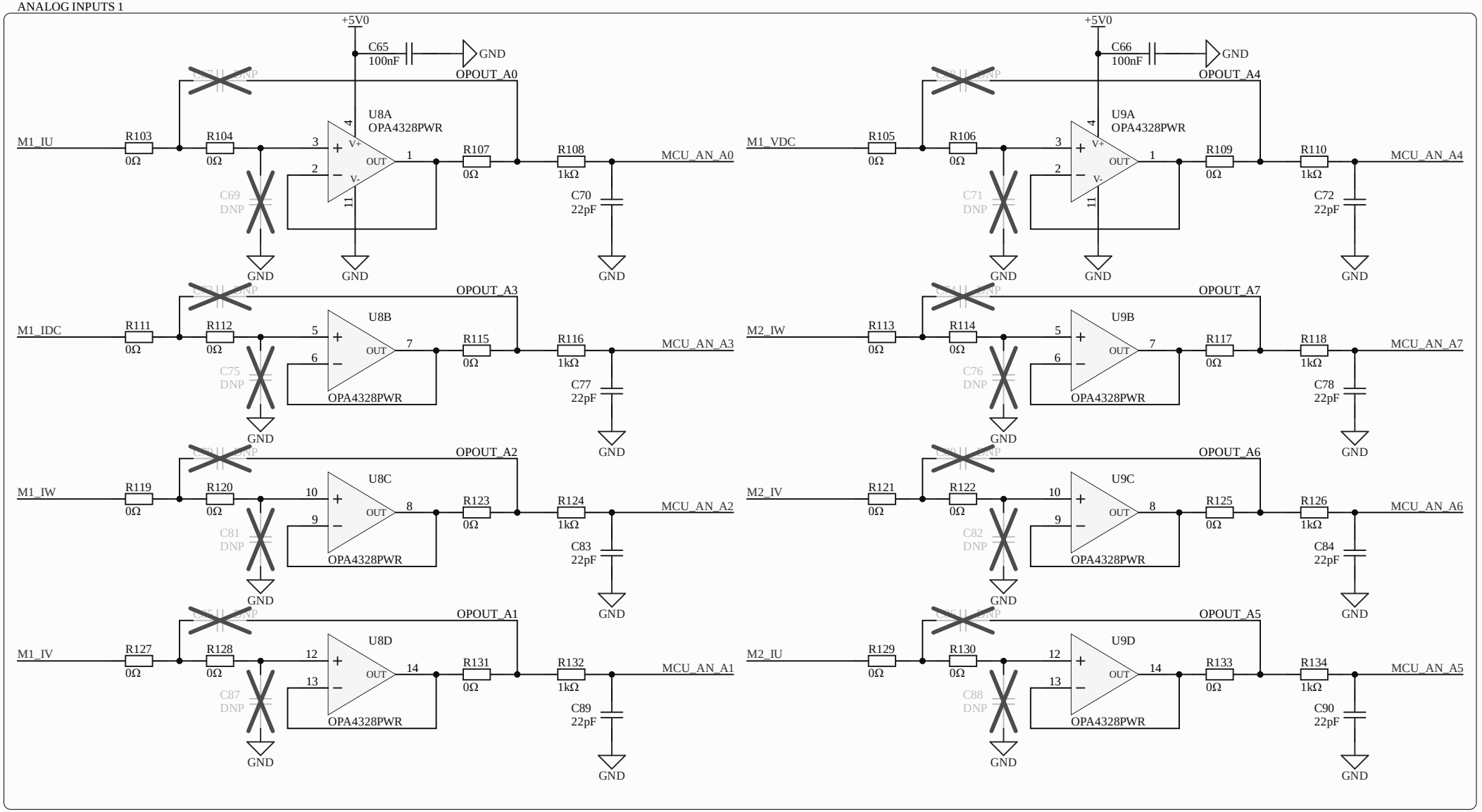
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Project	KIT_PSC3Mx_CC2		
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ANALOG INPUTS 1

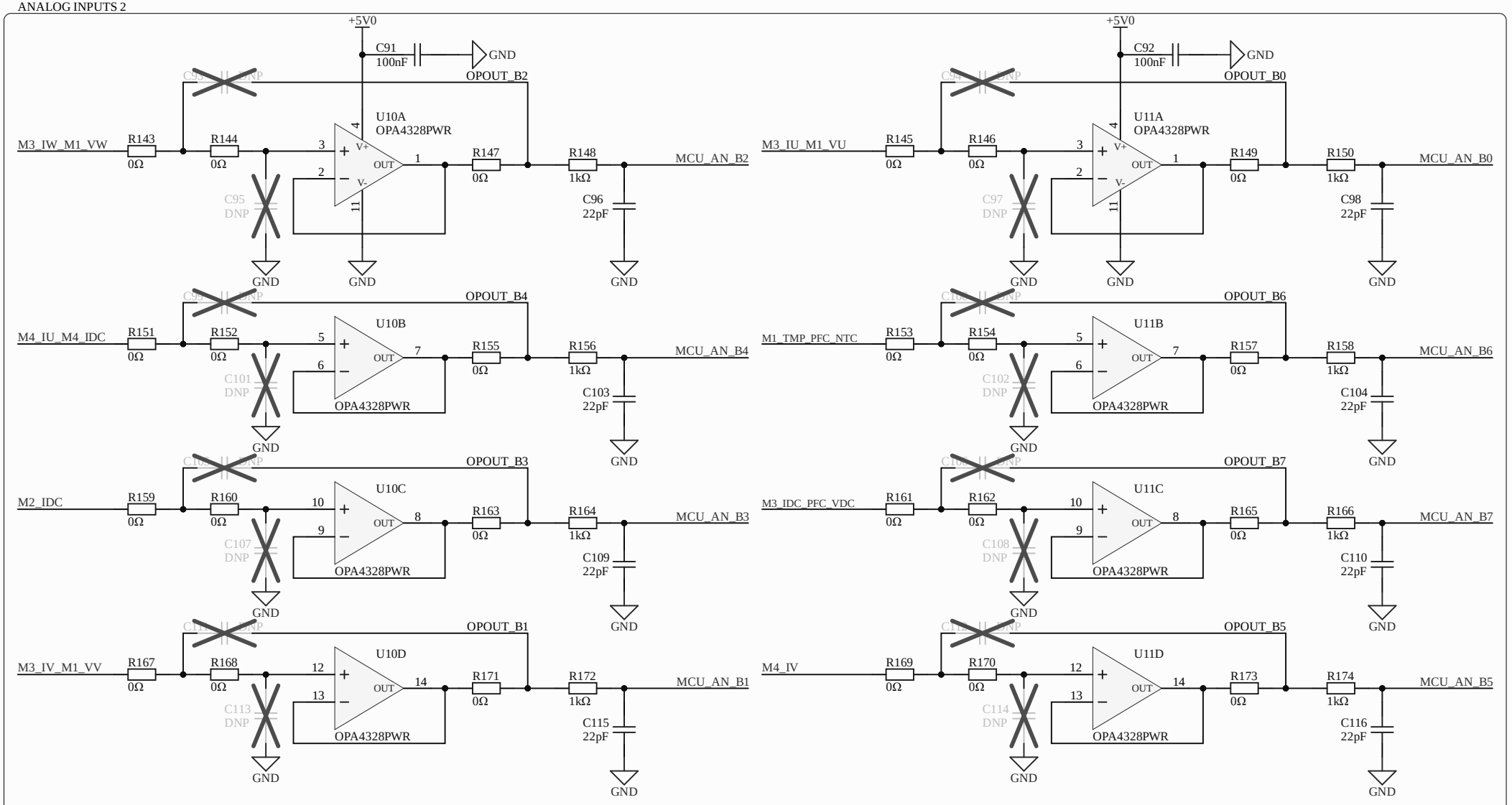


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Title	ANALOG INPUTS 1	Variant	PSC3M6_QFP100
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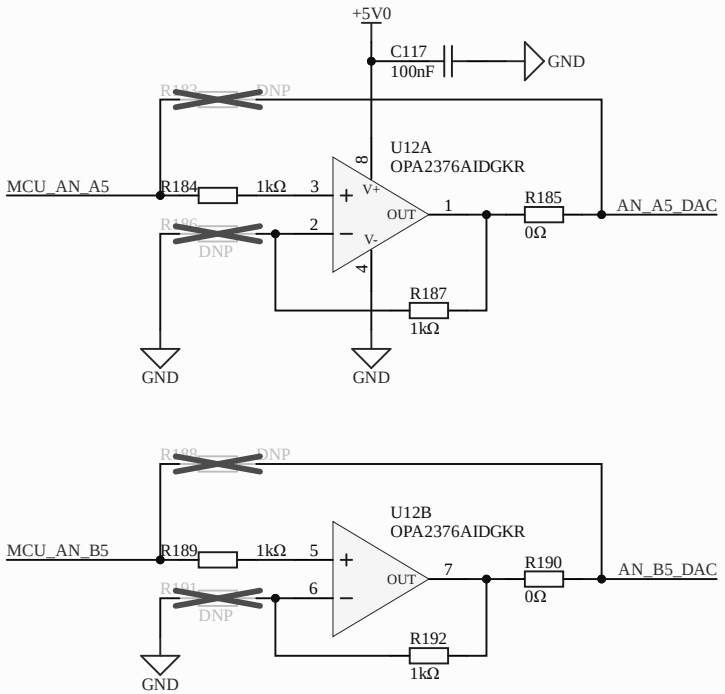
ANALOG INPUTS 2



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Project	KIT_PSC3Mx_CC2		
Title	ANALOG INPUTS 2	Variant	PSC3M6_QFP100
Drawn By: M.N.	Reviewed By: --	Approved By: --	
Size: A4	Document Name: KIT_PSC3Mx_CC2_ANALOG_2.SchDoc	Rel.: 1.0	Rev.: --
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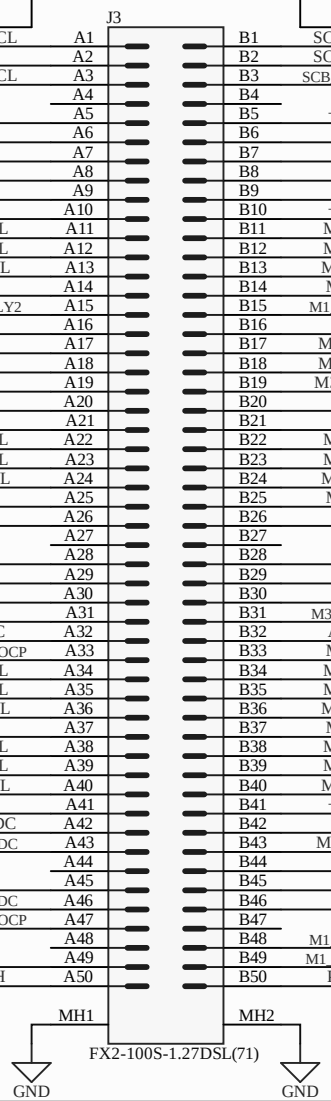
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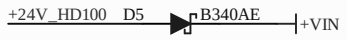
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Size: A4	Document Name: KIT_PSC3Mx_CC2_ANALOG_DAC.SchDoc	Rel.: 1.0	Rev.: --
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HD100 CONNECTOR

PSC3Mx		PSC3M5			PSC3M6			HD100	NET NAME			NET NAME	HD100	PSC3M6			PSC3M5			PSC3Mx	
QFN48	QFP64/QFN64	QFN48	QFP64/QFN64	QFP80	QFP64	QFP80	QFP100							QFP100	QFP80	QFP64	QFP80	QFP64/QFN64	QFN48	QFP64/QFN64	QFN48
	P5.1		P5.1	P5.1	P5.1	P5.1	P5.1	IIC_SCL	SCB_MISO_SCL	A1	B1	SCB_MOSI_SDA	IIC_SDA	P5.0	P5.0	P5.0	P5.0	P5.0		P5.0	
	P5.2		P5.2	P5.2	P5.2	P5.2	P5.2	SPI_CLK	SCB_CLK	A2	B2	SCB_MOSI_SDA	SPI_MOSI	P5.0	P5.0	P5.0	P5.0	P5.0		P5.0	
	P5.1		P5.1	P5.1	P5.1	P5.1	P5.1	SPI_MISO	SCB_MISO_SCL	A3	B3	SCB_SEL0_USR_SW2	SPI_CS0	P5.3	P5.3	P5.3	P5.3	P5.3		P5.3	
								SPI_CS1	NC	A4	B4	NC	SPI_CS2								
								GND	GND	A5	B5	+5V0_HD100	PWR_IN_5V								
								AGND	GND	A6	B6	M1_IU	AIN_IU_VU	AN_A0	AN_A0	AN_A0	AN_A0	AN_A0	AN_A0	P10.0	P10.0
								AGND	GND	A7	B7	M1_IV	AIN_IV_VV	AN_A1	AN_A1	AN_A1	AN_A1	AN_A1	AN_A1	P10.1	P10.1
								AGND	GND	A8	B8	M1_IW	AIN_IW_VW	AN_A2	AN_A2	AN_A2	AN_A2	AN_A2	AN_A2	P10.2	P10.2
								AGND	GND	A9	B9	M1_IDC	AIN_IDC	AN_A3	AN_A3	AN_A3	AN_A3	AN_A3	AN_A3	P10.3	P10.3
								GND	GND	A10	B10	+5V0_HD100	PWR_IN_5V								
P4.1	P4.1	P4.1	P4.1	P4.1	P4.1	P4.1	P4.1	PWM_UL	M1_PWM_UL	A11	B11	M1_PWM_UH	PWM_UH	P4.0	P4.0	P4.0	P4.0	P4.0	P4.0	P4.0	P4.0
P4.3	P4.3	P4.3	P4.3	P4.3	P4.3	P4.3	P4.3	PWM_VL	M1_PWM_VL	A12	B12	M1_PWM_VH	PWM_VH	P4.2	P4.2	P4.2	P4.2	P4.2	P4.2	P4.2	P4.2
P4.5	P4.5	P4.5	P4.5	P4.5	P4.5	P4.5	P4.5	PWM_WL	M1_PWM_WL	A13	B13	M1_PWM_WH	PWM_WH	P4.4	P4.4	P4.4	P4.4	P4.4	P4.4	P4.4	P4.4
P8.0	P8.0	P8.0	P8.0	P8.0	P8.0	P8.0	P8.0	ESTOP	M1_ESTOP	A14	B14	M1_PWR_EN	GD_EN	P0.1	P0.1	P0.1	P0.1	P0.1	P9.0	P0.1	P9.0
P8.1	P8.1	P8.1	P8.1	P8.1	P8.1	P8.1	P8.1	PWM_BRAKE	M1_BRK_PFC_RLY2	A15	B15	M1_TMP_PFC_NTC	AIN_TEMP1	AN_B6	AN_B6	AN_B6	AN_B6	AN_B6		P10.6	
								AGND	GND	A16	B16	M1_VDC	AIN_VDC	AN_A4	AN_A4	AN_A4	AN_A4	AN_A4	AN_A4	P10.4	P10.4
								AGND	GND	A17	B17	M3_IU_M1_VU	AIN_IU_VU	AN_B0	AN_B0	AN_B0	AN_B0	AN_B0		P11.0	
								AGND	GND	A18	B18	M3_IV_M1_VV	AIN_IV_VV	AN_B1	AN_B1	AN_B1	AN_B1	AN_B1		P11.1	
								AGND	GND	A19	B19	M3_IW_M1_VW	AIN_IW_VW	AN_B2	AN_B2	AN_B2	AN_B2	AN_B2		P11.2	
								AGND	GND	A20	B20	M2_IDC	AIN_IDC	AN_B3	AN_B3	AN_B3	AN_B3	AN_B3	AN_B3	P11.3	P11.3
								GND	GND	A21	B21	+3V3	PWR_OUT_VSYS								
P8.3	P8.3	P8.3	P8.3	P8.3	P8.3	P8.3	P8.3	PWM_UL	M2_PWM_UL	A22	B22	M2_PWM_UH	PWM_UH	P8.2	P8.2	P8.2	P8.2	P8.2	P8.2	P8.2	P8.2
P9.3	P9.1	P9.3	P9.1	P9.1	P9.1	P9.1	P9.1	PWM_VL	M2_PWM_VL	A23	B23	M2_PWM_VH	PWM_VH	P9.0	P9.0	P9.0	P9.0	P9.0	P9.2	P9.0	P9.2
P6.3	P9.3	P6.3	P9.3	P9.3	P9.3	P9.3	P9.3	PWM_WL	M2_PWM_WL	A24	B24	M2_PWM_WH	PWM_WH	P9.2	P9.2	P9.2	P9.2	P9.2	P6.2	P9.2	P6.2
				P7.7		P7.7	P7.7	ESTOP	M2_ESTOP	A25	B25	M2_PWR_EN	GD_EN	P9.7	P9.7		P8.5				
			P7.3	P9.4	P7.3	P9.4	P9.4	GPIO_RES1	MCU_P9.4	A26	B26	MCU_P9.5	GPIO_RES2	P9.5	P9.5	P7.4	P9.5	P7.4			
								GPIO_RES3	NC	A27	B27	NC	GPIO_RES4								
								AGND	GND	A28	B28	M2_IU	AIN_IU_VU	AN_A5	AN_A5	AN_A5	AN_A5	AN_A5	AN_A5	P10.5	P10.5
								AGND	GND	A29	B29	M2_IV	AIN_IV_VV	AN_A6	AN_A6	AN_A6	AN_A6	AN_A6	AN_B3	P10.6	P10.3
								AGND	GND	A30	B30	M2_IW	AIN_IW_VW	AN_A7	AN_A7	AN_A7	AN_A7	AN_A7		P10.7	
								AGND	GND	A31	B31	M3_IDC_PFC_VDC	AIN_IDC	AN_B7	AN_B7	AN_B7	AN_B7	AN_B7		P11.7	
							DAC0	AIN_RES1	AN_A5_DAC	A32	B32	AN_B5_DAC	AIN_RES2	DAC1							
				P3.3		P3.3	P3.3	ESTOP	M3_ESTOP_PFC_OCP	A33	B33	M3_PWR_EN	GD_EN	P9.6	P9.6		P8.4				
	P7.1*		P7.1*	P7.1	P7.1*	P7.1	P7.1	PWM_UL	M3_PWM_UL	A34	B34	M3_PWM_UH	PWM_UH	P7.0	P7.0	P7.0*	P7.0	P7.0*		P7.0*	
	P7.3		P7.3	P7.3	P7.3	P7.3	P7.3	PWM_VL	M3_PWM_VL	A35	B35	M3_PWM_VH	PWM_VH	P7.2	P7.2	P7.2*	P7.2	P7.2*		P7.2*	
	P6.3		P6.3*	P6.3*		P6.3*	P12.1	PWM_WL	M3_PWM_WL	A36	B36	M3_PWM_WH	PWM_WH	P12.0	P6.2*		P6.2*	P6.2*		P6.2	
				(P8.7)		(P8.7)	P8.7	ESTOP	M4_ESTOP	A37	B37	M4_PWR_EN	GD_EN	P8.6	(P8.6)		(P8.6)				
							P12.3	PWM_UL	M4_PWM_UL	A38	B38	M4_PWM_UH	PWM_UH	P12.2							
							P12.5	PWM_VL	M4_PWM_VL	A39	B39	M4_PWM_VH	PWM_VH	P12.4							
							P12.7	PWM_WL	M4_PWM_WL	A40	B40	M4_PWM_WH	PWM_WH	P12.6							
								GND	GND	A41	B41	+24V_HD100	PWR_IN_24V								
							AN_B4	AIN_IU_VU	M4_IU_M4_IDC	A42	B42	M4_IV	AIN_IV_VV	AN_B5							
							AN_B7	AIN_IW_VW	M3_IDC_PFC_VDC	A43	B43	M4_IU_M4_IDC	AIN_IDC	AN_B4							
								AIN_RES4	NC	A44	B44	M2_IW	AIN_RES3	AN_A7	AN_A7	AN_A7	AN_A7	AN_A7		P10.7	
								AGND	GND	A45	B45	M2_IU	AIN_PFC_I1	AN_A5	AN_A5	AN_A5	AN_A5	AN_A5	AN_A5	P10.5	P10.5
	P11.7		AN_B7	AN_B7	AN_B7	AN_B7	AN_B7	AIN_PFC_VDC	M3_IDC_PFC_VDC	A46	B46	M2_IV	AIN_PFC_I2	AN_A6	AN_A6	AN_A6	AN_A6	AN_A6		P10.6	
				P3.3		P3.3	P3.3	GPIO_RES5	M3_ESTOP_PFC_OCP	A47	B47	NC	AIN_RES5								
								AIN_RES6	NC	A48	B48	M1_TMP_PFC_NTC	AIN_TEMP2	AN_B6	AN_B6	AN_B6	AN_B6	AN_B6		P11.6	
P0.0	P0.0	P0.0	P0.0	P0.0	P0.0	P0.0	P0.0	PFC_RELAY1	PFC_RLY1	A49	B49	M1_BRK_PFC_RLY2	PFC_RELAY2	P8.1	P8.1	P8.1	P8.1	P8.1	P8.1	P8.1	P8.1
P6.0	P6.0	P6.0	P6.0	P6.0	P6.0	P6.0	P6.0	PWM_PFC_H	PFC_PWM_H	A50	B50	PFC_PWM_L	PWM_PFC_L	P6.1	P6.1	P6.1	P6.1	P6.1	P6.1	P6.1	P6.1





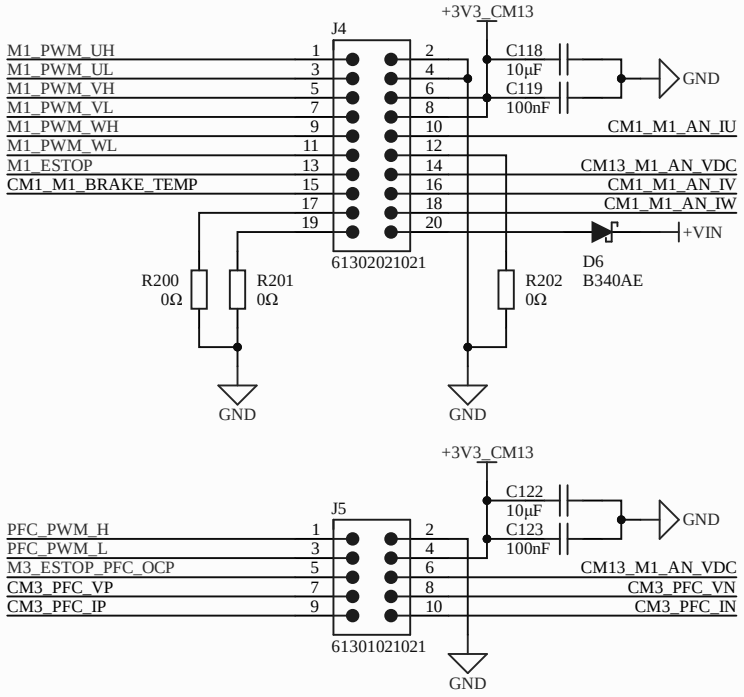
NC - NOT CONNECTED
Px.y*, AN_xy* - SHARED
(Px.y) - NOT USED

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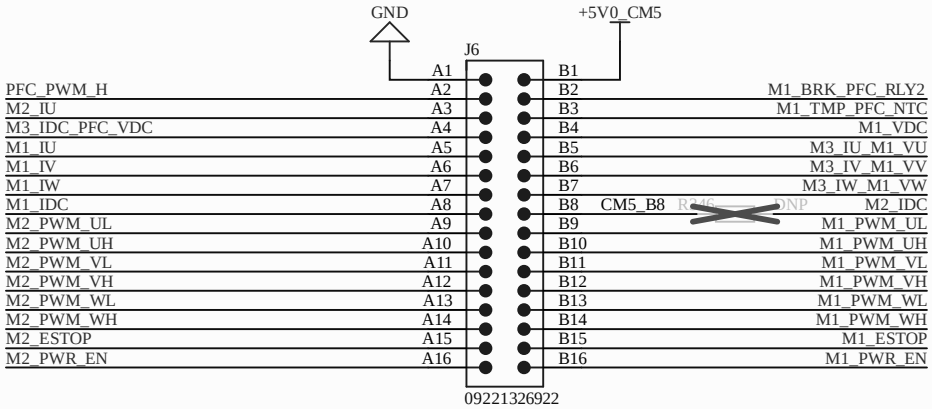
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Project	KIT_PSC3Mx_CC2		
Title	HD100 CONNECTOR	Variant	PSC3M6_QFP100
Drawn By: M.N.	Reviewed By: --	Approved By: --	
Size: A4	Document Name: KIT_PSC3Mx_CC2_HD100_CONN.SchDoc	Rel.: 1.0	Rev.: --
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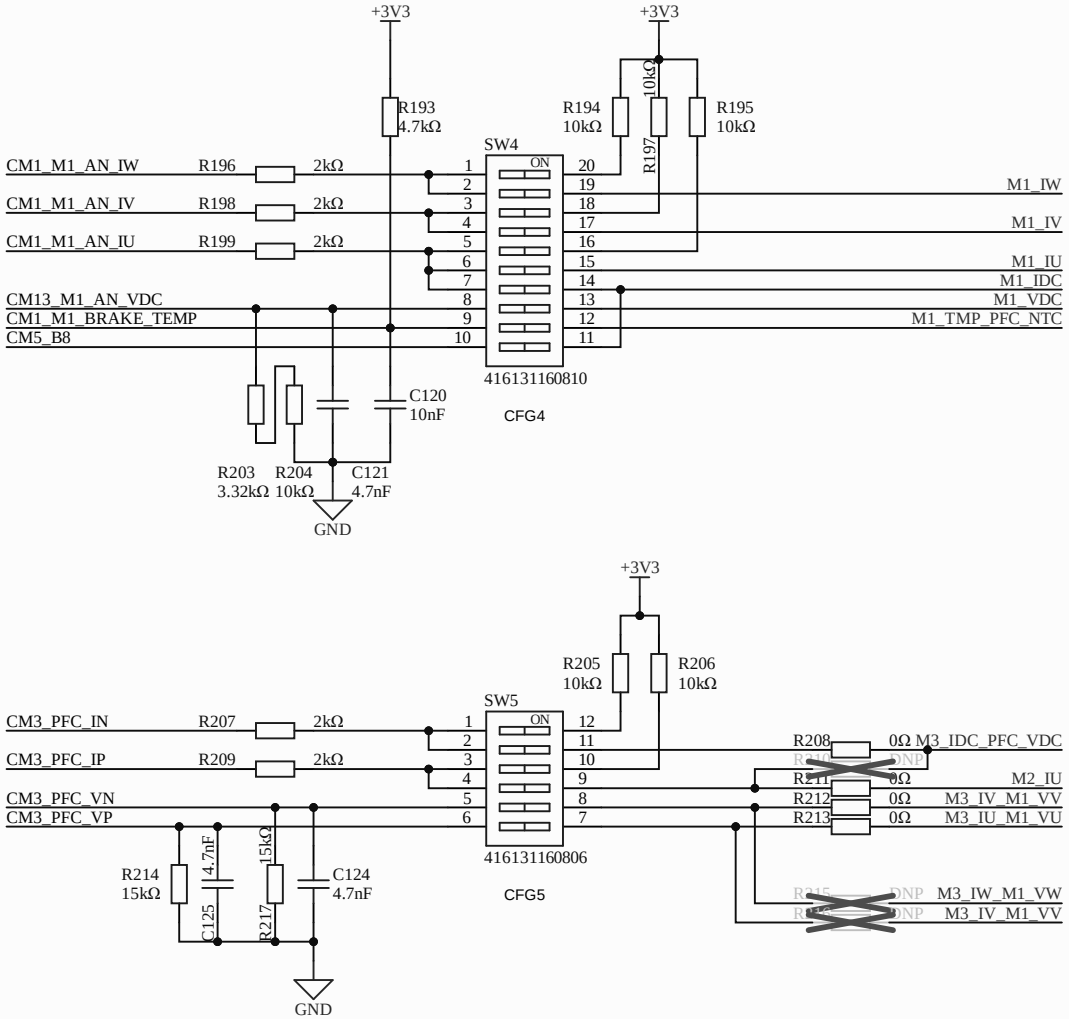
M1/M3 CONNECTOR



M5 CONNECTOR



M1/M3 CONNECTOR CONFIG

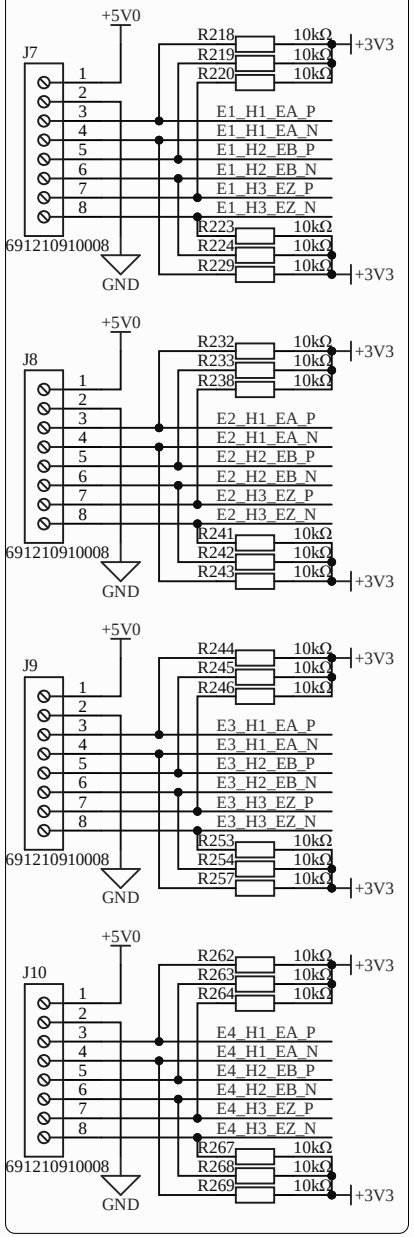


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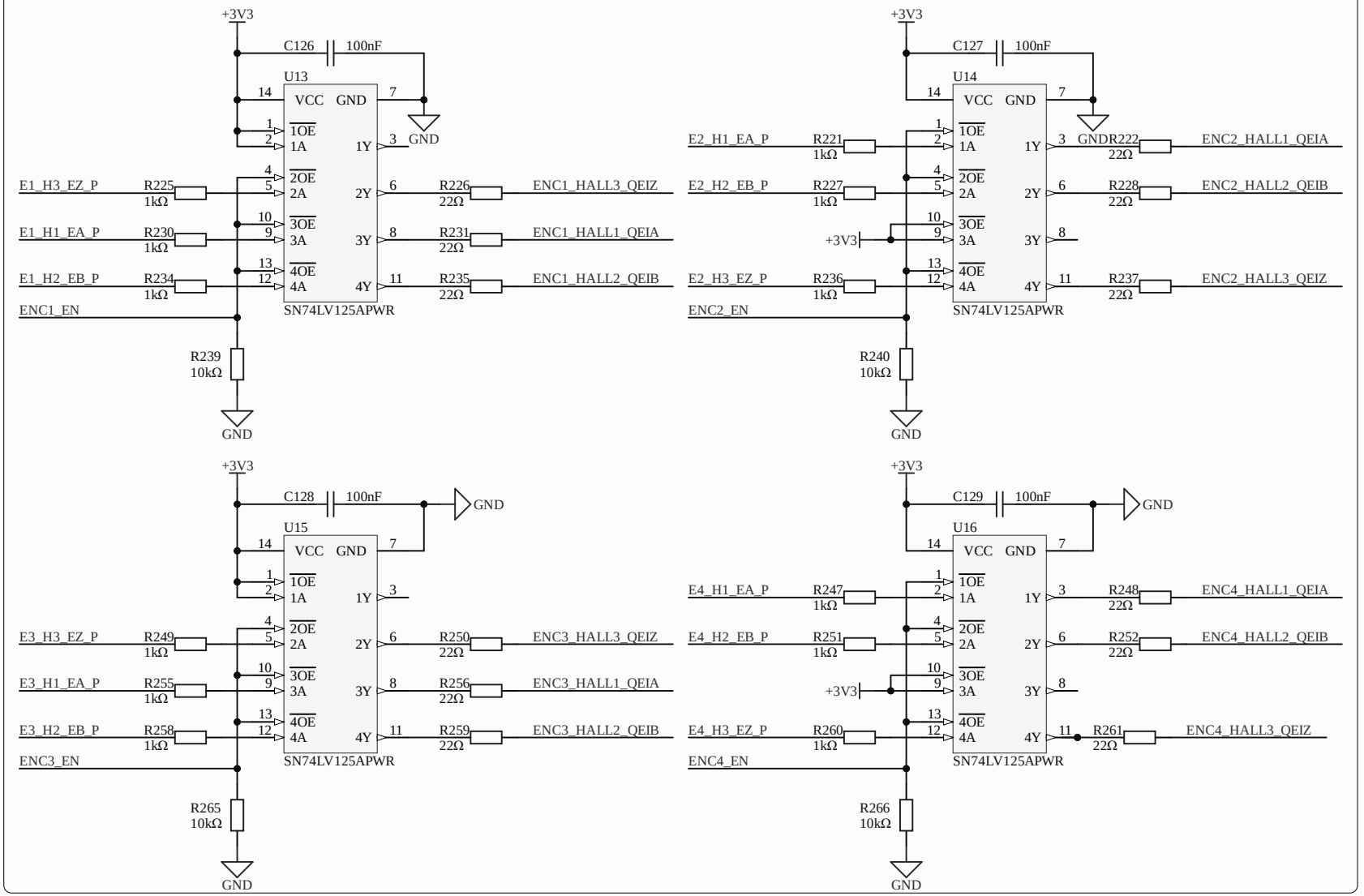
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Project	KIT_PSC3Mx_CC2		
Title	M1/M3 & M5 CONNECTOR	Variant	PSC3M6_QFP100
Drawn By: M.N.	Reviewed By: --	Approved By: --	
Size: A4	Document Name: KIT_PSC3Mx_CC2_M1_M3_M5_CONN.SchDoc	Rel.: 1.0	Rev.: --
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HALL/QEI CONNECTORS



HALL INPUTS

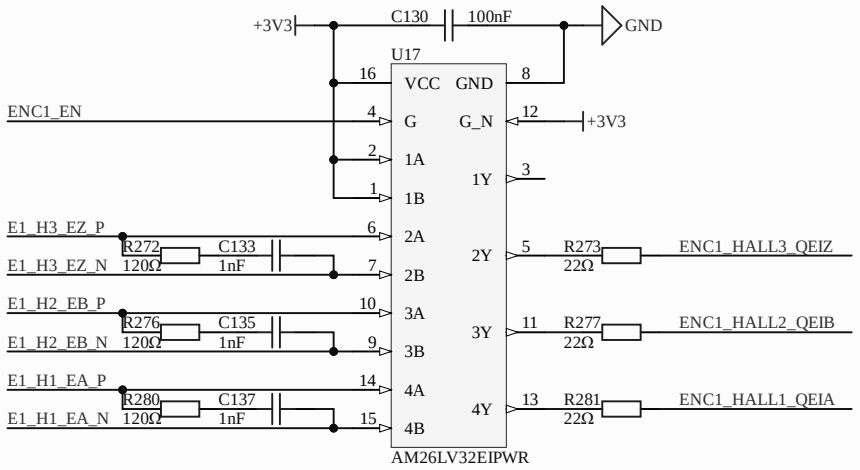


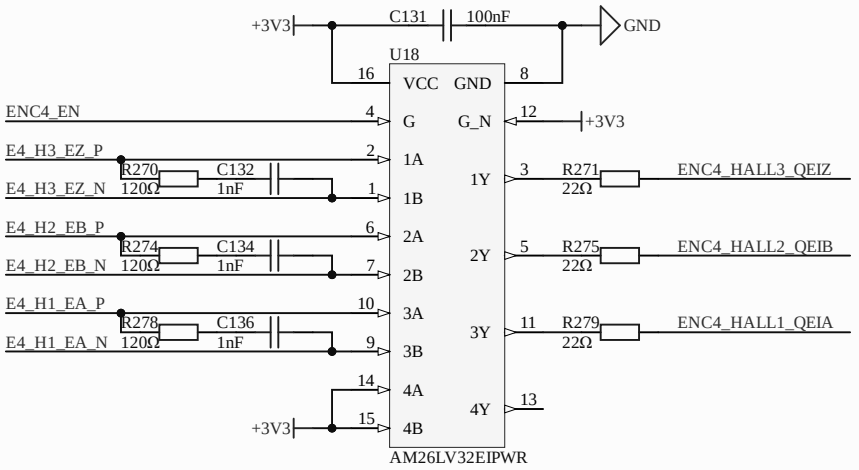
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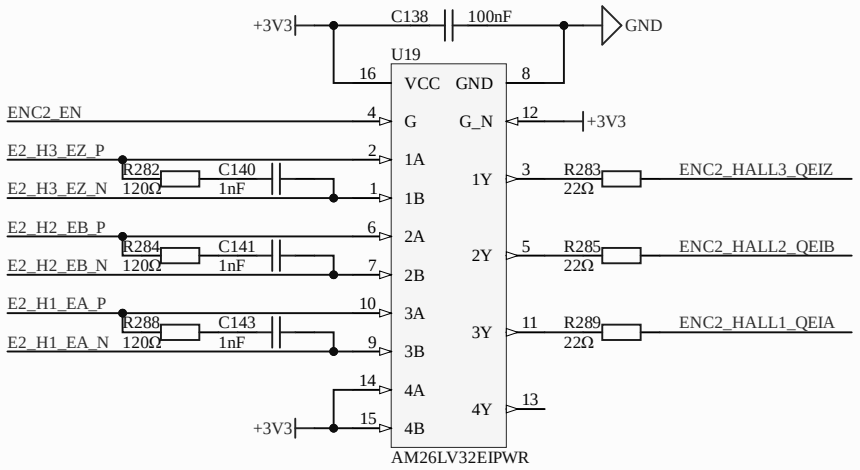
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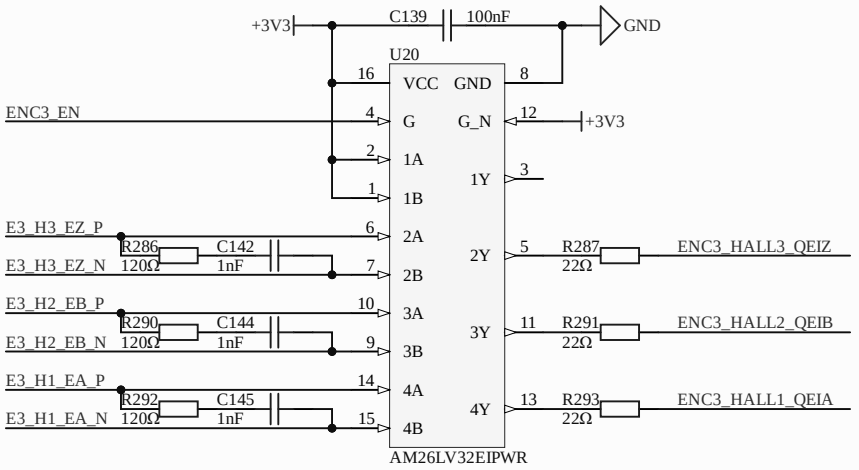
Project	KIT_PSC3Mx_CC2		
Title	HALL INTERFACE	Variant	PSC3M6_QFP100
Drawn By: M.N.	Reviewed By: --	Approved By: --	
Size: A4	Document Name: KIT_PSC3Mx_CC2_HALL.SchDoc	Rel.: 1.0	Rev.: --
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QEI INPUTS







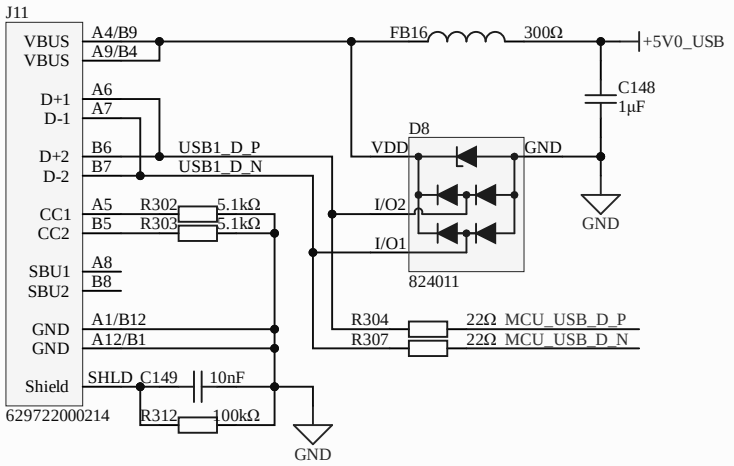


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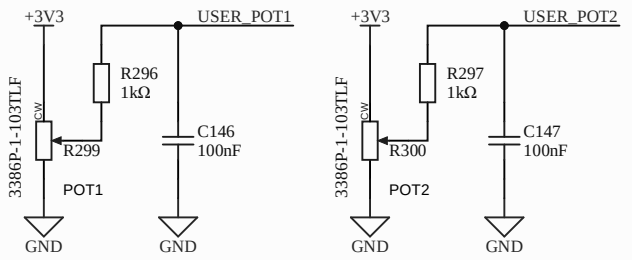
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Project	KIT_PSC3Mx_CC2		
Title	QEI INTERFACE	Variant	PSC3M6_QFP100
Drawn By: M.N.	Reviewed By: --	Approved By: --	
Size: A4	Document Name: KIT_PSC3Mx_CC2_QEI.SchDoc	Rel.: 1.0	Rev.: --
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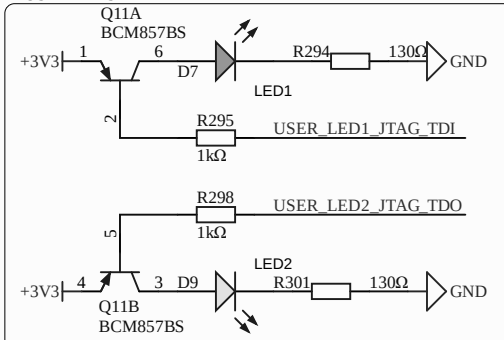
USB DEVICE INTERFACE



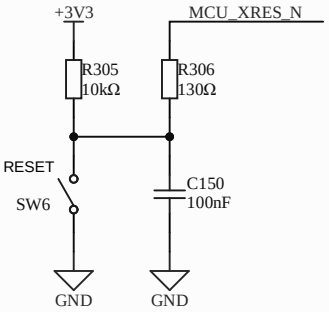
USER POTENTIOMETERS



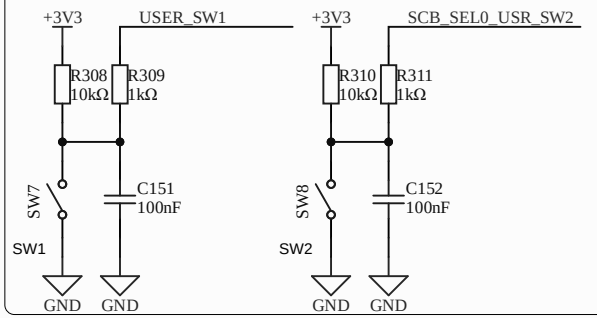
USER LEDS



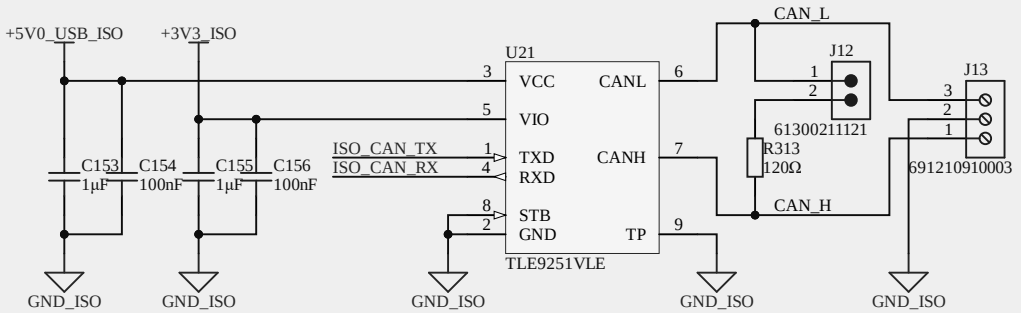
RESET SWITCH



USER SWITCHES



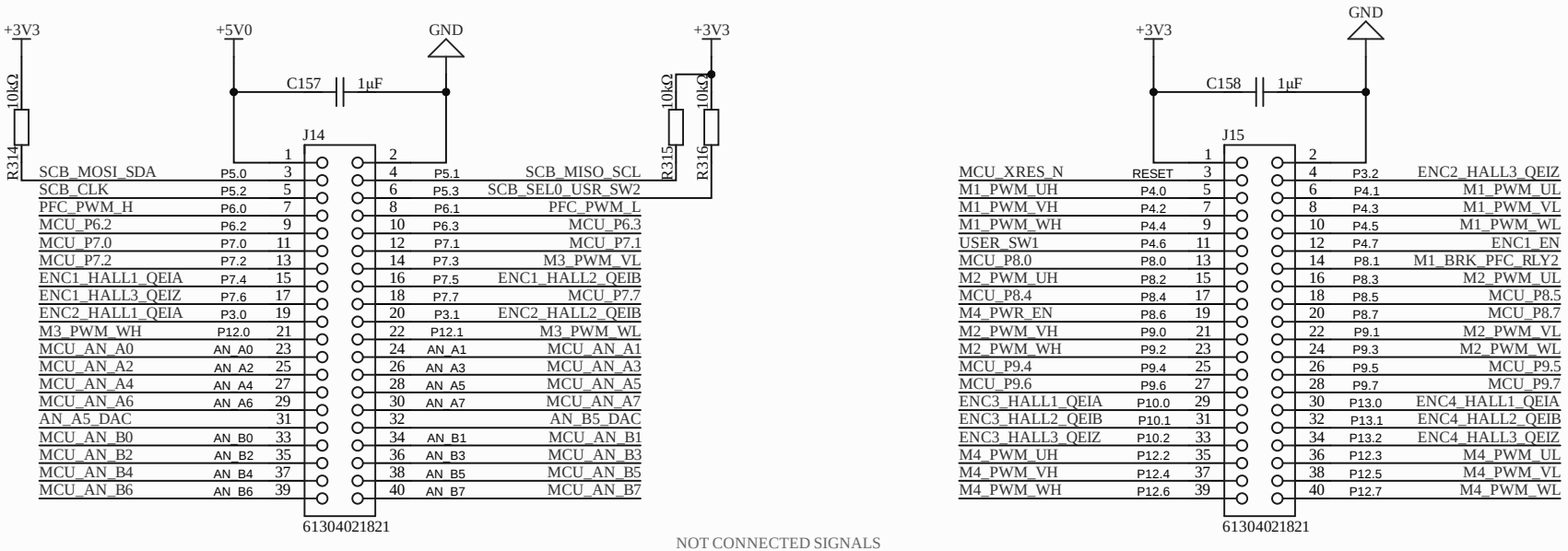
CAN FD INTERFACE - ISOLATED



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Project	KIT_PSC3Mx_CC2		
Title	USER INTERFACE	Variant	PSC3M6_QFP100
Drawn By: M.N.	Reviewed By: --	Approved By: --	
Size: A4	Document Name: KIT_PSC3Mx_CC2_USER_INTERFACE.SchDoc	Rel.: 1.0	Rev.: --
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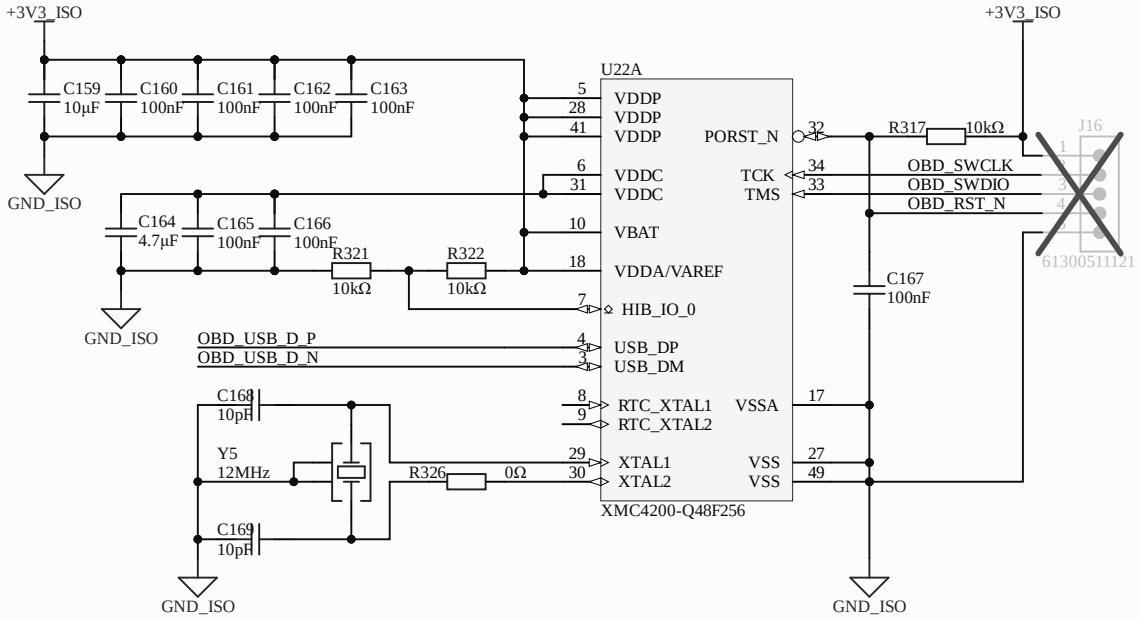


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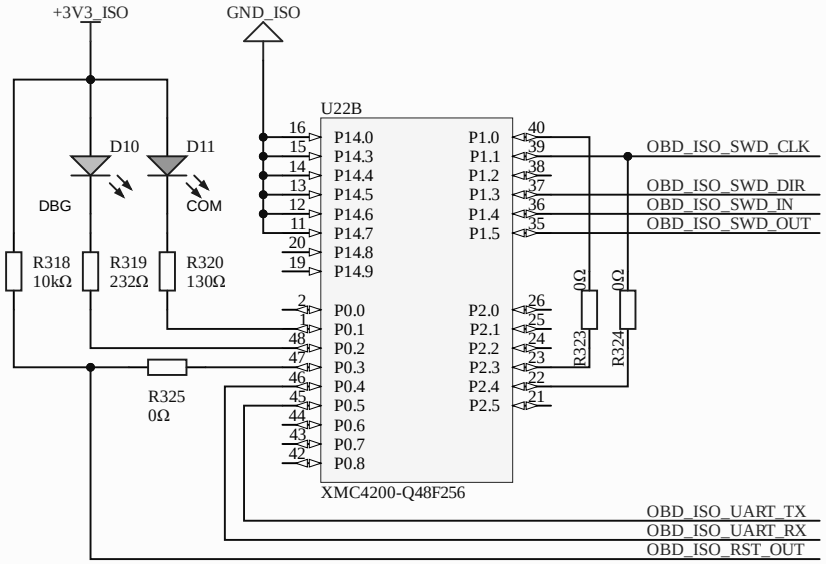
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Project	KIT_PSC3Mx_CC2		
Title	EXPANSION HEADERS	Variant	PSC3M6_QFP100
Drawn By: M.N.	Reviewed By: --	Approved By: --	
Size: A4	Document Name: KIT_PSC3Mx_CC2_EXPANSION_HEADERS.SchDoc	Rel.: 1.0	Rev.: --
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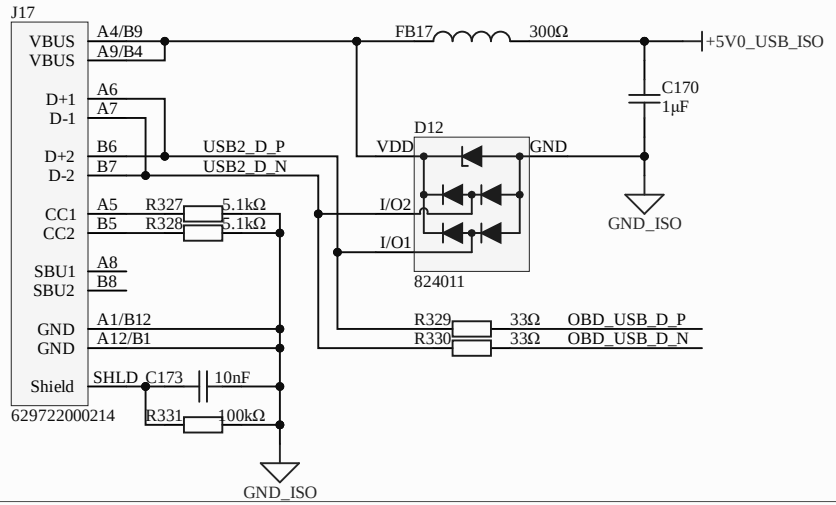
OBD XMC4200 POWER, XTAL & DEBUG



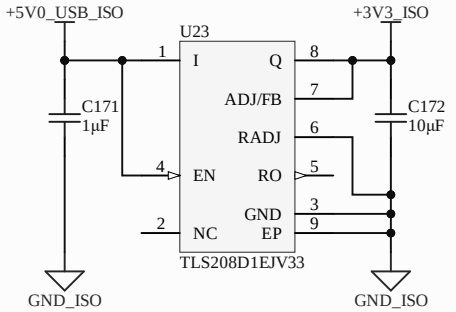
OBD XMC4200 IOs



OBD USB INTERFACE



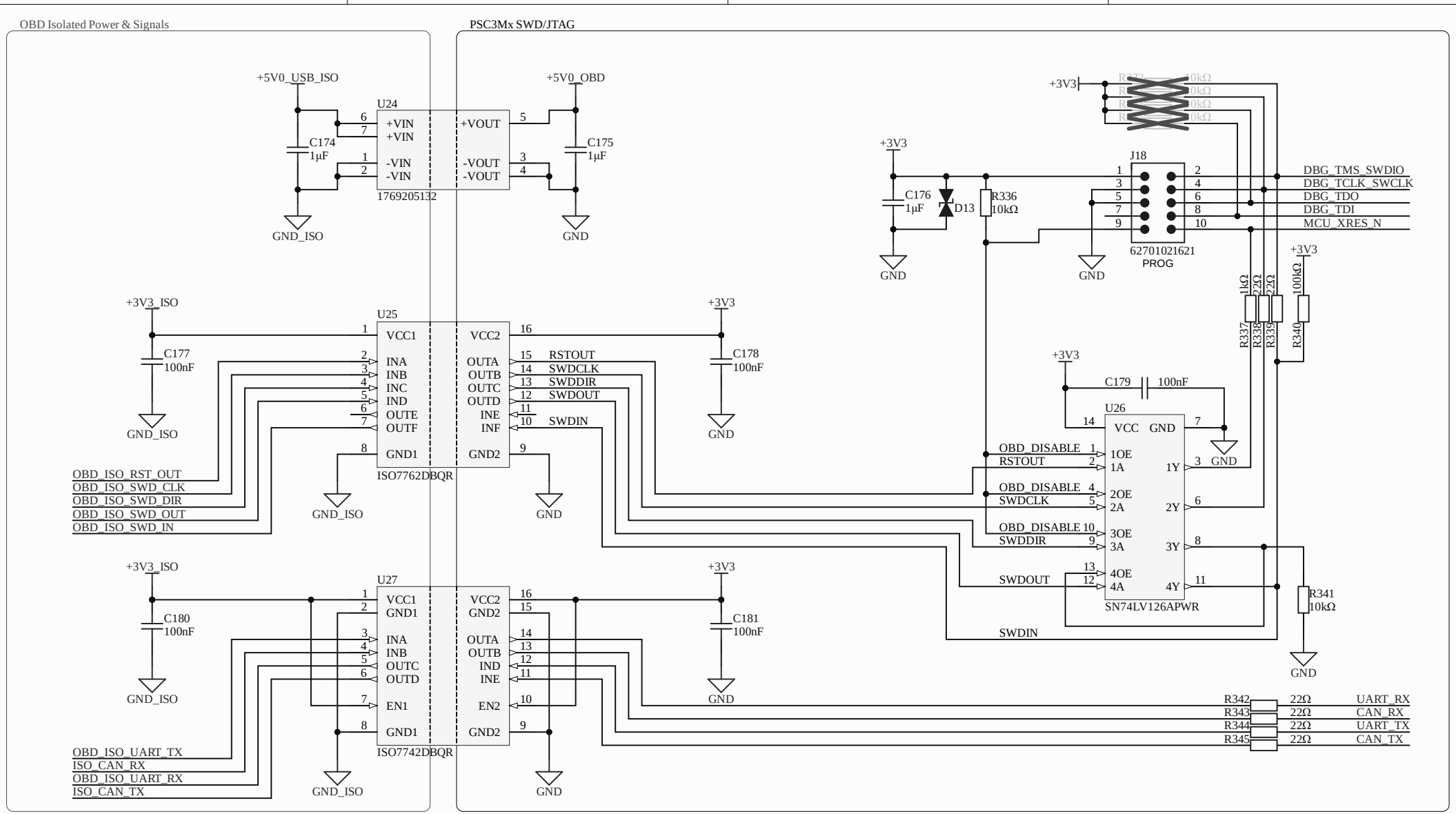
OBD 3.3V LDO



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Project	KIT_PSC3Mx_CC2		
Title	ON-BOARD DEBUGGER MCU	Variant	PSC3M6_QFP100
Drawn By: M.N.	Reviewed By: --	Approved By: --	
Size: A4	Document Name: KIT_PSC3Mx_CC2_OBD_MCU.SchDoc	Rel.: 1.0	Rev.: --
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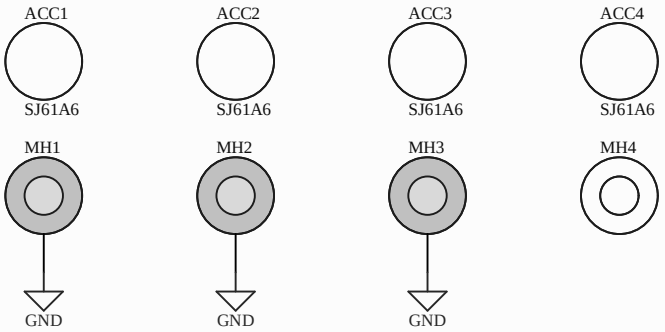


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Infineon Technologies AG
IFAG CSS ICW MCU APP IND
Am Campeon 1-15 - 85579 Neubiberg - Germany

Project	KIT_PSC3Mx_CC2		
Title	ON-BOARD DEBUGGER IOs	Variant	PSC3M6_QFP100
Drawn By: M.N.	Reviewed By: --	Approved By: --	
Size: A4	Document Name: KIT_PSC3Mx_CC2_OBD_ISO.SchDoc	Rel.: 1.0	Rev.: --
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ACCESSORIES



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Project	KIT_PSC3Mx_CC2		
Title	ACCESSORIES	Variant	PSC3M6_QFP100
Drawn By: M.N.	Reviewed By: --	Approved By: --	
Size: A4	Document Name: KIT_PSC3Mx_CC2_ACCESSORIES.SchDoc	Rel.: 1.0	Rev.: --
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PIN MAPPING

PERIP	FUNC	NAME	PSC3M6						PSC3M5						PSC3Mx			
			LQFP100		LQFP80		LQFP64		LQFP80		LQFP/VQFN64		VQFN48		LQFP/VQFN64		VQFN48	
			PIN	FUNC	PIN	FUNC	PIN	FUNC	PIN	FUNC	PIN	FUNC	PIN	FUNC	PIN	FUNC	PIN	FUNC
MOTOR 1	PWM	M1_PWM_UH	32	P4.0	27	P4.0	19	P4.0	27	P4.0	19	P4.0	15	P4.0	19	P4.0	15	P4.0
		M1_PWM_UL	33	P4.1	28	P4.1	20	P4.1	28	P4.1	20	P4.1	16	P4.1	20	P4.1	16	P4.1
		M1_PWM_VH	34	P4.2	29	P4.2	21	P4.2	29	P4.2	21	P4.2	17	P4.2	21	P4.2	17	P4.2
		M1_PWM_VL	35	P4.3	30	P4.3	22	P4.3	30	P4.3	22	P4.3	18	P4.3	22	P4.3	18	P4.3
		M1_PWM_WH	41	P4.4	31	P4.4	23	P4.4	31	P4.4	23	P4.4	19	P4.4	23	P4.4	19	P4.4
		M1_PWM_WL	42	P4.5	32	P4.5	24	P4.5	32	P4.5	24	P4.5	20	P4.5	24	P4.5	20	P4.5
	CTRL	M1_PWR_EN	15	P0.1	13	P0.1	9	P0.1	13	P0.1	9	P0.1	47	P9.0	9	P0.1	47	P9.0
		M1_ESTOP	88	P8.0	71	P8.0	59	P8.0	71	P8.0	59	P8.0	43	P8.0	59	P8.0	43	P8.0
		M1_BRAKE	89	P8.1	72	P8.1	60	P8.1	72	P8.1	60	P8.1	44	P8.1	60	P8.1	44	P8.1
	ANALOG	M1_IU	69	AN_A0	53	AN_A0	41	AN_A0	53	AN_A0	41	AN_A0	31	AN_A0	41	P10.0	31	P10.0
		M1_IV	70	AN_A1	54	AN_A1	42	AN_A1	54	AN_A1	42	AN_A1	32	AN_A1	42	P10.1	32	P10.1
		M1_IW	71	AN_A2	55	AN_A2	43	AN_A2	55	AN_A2	43	AN_A2	33	AN_A2	43	P10.2	33	P10.2
		M1_IDC	72	AN_A3	56	AN_A3	44	AN_A3	56	AN_A3	44	AN_A3	34	AN_A3	44	P10.3	34	P10.3
		M1_VDC	73	AN_A4	57	AN_A4	45	AN_A4	57	AN_A4	45	AN_A4	35	AN_A4	45	P10.4	35	P10.4
		M1_BRAKE_TEMP	86	AN_B6	69	AN_B6	57	AN_B6	69	AN_B6	57	AN_B6	38	AN_B2	57	P11.6	38	P11.2
MOTOR 2	PWM	M2_PWM_UH	90	P8.2	73	P8.2	61	P8.2	73	P8.2	61	P8.2	45	P8.2	61	P8.2	45	P8.2
		M2_PWM_UL	91	P8.3	74	P8.3	62	P8.3	74	P8.3	62	P8.3	46	P8.3	62	P8.3	46	P8.3
		M2_PWM_VH	96	P9.0	79	P9.0	63	P9.0	79	P9.0	63	P9.0	26	P6.2	63	P9.0	26	P6.2
		M2_PWM_VL	1	P9.1	1	P9.1	1	P9.1	1	P9.1	1	P9.1	27	P6.3	1	P9.1	27	P6.3
		M2_PWM_WH	2	P9.2	2	P9.2	2	P9.2	2	P9.2	2	P9.2	1	P9.2	2	P9.2	1	P9.2
		M2_PWM_WL	3	P9.3	3	P9.3	3	P9.3	3	P9.3	3	P9.3	2	P9.3	3	P9.3	2	P9.3
	CTRL	M2_PWR_EN	7	P9.7	7	P9.7	--	--	7	P9.7	--	--	--	--	--	--	--	--
		M2_ESTOP	68	P7.7	52	P7.7	--	--	52	P7.7	--	--	--	--	--	--	--	--
	ANALOG	M2_IU	74	AN_A5	58	AN_A5	46	AN_A5	58	AN_A5	46	AN_A5	36	AN_A5	46	P10.5	36	P10.5
		M2_IV	75	AN_A6	59	AN_A6	47	AN_A6	59	AN_A6	47	AN_A6	--	--	47	P10.6	--	--
		M2_IW	76	AN_A7	60	AN_A7	48	AN_A7	60	AN_A7	48	AN_A7	--	--	48	P10.7	--	--
		M2_IDC	80	AN_B3	64	AN_B3	52	AN_B3	64	AN_B3	52	AN_B3	39	AN_B3	52	P11.3	39	P11.3
MOTOR 3	PWM	M3_PWM_UH	60	P7.0	44	P7.0	36	P7.0*	44	P7.0	36	P7.0*	--	--	36	P7.0*	--	--
		M3_PWM_UL	61	P7.1	45	P7.1	37	P7.1*	45	P7.1	37	P7.1*	--	--	37	P7.1*	--	--
		M3_PWM_VH	62	P7.2	46	P7.2	38	P7.2*	46	P7.2	38	P7.2*	--	--	38	P7.2*	--	--
		M3_PWM_VL	63	P7.3	47	P7.3	39	P7.3	47	P7.3	39	P7.3	--	--	39	P7.3	--	--
		M3_PWM_WH	36	P12.0	42	P6.2*	34	P6.2*	42	P6.2*	34	P6.2*	--	--	34	P6.2	--	--
		M3_PWM_WL	37	P12.1	43	P6.3*	35	P6.3*	43	P6.3*	35	P6.3*	--	--	35	P6.3	--	--
	CTRL	M3_PWR_EN	6	P9.6	6	P9.6	--	--	6	P9.6	--	--	--	--	--	--	--	--
		M3_ESTOP	31	P3.3	26	P3.3	--	--	26	P3.3	--	--	--	--	--	--	--	--
	ANALOG	M3_IU	77	AN_B0	61	AN_B0	49	AN_B0	61	AN_B0	49	AN_B0	--	--	49	P11.0	--	--
		M3_IV	78	AN_B1	62	AN_B1	50	AN_B1	62	AN_B1	50	AN_B1	--	--	50	P11.1	--	--
		M3_IW	79	AN_B2	63	AN_B2	51	AN_B2	63	AN_B2	51	AN_B2	--	--	51	P11.2	--	--
		M3_IDC	87	AN_B7	70	AN_B7	58	AN_B7	70	AN_B7	58	AN_B7	--	--	58	P11.7	--	--
MOTOR 4	PWM	M4_PWM_UH	54	P12.2	--	--	--	--	--	--	--	--	--	--	--	--	--	--
		M4_PWM_UL	55	P12.3	--	--	--	--	--	--	--	--	--	--	--	--	--	--
		M4_PWM_VH	56	P12.4	--	--	--	--	--	--	--	--	--	--	--	--	--	--
		M4_PWM_VL	57	P12.5	--	--	--	--	--	--	--	--	--	--	--	--	--	--
		M4_PWM_WH	58	P12.6	--	--	--	--	--	--	--	--	--	--	--	--	--	--
		M4_PWM_WL	59	P12.7	--	--	--	--	--	--	--	--	--	--	--	--	--	--
	CTRL	M4_PWR_EN	94	P8.6	94	(P8.6)	--	--	--	--	--	--	--	--	--	--	--	--
		M4_ESTOP	95	P8.7	95	(P8.7)	--	--	--	--	--	--	--	--	--	--	--	--
	ANALOG	M4_IU	84	AN_B4	--	--	--	--	--	--	--	--	--	--	--	--	--	--
		M4_IV	85	AN_B5	--	--	--	--	--	--	--	--	--	--	--	--	--	--
		M4_IW	87	AN_B7	--	--	--	--	--	--	--	--	--	--	--	--	--	--
		M4_IDC	84	AN_B4	--	--	--	--	--	--	--	--	--	--	--	--	--	--
PFC	PWM	PFC_PWM_UH	49	P6.0	39	P6.0	31	P6.0	39	P6.0	31	P6.0	23	P6.0	31	P6.0	23	P6.0
		PFC_PWM_UL	51	P6.1	41	P6.1	33	P6.1	41	P6.1	33	P6.1	25	P6.1	33	P6.1	25	P6.1
	CTRL	PFC_RELAY1	14	P0.0	12	P0.0	8	P0.0	12	P0.0	8	P0.0	7	P0.0	8	P0.0	7	P0.0
		PFC_RELAY2	89	P8.1	72	P8.1	60	P8.1	72	P8.1	60	P8.1	44	P8.1	60	P8.1	44	P8.1
		PFC_OCP	31	P3.3	26	P3.3	--	--	26	P3.3	--	--	--	--	--	--	--	--
		PFC_VDC	87	AN_B7	70	AN_B7	58	AN_B7	70	AN_B7	58	AN_B7	--	--	58	P11.7	--	--
	ANALOG	PFC_I1	74	AN_A5	58	AN_A5	46	AN_A5	58	AN_A5	46	AN_A5	36	AN_A5	46	P10.5	36	P10.5
		PFC_I2	75	AN_A6	59	AN_A6	47	AN_A6	59	AN_A6	47	AN_A6	--	--	47	P10.6	--	--
		PFC_NTC	86	AN_B6	69	AN_B6	57	AN_B6	69	AN_B6	57	AN_B6	38	AN_B2	57	P11.6	38	P11.2

PERIP	FUNC	NAME	PSC3M6						PSC3M5						PSC3Mx			
			LQFP100		LQFP80		LQFP64		LQFP80		LQFP/VQFN64		VQFN48		LQFP/VQFN64		VQFN48	
			PIN	FUNC	PIN	FUNC	PIN	FUNC	PIN	FUNC	PIN	FUNC	PIN	FUNC	PIN	FUNC	PIN	FUNC
ENC. 1	IOs	ENC1_HALL1_QEIA	64	P7.4	48	P7.4	36	P7.0*	48	P7.4	36	P7.0*	28	P7.0	36	P7.0*	28	P7.1
		ENC1_HALL2_QEIB	66	P7.5	50	P7.5	37	P7.1*	50	P7.5	37	P7.1*	29	P7.1	37	P7.1*	29	P7.3
		ENC1_HALL3_QEIZ	67	P7.6	51	P7.6	38	P7.2*	51	P7.6	38	P7.2*	30	P7.2	38	P7.2*	6	P0.2
		ENC1_EN	44	P4.7	34	P4.7	26	P4.7	34	P4.7	26	P4.7	22	P4.7	26	P4.7	22	P4.7
ENC. 2	IOs	ENC2_HALL1_QEIA	28	P3.0	23	P3.0	--	--	23	P3.0	--	--	--	--	--	--	--	--
		ENC2_HALL2_QEIB	29	P3.1	24	P3.1	--	--	24	P3.1	--	--	--	--	--	--	--	--
		ENC2_HALL3_QEIZ	30	P3.2	25	P3.2	--	--	25	P3.2	--	--	--	--	--	--	--	--
		ENC2_EN	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--
ENC. 3	IOs	ENC3_HALL1_QEIA	16	P10.0	--	--	--	--	--	--	--	--	--	--	--	--	--	--
		ENC3_HALL2_QEIB	17	P10.1	--	--	--	--	--	--	--	--	--	--	--	--	--	--
		ENC3_HALL3_QEIZ	18	P10.2	--	--	--	--	--	--	--	--	--	--	--	--	--	--
		ENC3_EN	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--
ENC. 4	IOs	ENC4_HALL1_QEIA	97	P13.0	--	--	--	--	--	--	--	--	--	--	--	--	--	--
		ENC4_HALL2_QEIB	98	P13.1	--	--	--	--	--	--	--	--	--	--	--	--	--	--
		ENC4_HALL3_QEIZ	99	P13.2	--	--	--	--	--	--	--	--	--	--	--	--	--	--
		ENC4_EN	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--
SCB	IOs	SCB_MOSI_SDA	45	P5.0	35	P5.0	27	P5.0	35	P5.0	27	P5.0	--	--	35	P5.0	--	--
		SCB_MISO_SCL	46	P5.1	36	P5.1	28	P5.1	36	P5.1	28	P5.1	--	--	36	P5.1	--	--
		SCB_CLK	47	P5.2	37	P5.2	29	P5.2	37	P5.2	29	P5.2	--	--	37	P5.2	--	--
		SCB_SEL0	48	P5.3*	38	P5.3*	30	P5.3*	38	P5.3*	30	P5.3*	--	--	38	P5.3*	--	--
DFU	IOs	DFU1	26	P2.2*	21	P2.2*	17	P2.2*	21	P2.2*	17	P2.2*	13	P2.2*	17	P2.2*	13	P2.2*
		DFU2	27	P2.3*	22	P2.3*	18	P2.3*	22	P2.3*	18	P2.3*	14	P2.3*	18	P2.3*	14	P2.3*
UART	IOs	UART_RX	26	P2.2*	21	P2.2*	17	P2.2*	21	P2.2*	17	P2.2*	13	P2.2*	17	P2.2*	13	P2.2*
		UART_TX	27	P2.3*	22	P2.3*	18	P2.3*	22	P2.3*	18	P2.3*	14	P2.3*	18	P2.3*	14	P2.3*
CAN	IOs	CAN_RX	52	P6.2	42	P6.2*	34	P6.2*	42	P6.2*	34	P6.2*	--	--	--	--	--	--
		CAN_TX	53	P6.3	43	P6.3*	35	P6.3*	43	P6.3*	35	P6.3*	--	--	--	--	--	--
USB	IOs	USB_DM	39	P11.0	--	--	--	--	--	--	--	--	--	--	--	--	--	--
		USB_DP	40	P11.1	--	--	--	--	--	--	--	--	--	--	--	--	--	--
POT.	IOs	USER_POT1	92	P8.4	67	AN_B4	55	AN_B4	67	AN_B4	55	AN_B4	42	AN_B4	55	P11.4	42	P11.4
		USER_POT2	93	P8.5	68	AN_B5	56	AN_B5	68	AN_B5	56	AN_B5	--	--	56	P11.5	--	--
SWITCH	IOs	RESET	8	XRES	8	XRES	4	XRES	8	XRES	4	XRES	3	XRES	4	XRES	3	XRES
		USER_SW1	43	P4.6	33	P4.6	25	P4.6	33	P4.6	25	P4.6	21	P4.6	25	P4.6	21	P4.6
		USER_SW2	48	P5.3*	38	P5.3*	30	P5.3*	38	P5.3*	30	P5.3*	--	--	30	P5.3*	--	--
LED	IOs	USER_LED1	23	P2.0*	18	P2.0*	14	P2.0*	18	P2.0*	14	P2.0*	10	P2.0*	14	P2.0*	10	P2.0*
		USER_LED2	24	P2.1*	19	P2.1*	15	P2.1*	19	P2.1*	15	P2.1*	11	P2.1*	15	P2.1*	11	P2.1*
XTAL	WCO	WCO_OUT	14	P0.0*	12	P0.0*	8	P0.0*	12	P0.0*	8	P0.0*	--	--	8	P0.0*	--	--
		WCO_IN	15	P0.1*	13	P0.1*	9	P0.1*	13	P0.1*	9	P0.1*	--	--	9	P0.1*	--	--
	ECO	ECO_IN	19	P1.0	14	P1.0	10	P1.0	14	P1.0	10	P1.0	--	--	10	P1.0	--	--
		ECO_OUT	20	P1.1	15	P1.1	11	P1.1	15	P1.1	11	P1.1	--	--	11	P1.1	--	--
SWD/JTAG	IOs	TCLK_SWCLK	21	P1.2	16	P1.2	12	P1.2	16	P1.2	12	P1.2	8	P1.2	12	P1.2	8	P1.2
		TMS_SWDIO	22	P1.3	17	P1.3	13	P1.3	17	P1.3	13	P1.3	9	P1.3	13	P1.3	9	P1.3
		TDI	23	P2.0*	18	P2.0*	14	P2.0*	18	P2.0*	14	P2.0*	10	P2.0*	14	P2.0*	10	P2.0*
		TDO	24	P2.1*	19	P2.1*	15	P2.1*	19	P2.1*	15	P2.1*	11	P2.1*	15	P2.1*	11	P2.1*

NC - NOT CONNECTED
Px.y*, AN_xy* - SHARED
(Px.y) - NOT USED

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KIT_PSC3Mx_CC2

VERSION HISTORY

VER	DESCRIPTION	BY	DATE
1.0	Initial version 1.0 of KIT_PSC3Mx_CC2 board.	M.N.	20.05.2026

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