PSOC CONTROL C3M5 INTERFACE BOARD

CONTENTS

PAGE	DESCRIPTION
01	Title, Table of Contents & Drawing Numbers
02	Connectors
03	Revision history

Drawing Numbers

PCBA	121-60702-01
PCB	600-60702-01
FAB DRW	610-60702-01
ASSY DRW	620-60702-01
SCH DRW	630-60702-01

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SCH Title : PSOC CONTROL C3M5 Interface board

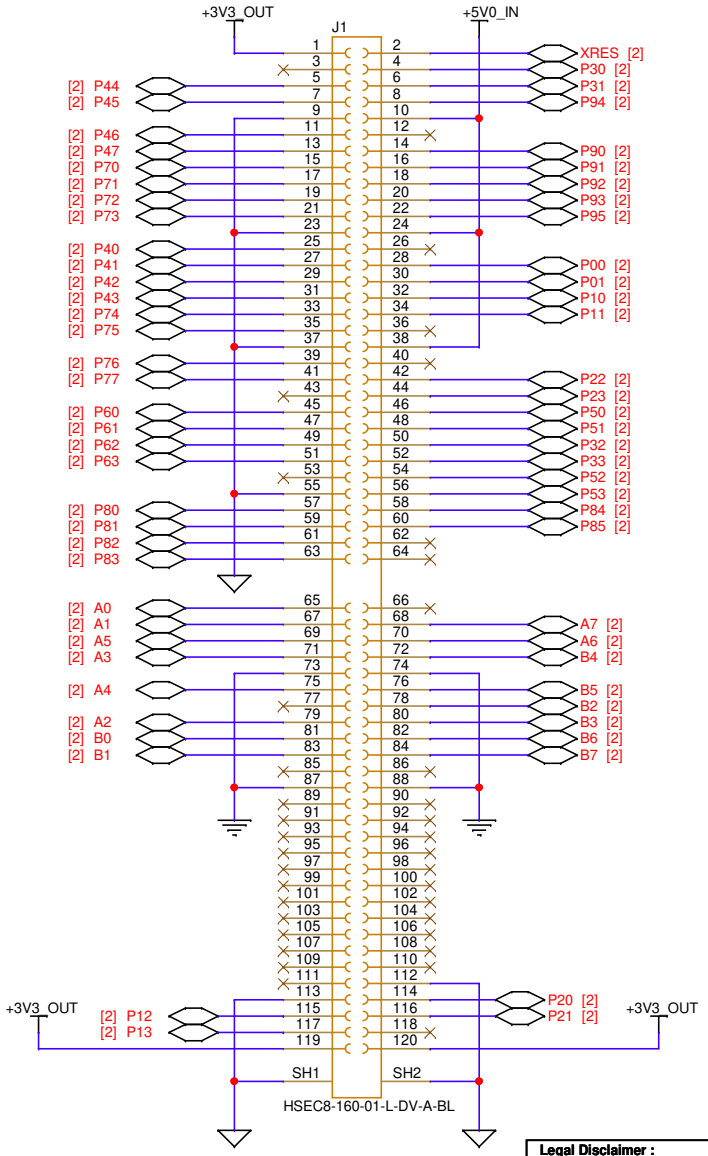
Page Title: Title page

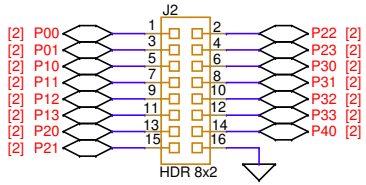
Size	Document Number	Drawn By	Approved By	Rev
A4	630-60702-01	GURRAL	RKAD	03

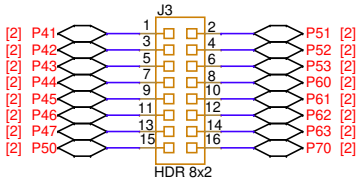
Date: Monday, September 02, 2024 Sheet 1 of 3

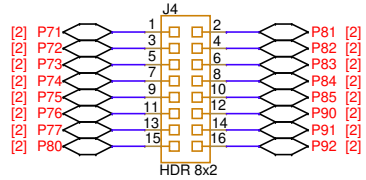
CONNECTORS

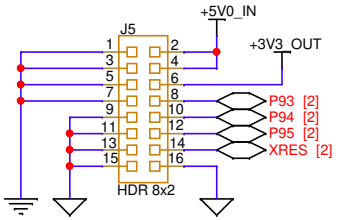
120PIN CONNECTOR

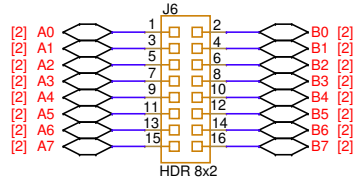


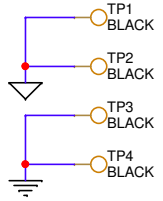


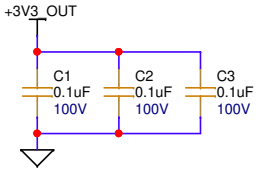


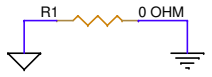


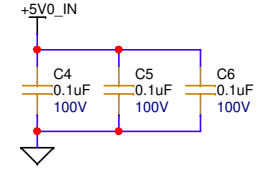


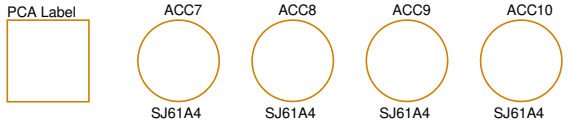












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SCH Title : **PSOC CONTROL C3M5 Interface board**				
Page Title: **Connectors**				
Size	Document Number	Drawn By	Approved By	Rev
A4	**630-60702-01**	**GURRAL**	**RKAD**	**03**
Date: Monday, September 02, 2024		Sheet **2** of **3**		

REVISION HISTORY

REV	DESCRIPTION OF CHANGE	DATE
0.1	Initial Draft	07-11-2023
1.1	Updated IOs	17-12-2023
2.1	Updated IOs	31-05-2024

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A4	630-60702-01	GURRAL	RKAD	03

Date: Monday, September 02, 2024 Sheet 3 of 3