CY8CPROTO-063-BLE PSoC 6 BLE Prototyping Kit

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PCB	600-60527-01
FAB DRW	610-60527-01
ASSY DRW	620-60527-01
SCH DRW	630-60527-01

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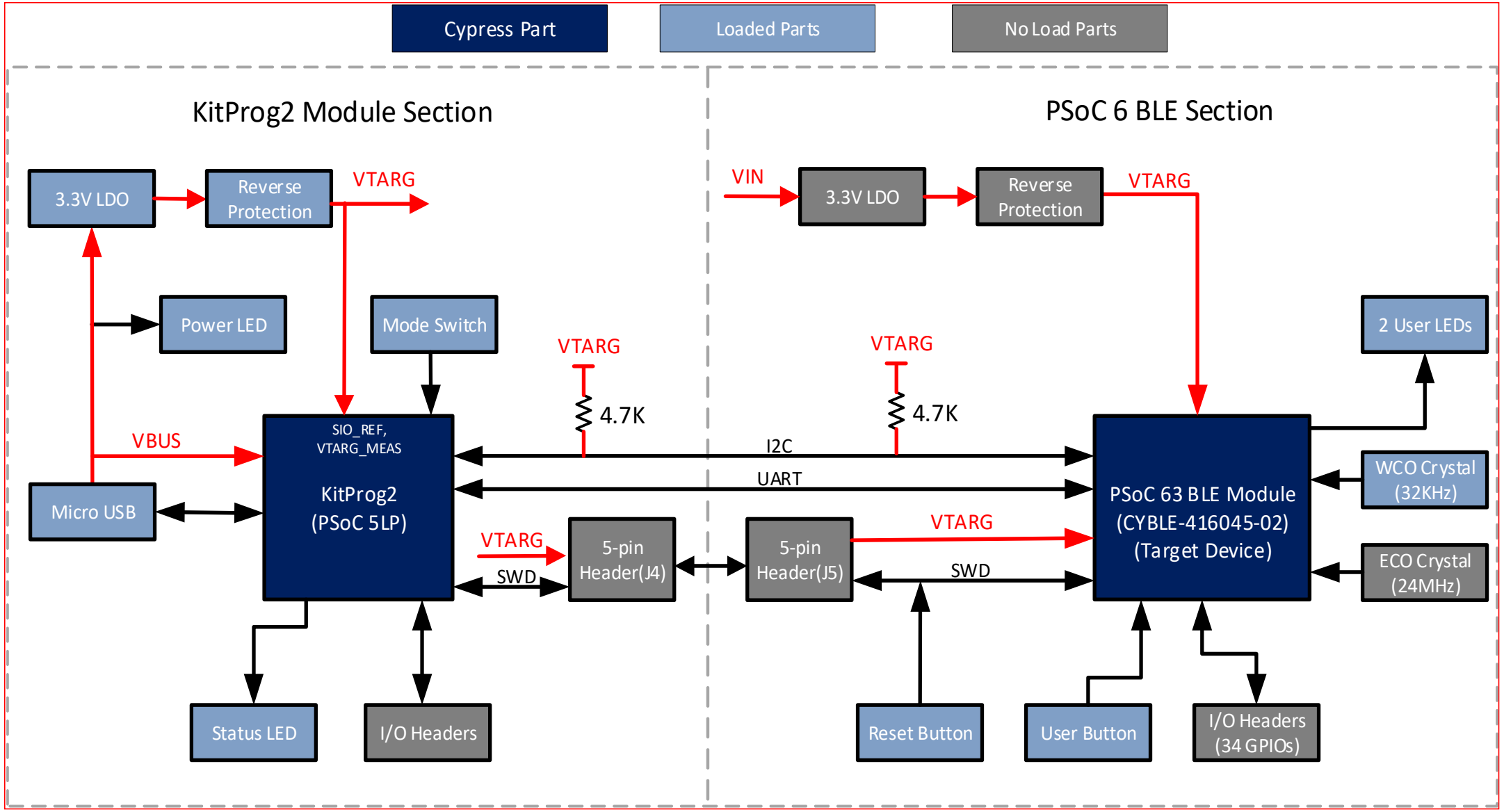
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SCH Title : PSoC 6 BLE Prototyping Kit

Page Title : Title Page

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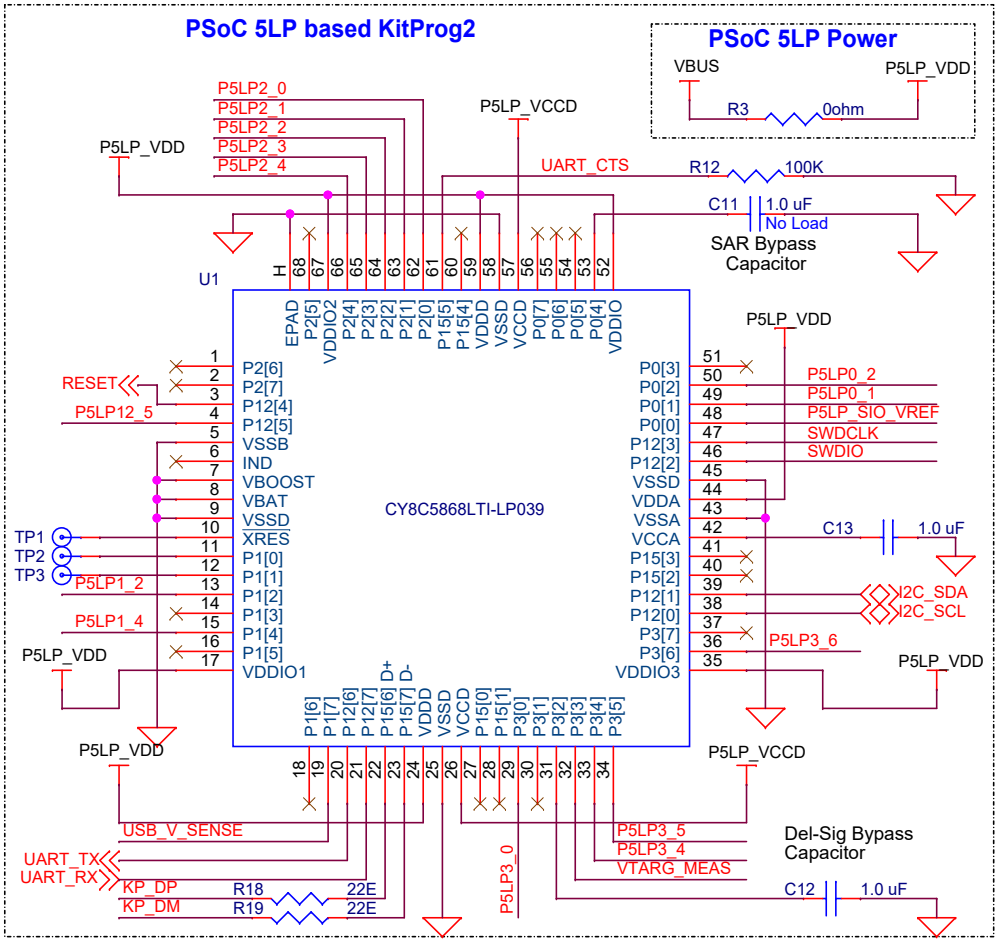
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Page Title : Block Diagram

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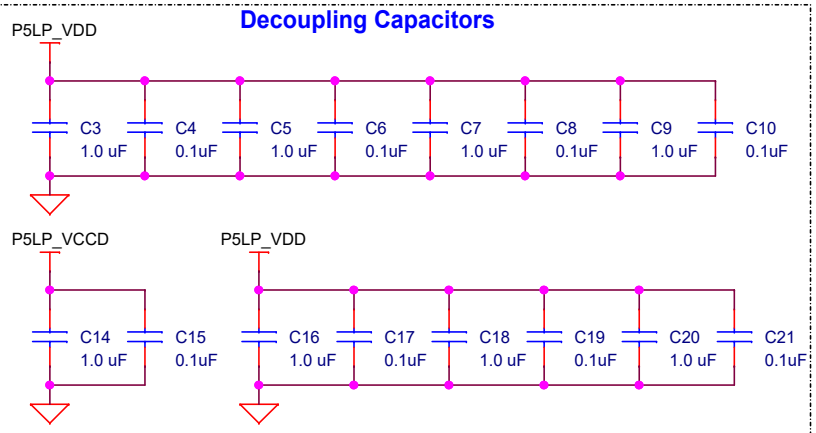
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PSoC 5LP based KitProg2

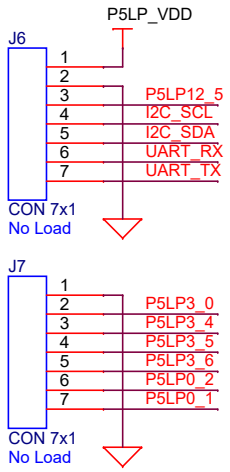


PSoC 5LP Power

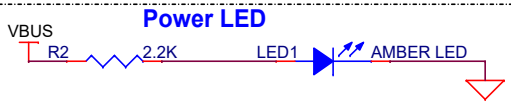
Decoupling Capacitors



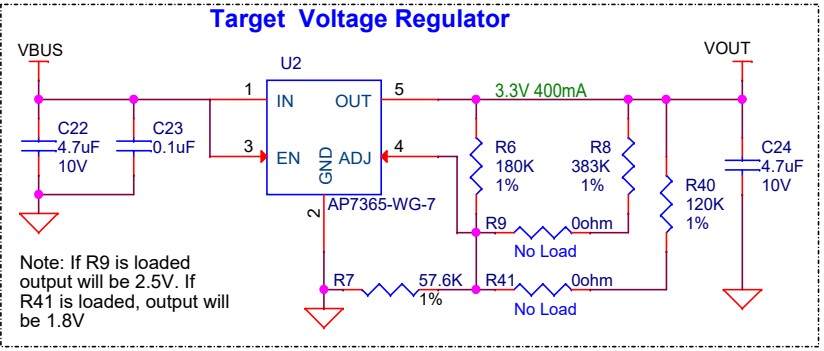
KitProg2 I/O Connectors



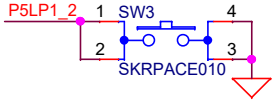
Power LED



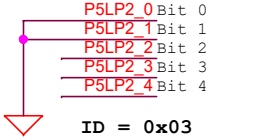
Target Voltage Regulator



Mode Switch

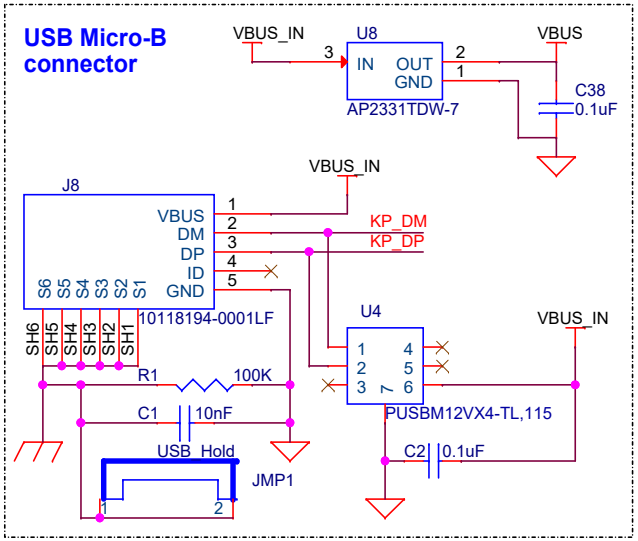


KitProg2 HW Revision

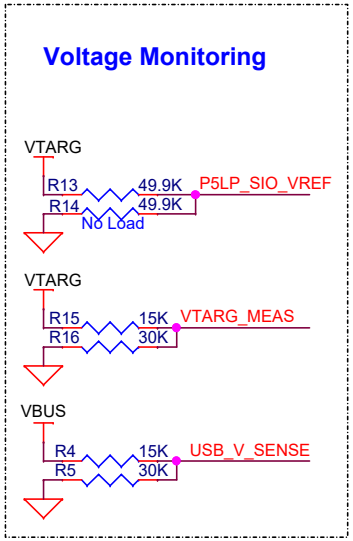


Note: GND is read as binary "1" and floating as "0"

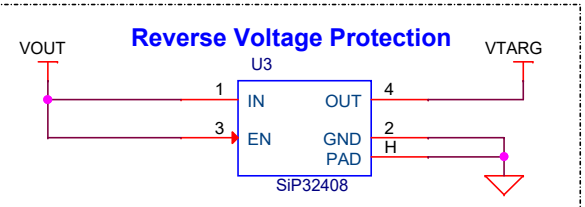
USB Micro-B connector



Voltage Monitoring



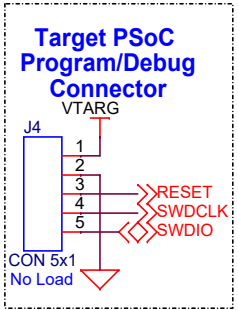
Reverse Voltage Protection



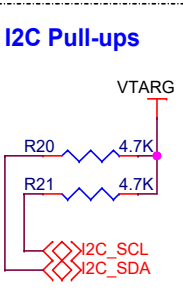
KitProg2 Status LED



Target PSoC Program/Debug Connector



I2C Pull-ups

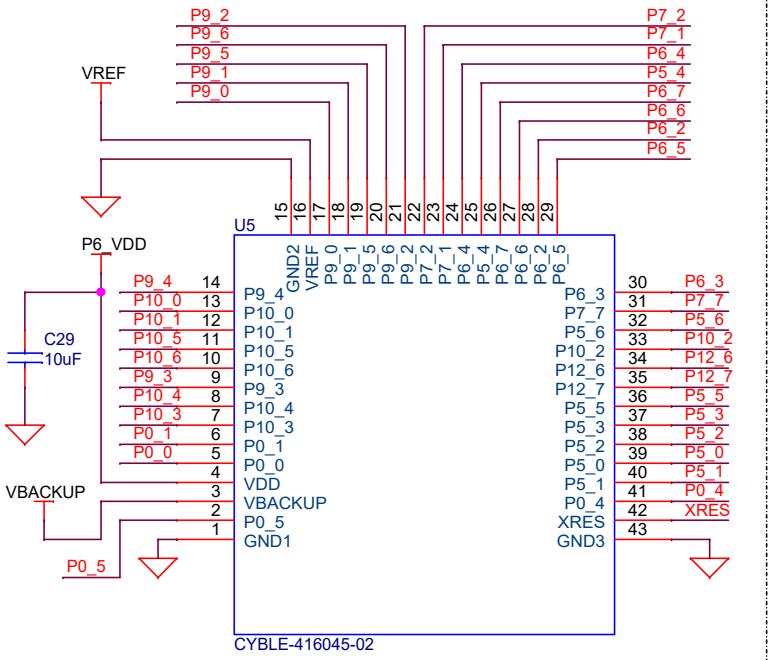


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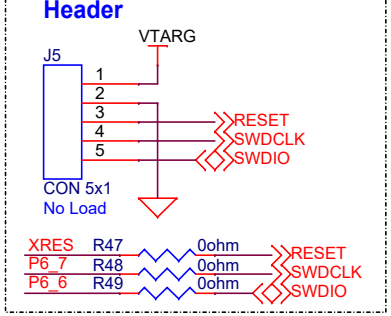
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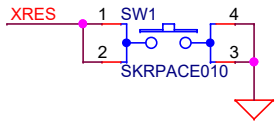
PSoC 63 BLE Module



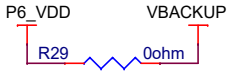
Programming Header



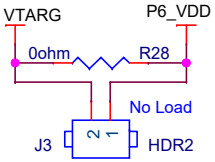
Reset Button



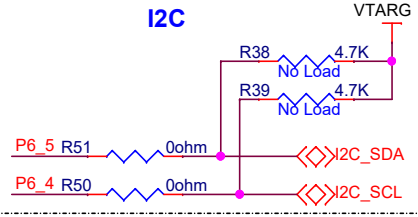
VBACKUP



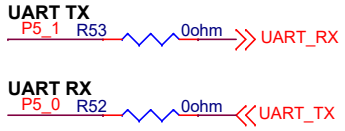
Current Measurement Header



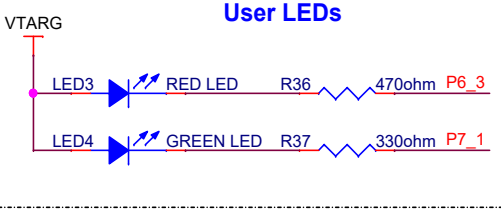
I2C



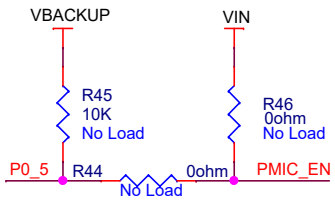
UART



User LEDs



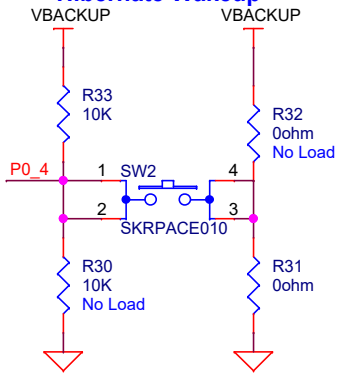
PMIC Control



Note: To control regulator output, remove R29 and load R44, R45. Make sure VBACKUP>1.6V

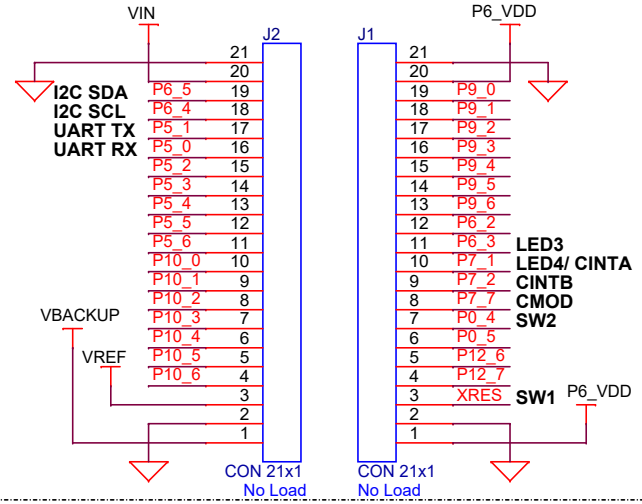
Load R46 to enable output of PSoC 6 regulator by default

User Button / Hibernate Wakeup

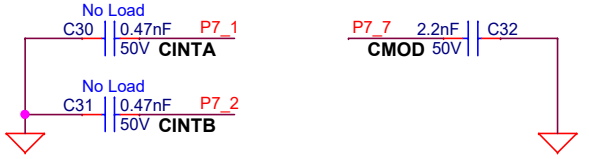


PSoC 63 BLE I/O Headers

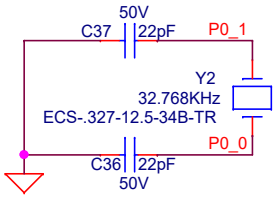
Note: Voltage above 3.6V on VTARG/P6_VDD will permanently damage PSoC 6



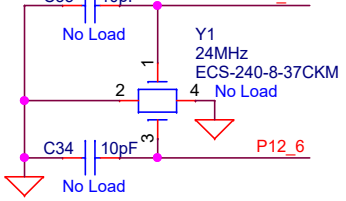
CapSense Capacitors



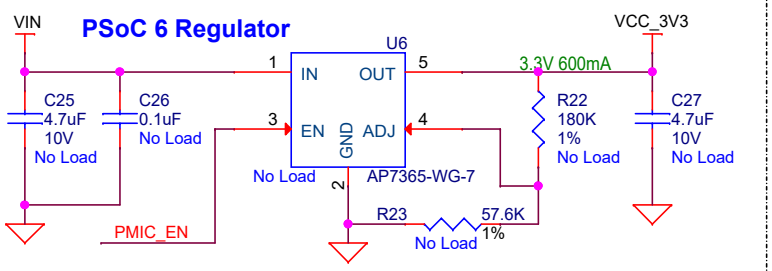
WCO



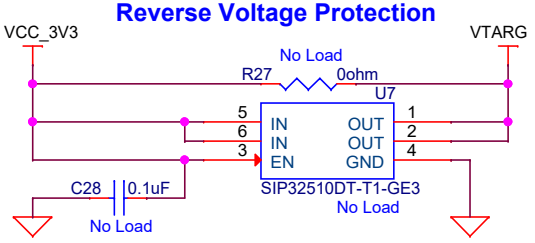
ECO



PSoC 6 Regulator



Reverse Voltage Protection



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Accessories

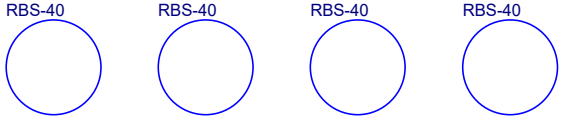
PCBA label

LBL PCA Label



Cylindrical Bumpons

RBS-40 RBS-40 RBS-40 RBS-40



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REVISION HISTORY

REV	DESCRIPTION OF CHANGE	DATE
01	Initial Proto Release	2018/06/13
02	Internal Release	2018/08/17
03	Initial Release	2018/09/26
04	Updated both WCO load caps C36,C37 to 22pF	2020/01/16

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