CY8CMOD-062S2-43012
PSoC 6 (2M) with CYW43012 Carrier Module

CONTENTS

PAGE	DESCRIPTION
01	Title, Table of Contents & Drawing Numbers
02	Block Diagram
03	PSoC 6 MCU Power
04	PSoC 6 MCU Signals
05	LBEE59B1LV Module Power
06	LBEE59B1LV Module Signals
07	Interface to BaseBoard
08	Revision History

Drawing Numbers

PCBA	121-60553-01
PCB	600-60553-01
FAB DRW	610-60553-01
ASSY DRW	620-60553-01
SCH DRW	630-60553-01

CYPRESS SEMICONDUCTOR
198 CHAMPION COURT
SAN JOSE, CA 95134
(408) 943-2600

CYPRESS SEMICONDUCTOR © 2020

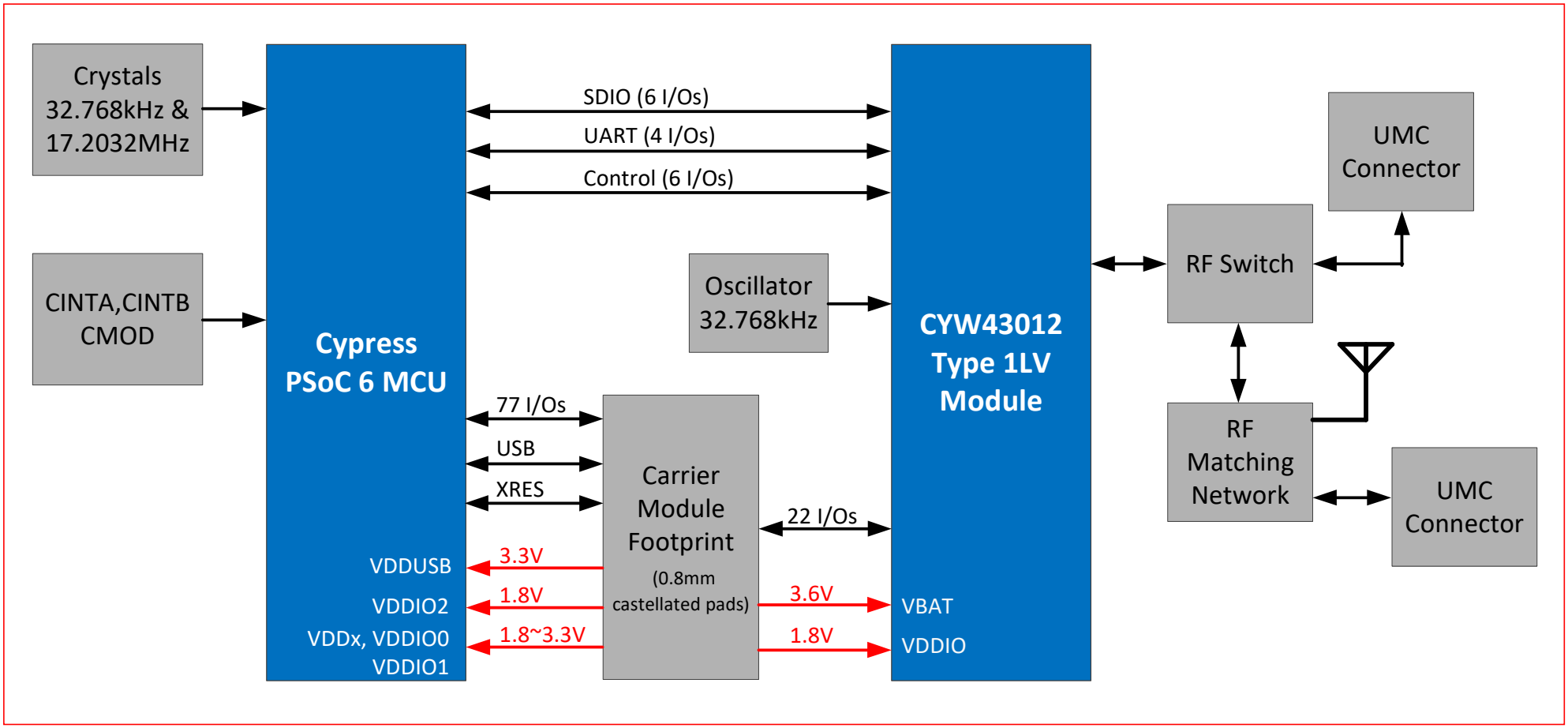
SCH Title : PSoC 6 (2M) with CYW43012 Carrier Module

Page Title : Title Page

Size	Document Number	Drawn By	Approved By	Rev
A4	630-60553-01	AARA	RKAD	03

Date: Friday, January 31, 2020 Sheet 1 of 8

Block Diagram



CYPRESS SEMICONDUCTOR
198 CHAMPION COURT
SAN JOSE, CA 95134
(408) 943-2600

CYPRESS SEMICONDUCTOR © 2020

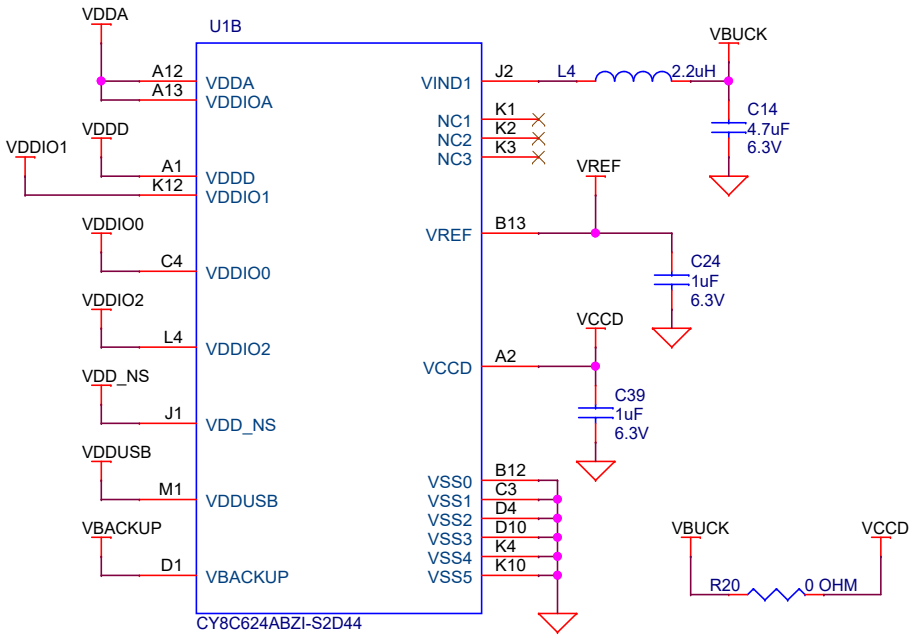
SCH Title : PSoC 6 (2M) with CYW43012 Carrier Module

Page Title : Block Diagram

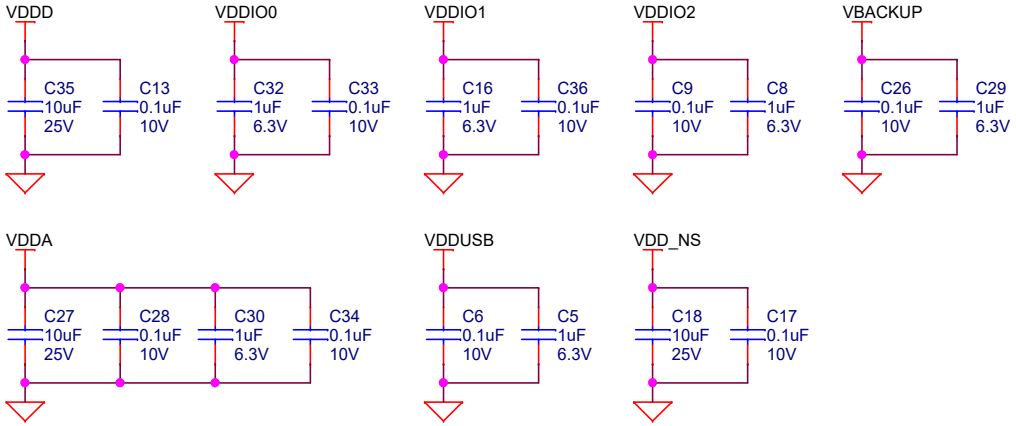
Size	Document Number	Drawn By	Approved By	Rev
A4	630-60553-01	AARA	RKAD	03

Date: Friday, January 31, 2020 Sheet 2 of 8

Supply for PSoC 6 MCU



Decoupling Capacitors



CYPRESS SEMICONDUCTOR
198 CHAMPION COURT
SAN JOSE, CA 95134
(408) 943-2600

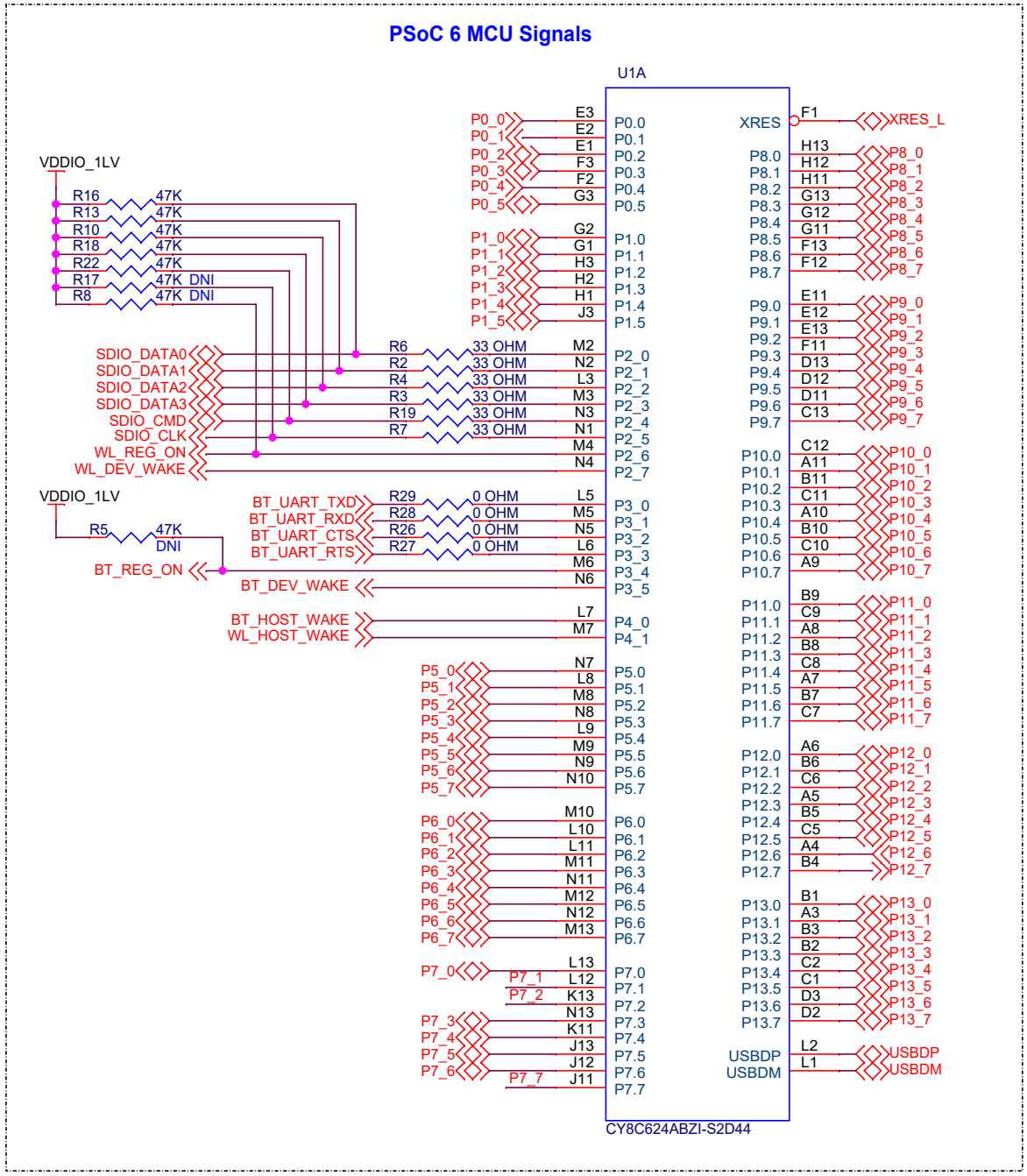
CYPRESS SEMICONDUCTOR © 2020

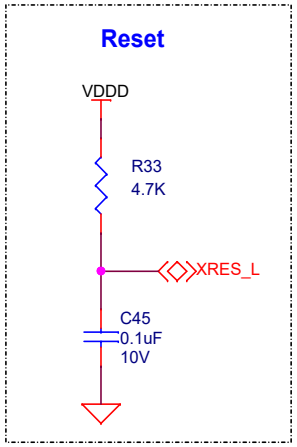
SCH Title : PSoC 6 (2M) with CYW43012 Carrier Module

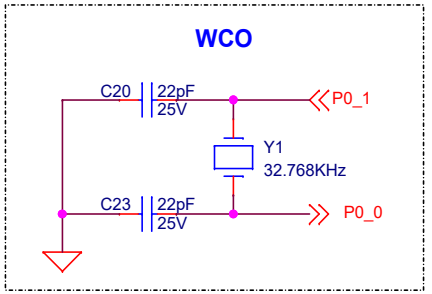
Page Title : PSoC 6 Power

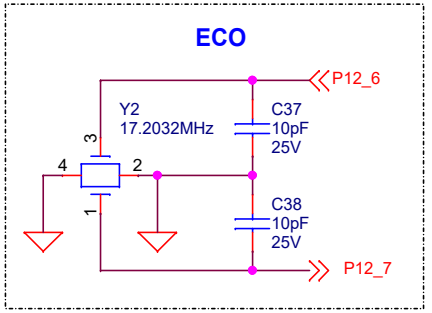
Size	Document Number	Drawn By	Approved By	Rev
A4	630-60553-01	AARA	RKAD	03

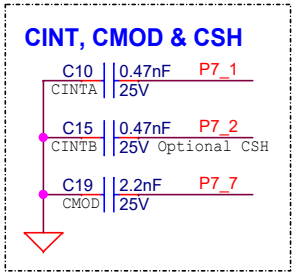
Date: Friday, January 31, 2020 Sheet 3 of 8











CYPRESS SEMICONDUCTOR
198 CHAMPION COURT
SAN JOSE, CA 95134
(408) 943-2600

CYPRESS SEMICONDUCTOR © 2020

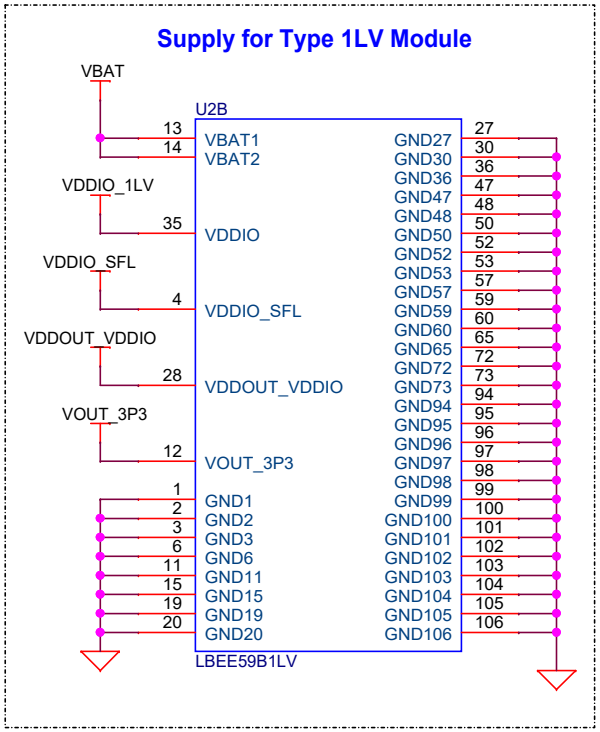
SCH Title : PSoC 6 (2M) with CYW43012 Carrier Module

Page Title : PSoC 6 Signals

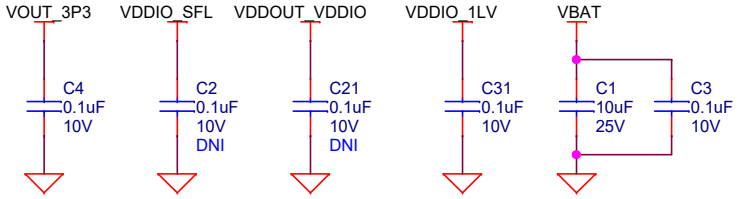
Size	Document Number	Drawn By	Approved By	Rev
A4	630-60553-01	AARA	RKAD	03

Date: Friday, January 31, 2020 Sheet 4 of 8

Supply for Type 1LV Module



Decoupling Capacitors



CYPRESS SEMICONDUCTOR
198 CHAMPION COURT
SAN JOSE, CA 95134
(408) 943-2600

CYPRESS SEMICONDUCTOR © 2020

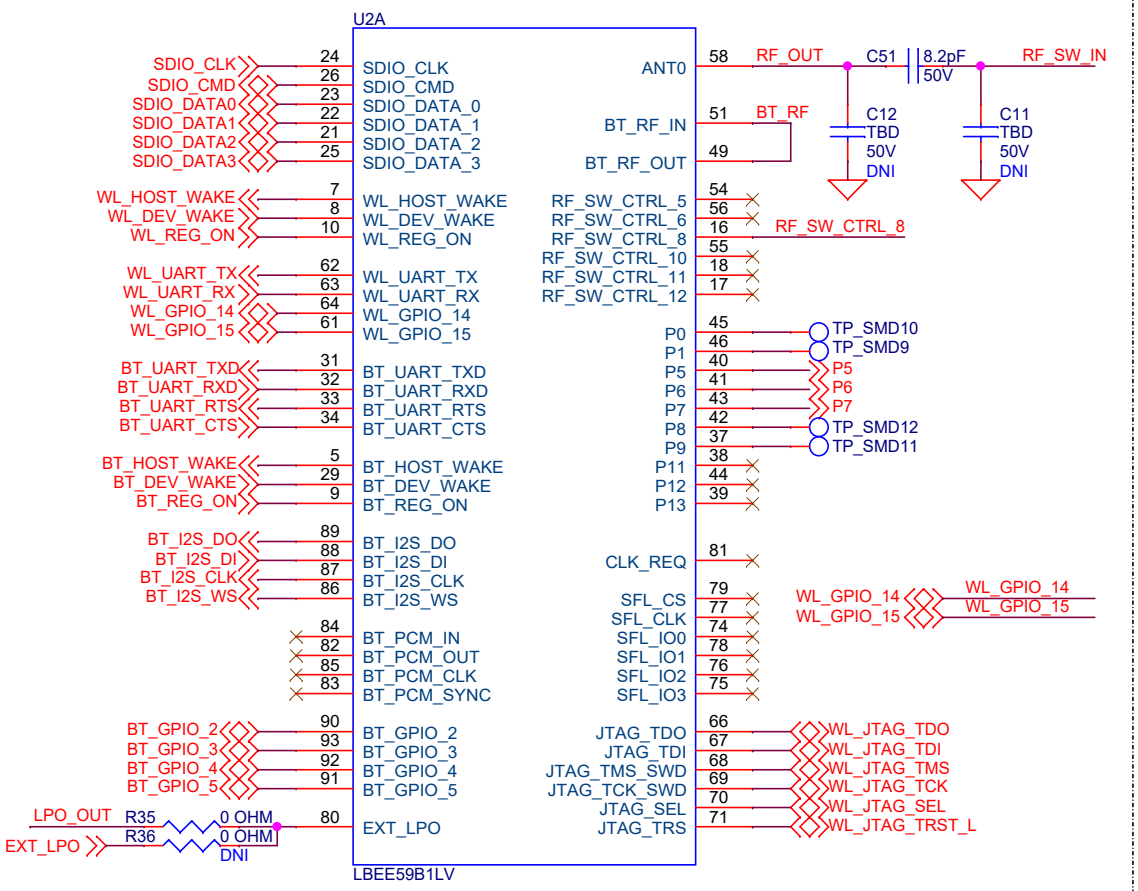
SCH Title : PSoC 6 (2M) with CYW43012 Carrier Module

Page Title : LBEE59B1LV Module Power

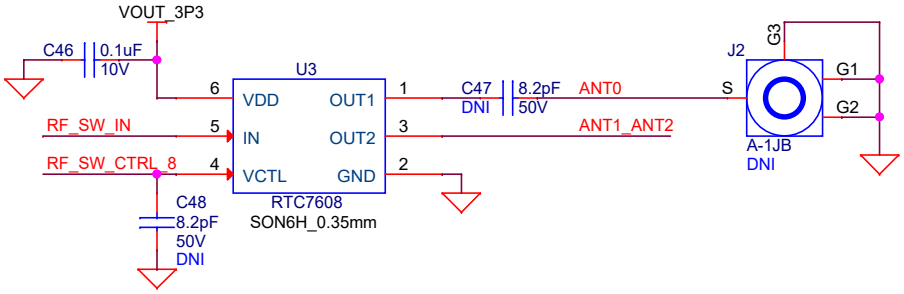
Size	Document Number	Drawn By	Approved By	Rev
A4	630-60553-01	AARA	RKAD	03

Date: Friday, January 31, 2020 Sheet 5 of 8

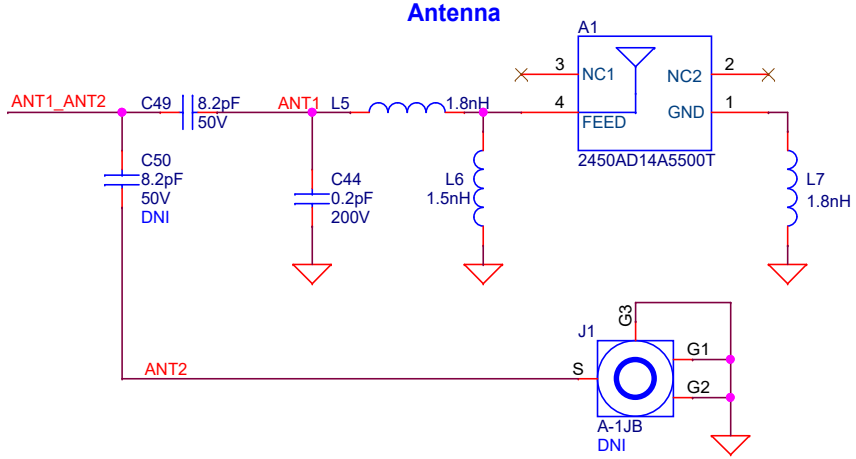
Type 1LV Module Signals



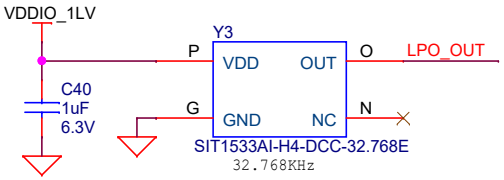
Antenna Diversity Switch



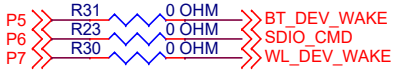
Antenna



Onboard LPO



LHL I/Os



Note: Make sure NO stub for SDIO_CMD net

CYPRESS SEMICONDUCTOR
198 CHAMPION COURT
SAN JOSE, CA 95134
(408) 943-2600

CYPRESS SEMICONDUCTOR © 2020

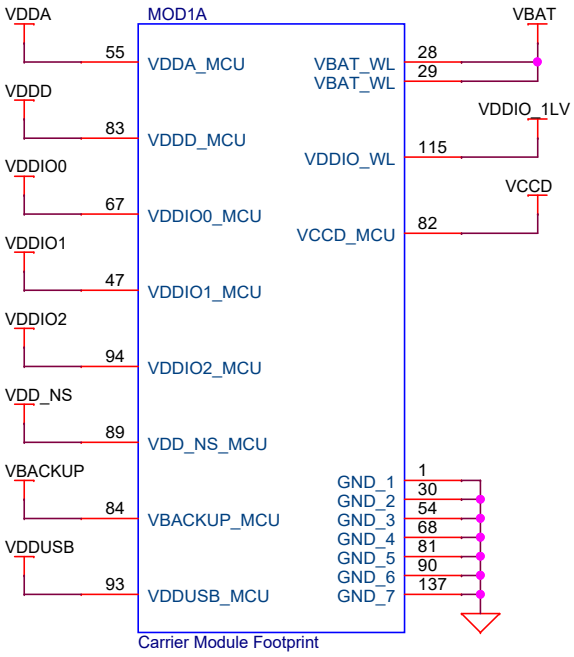
SCH Title : PSoC 6 (2M) with CYW43012 Carrier Module

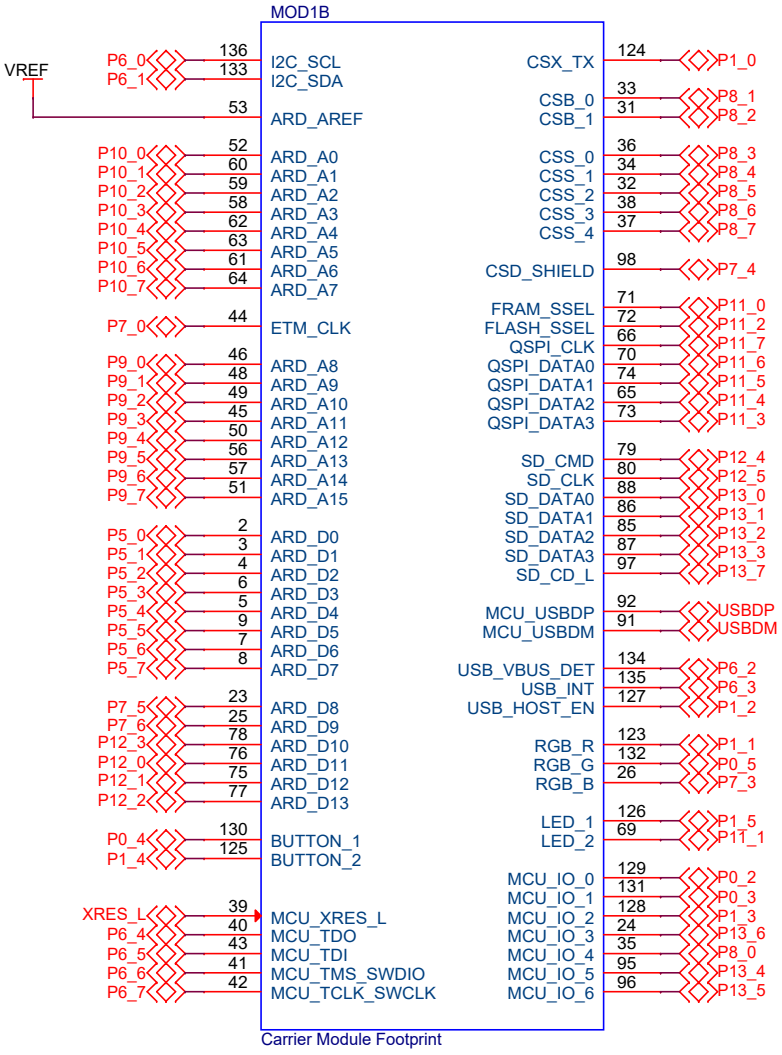
Page Title : LBEE59B1LV Module Signals

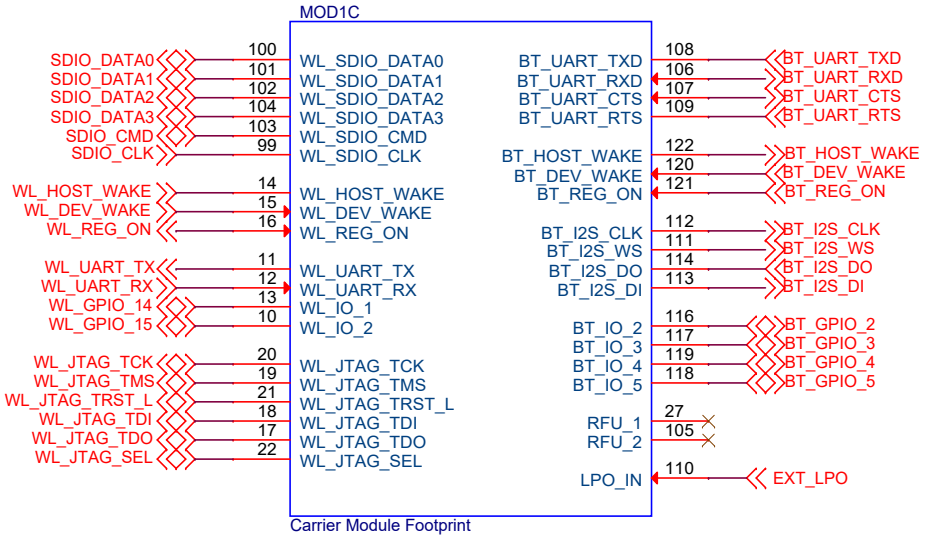
Size	Document Number	Drawn By	Approved By	Rev
A4	630-60553-01	AARA	RKAD	03

Date: Friday, January 31, 2020 Sheet 6 of 8

Carrier Module Footprint







CYPRESS SEMICONDUCTOR
198 CHAMPION COURT
SAN JOSE, CA 95134
(408) 943-2600

CYPRESS SEMICONDUCTOR © 2020

SCH Title : PSoC 6 (2M) with CYW43012 Carrier Module

Page Title : Interface to BaseBoard

Size	Document Number	Drawn By	Approved By	Rev
A4	630-60553-01	AARA	RKAD	03

Date: Friday, January 31, 2020 Sheet 7 of 8

REVISION HISTORY

REV	DESCRIPTION OF CHANGE	Orig. of Change	DATE
01	Internal Release	AARA	2019/03/28
02	Initial Release	AARA	2019/09/17
03	WCO load caps (C20,C23) updated to 22pF (earlier C20 = 18pF, C23 = 33pF)	AARA	2020/01/31

CYPRESS SEMICONDUCTOR
198 CHAMPION COURT
SAN JOSE, CA 95134
(408) 943-2600

CYPRESS SEMICONDUCTOR © 2020

SCH Title : PSoC 6 (2M) with CYW43012 Carrier Module

Page Title : Revision History

Size	Document Number	Drawn By	Approved By	Rev
A4	630-60553-01	AARA	RKAD	03

Date: Friday, January 31, 2020 Sheet 8 of 8