

CY8CKIT-062-BLE PSoC 6 BLE Pioneer Board

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Drawing Numbers

PCBA	121-60367-01
PCB	600-60380-01
FAB DRW	610-60337-01
ASSY DRW	620-60345-01
SCH DRW	630-60357-01



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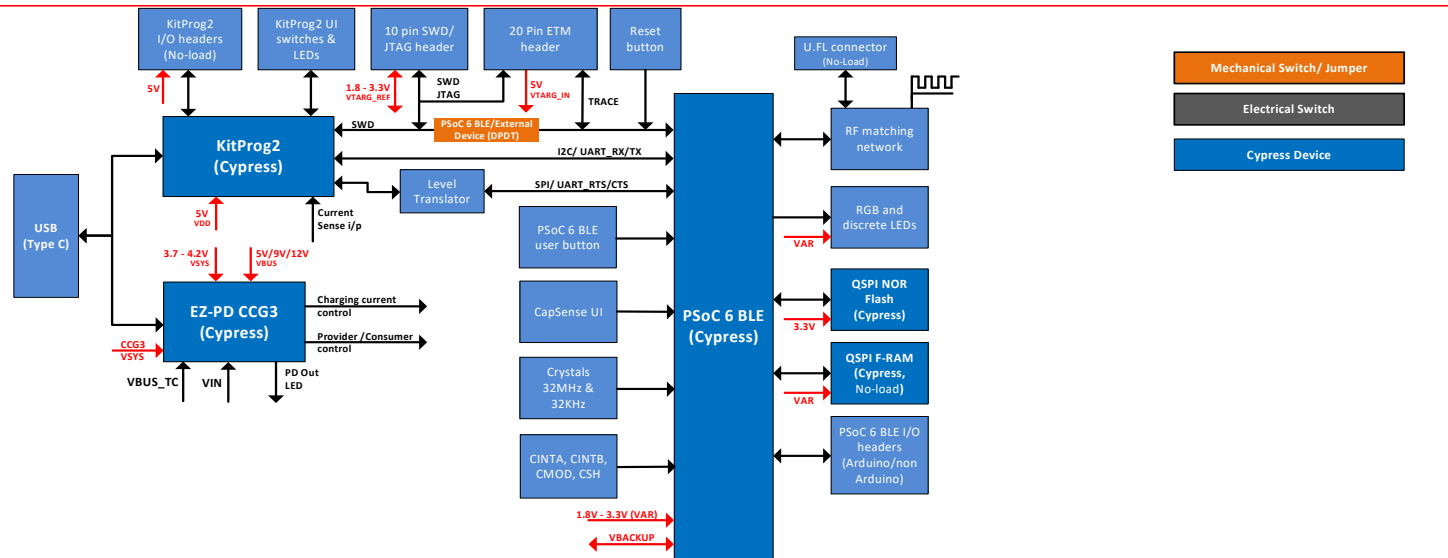
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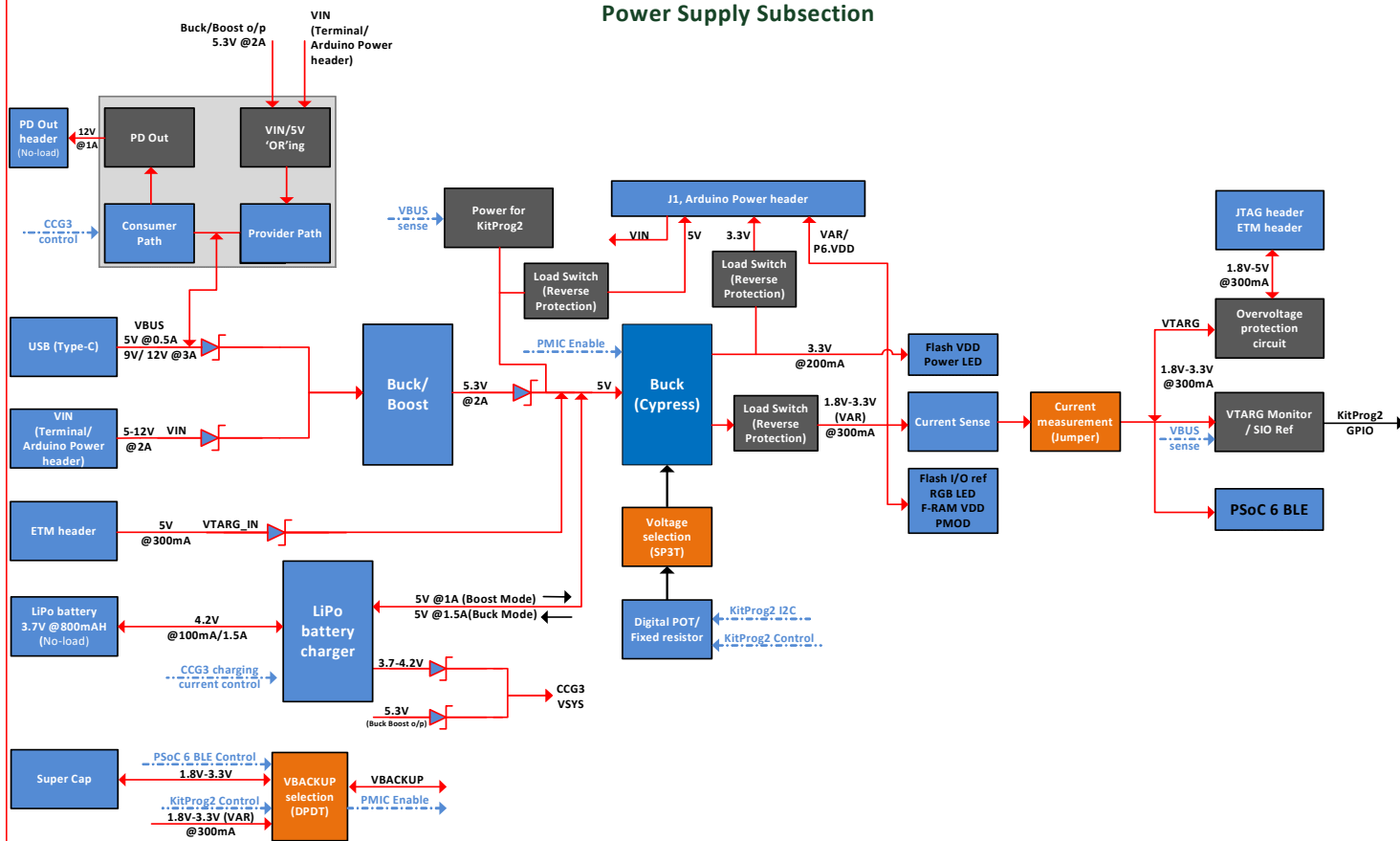
SCH Title : CY8CKIT-062-BLE PSoC 6 BLE Pioneer Board

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Power Supply Subsection



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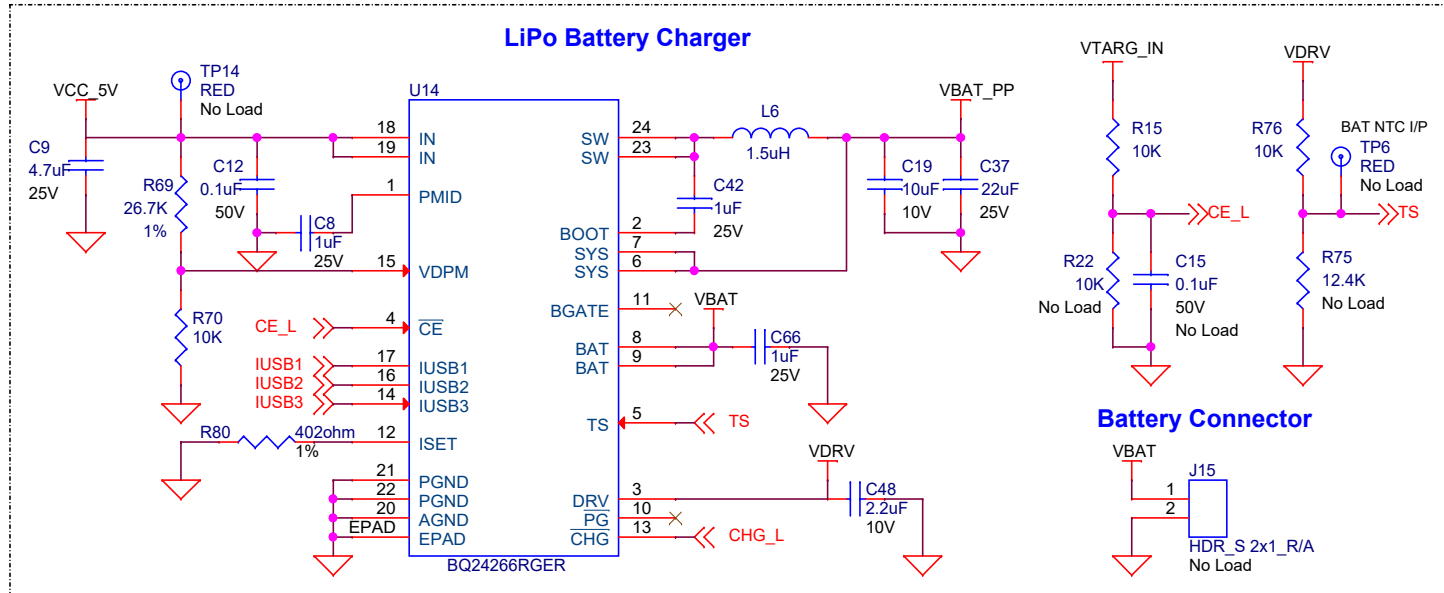
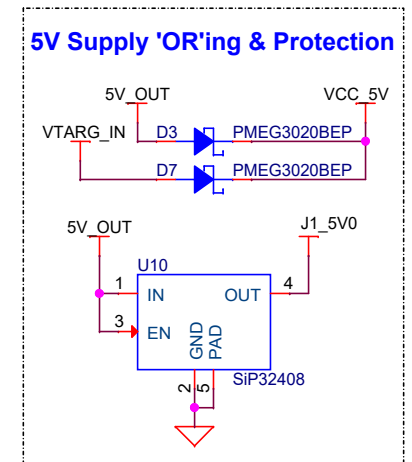
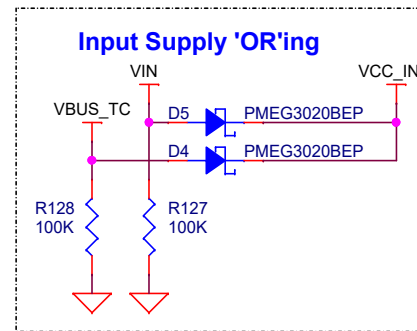
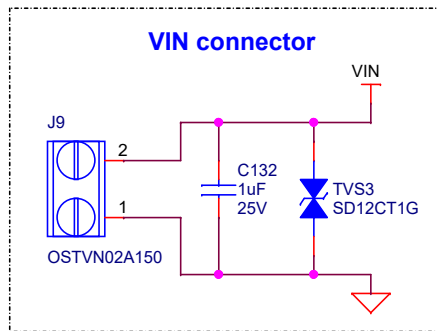
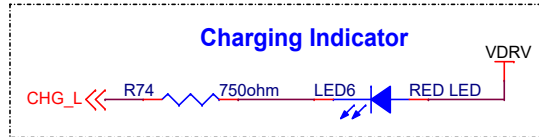
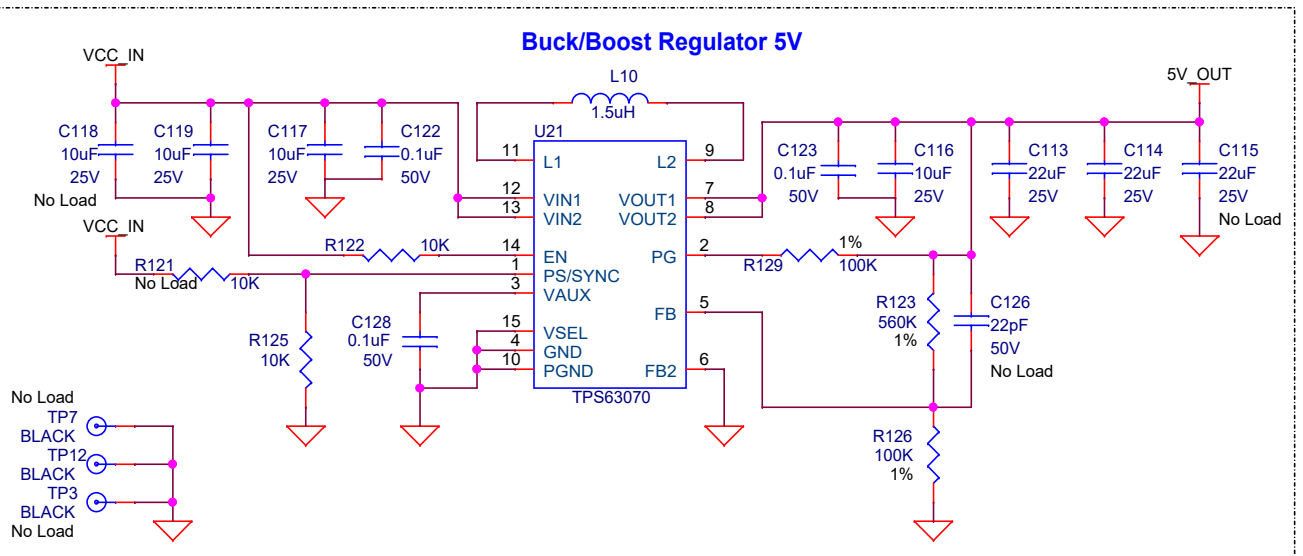
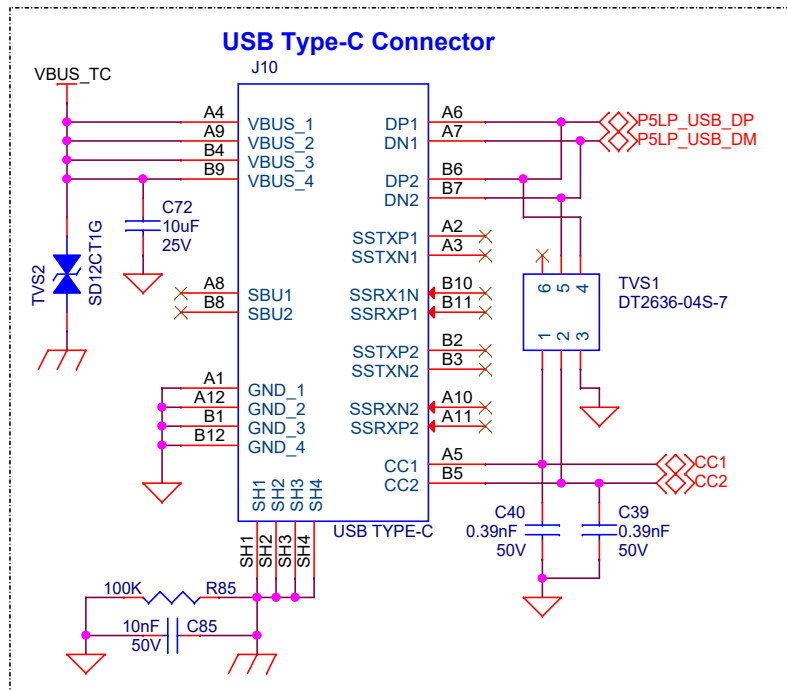
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SCH Title : CY8CKIT-062-BLE PSoc 6 BLE Pioneer Board

Page Title : Block Diagram

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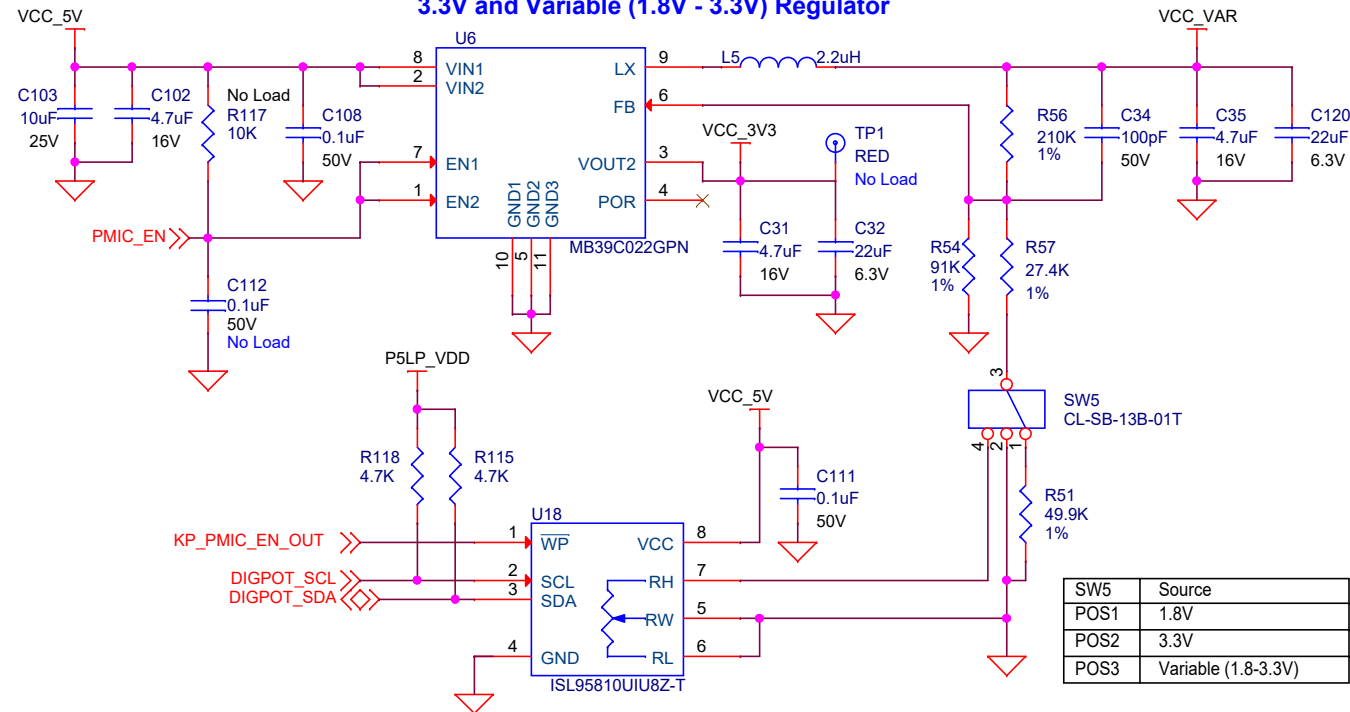
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Page Title : Input Regulators

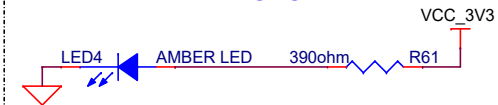
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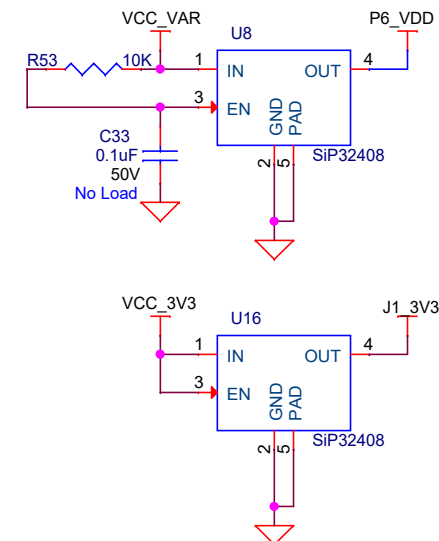
3.3V and Variable (1.8V - 3.3V) Regulator



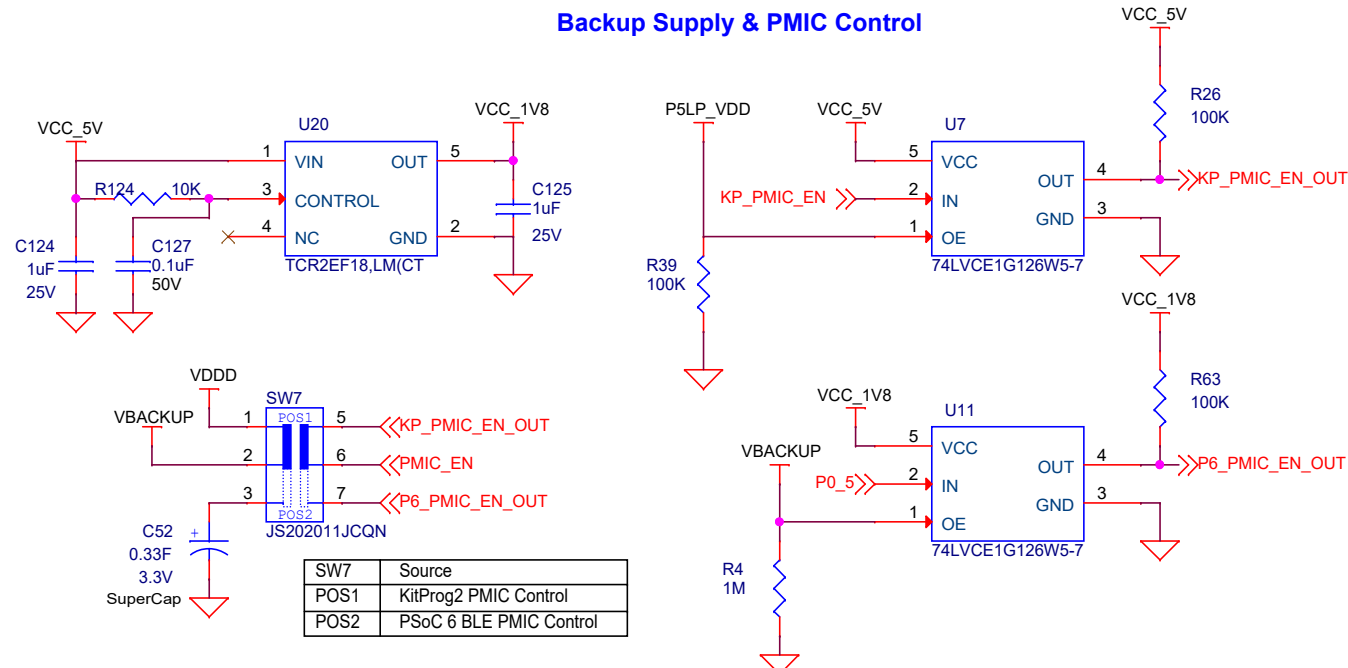
Power & Charging LED



Reverse protection



Backup Supply & PMIC Control



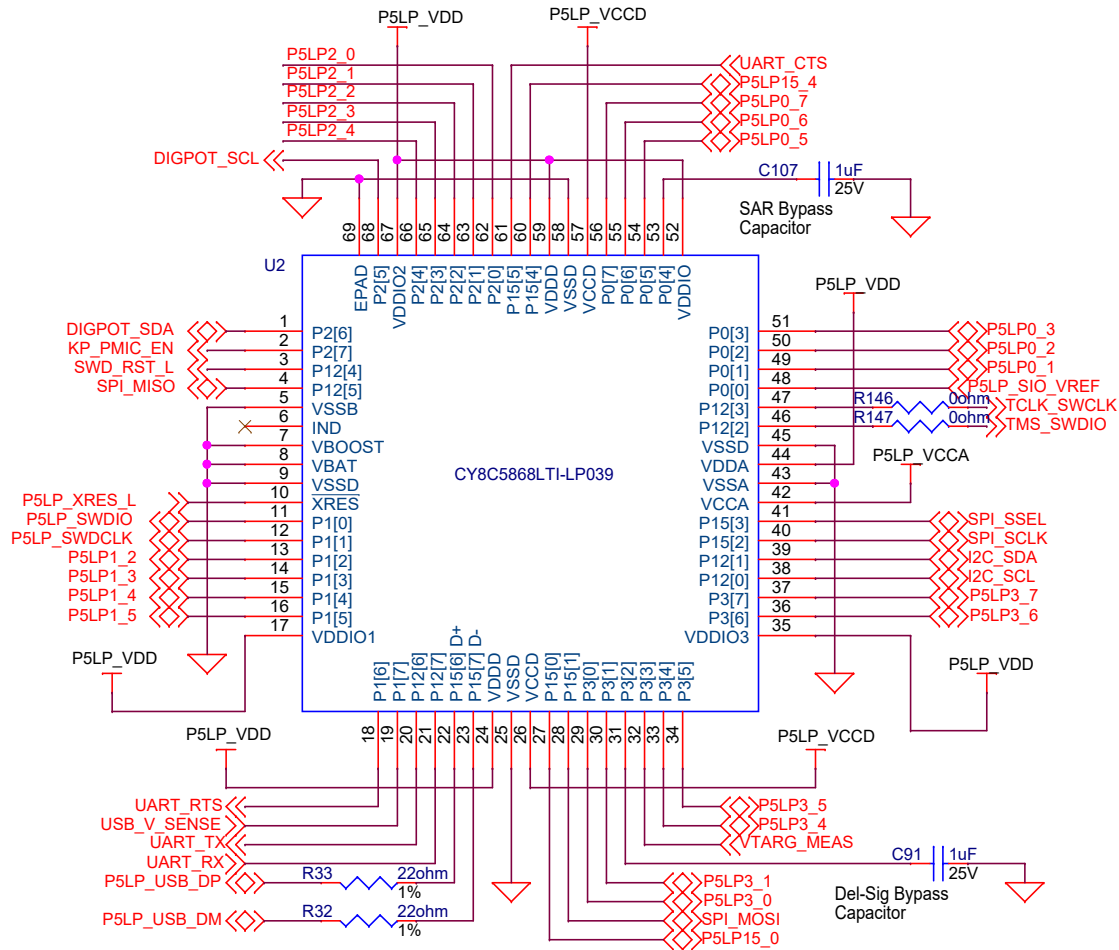
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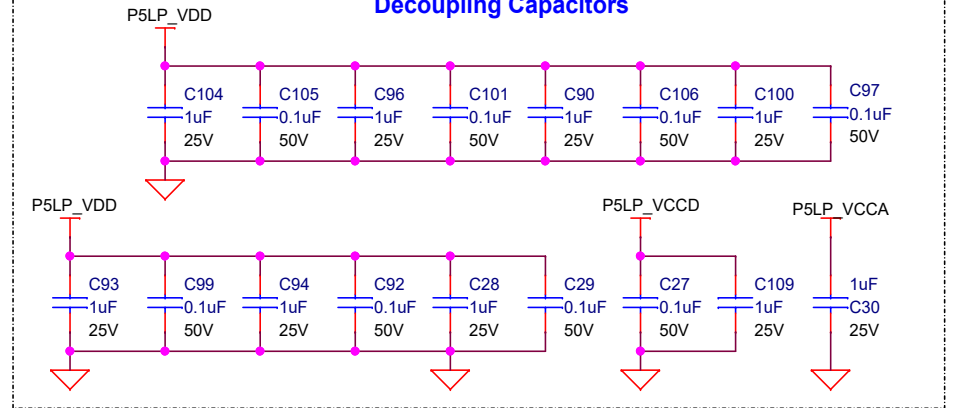
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PSoC 5LP based KitProg2



Decoupling Capacitors

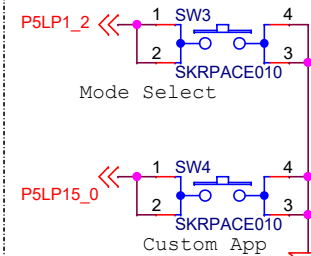


KitProg2 HW Revision

P5LP2_0 Bit 0
P5LP2_1 Bit 1
P5LP2_2 Bit 2
P5LP2_3 Bit 3
P5LP2_4 Bit 4

ID = 0x02
Note: GND is read as binary "1" and floating as "0"

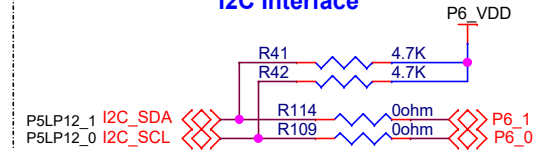
PSoC 5LP User Switches



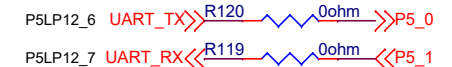
SWD Interface



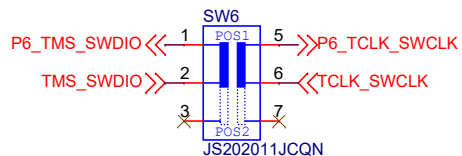
I2C Interface



UART Interface



SWD Target Select



SW6	Target
POS1	PSoC 6 BLE
POS2	External

PSoC 5LP Status LEDs



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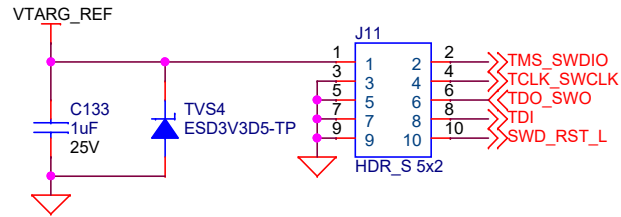
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SCH Title : CY8CKIT-062-BLE PSoC 6 BLE Pioneer Board

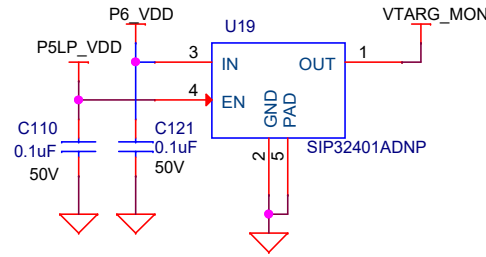
Page Title : KitProg2 Controller

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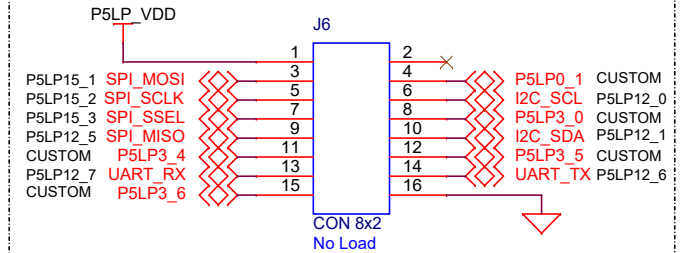
Target PSoC Program/Debug Header



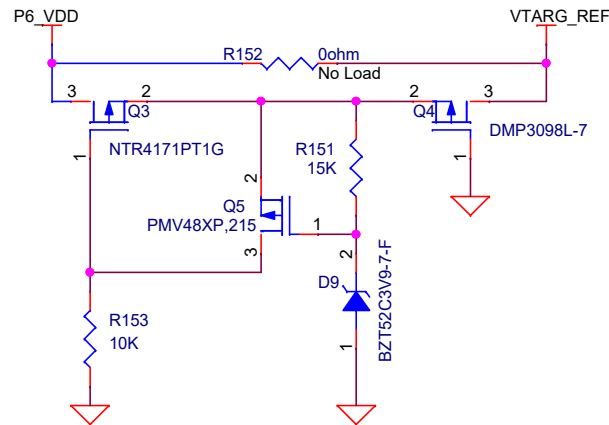
Voltage Monitoring



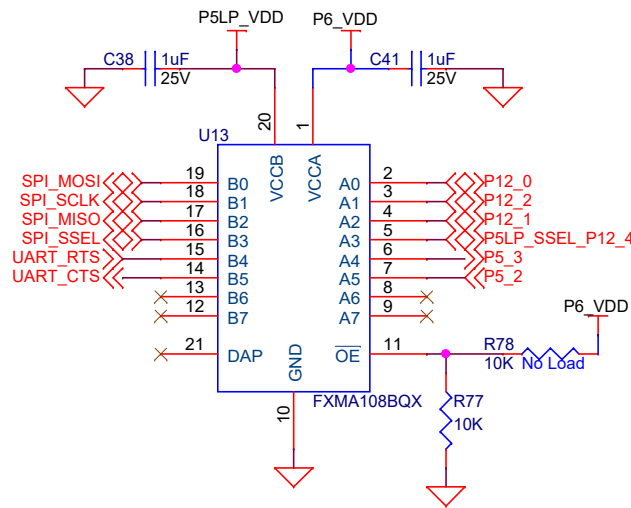
PSoC 5LP GPIO Expansion Header



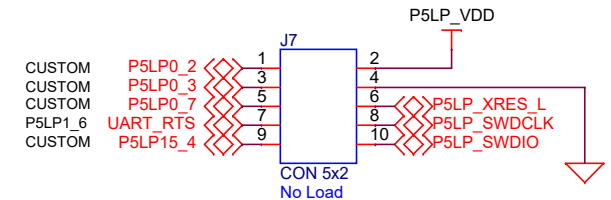
Program/Debug Overvoltage Protection



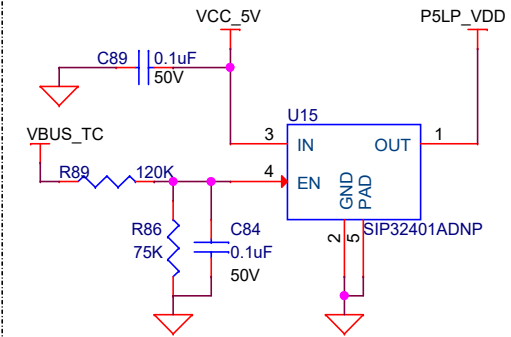
Level Translator for SPI and UART flow control



PSoC 5LP Custom Application Header



Supply for PSoC 5LP



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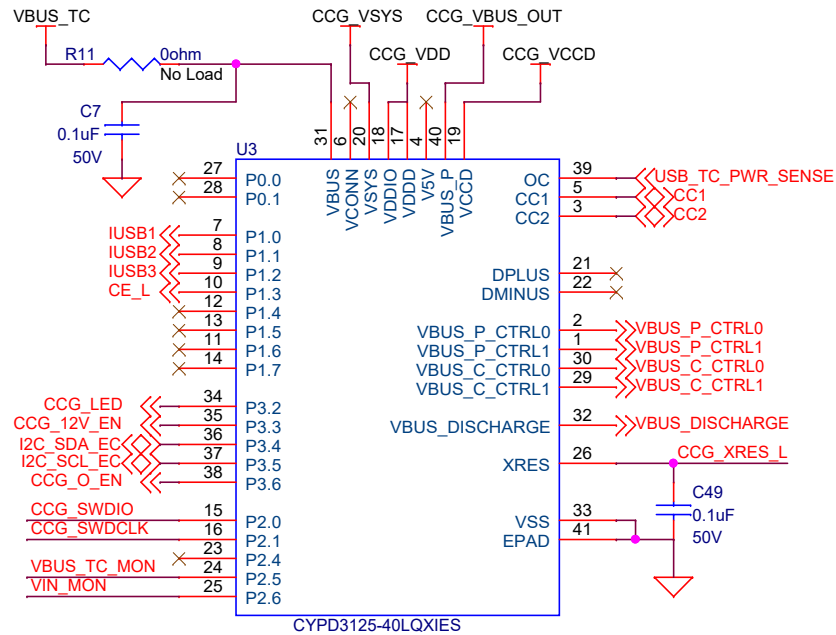
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SCH Title : CY8CKIT-062-BLE PSoC 6 BLE Pioneer Board

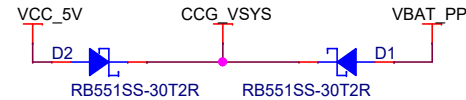
Page Title : KitProg2 Power & Headers

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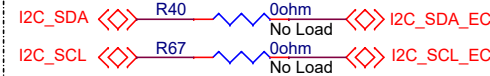
CCG3, USB Type-C and Power Delivery



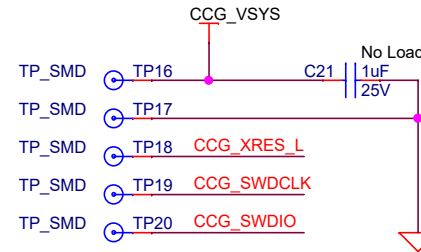
CCG3 supply selection



I2C Bootloader



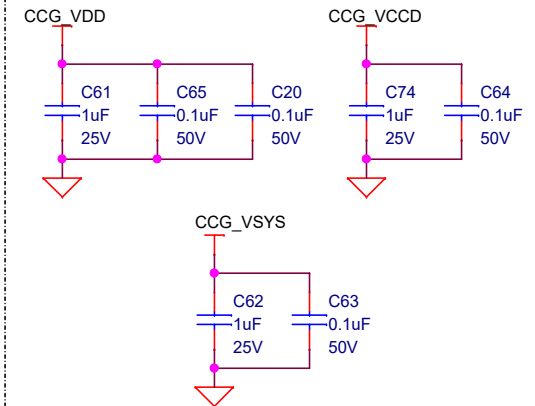
CCG3 Programming



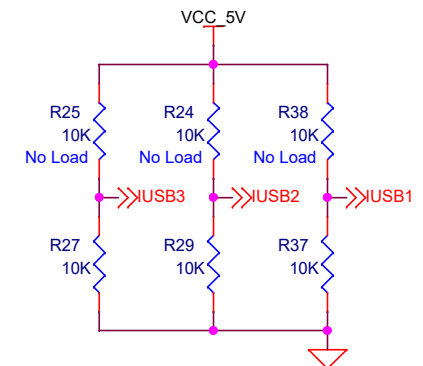
Output Indication



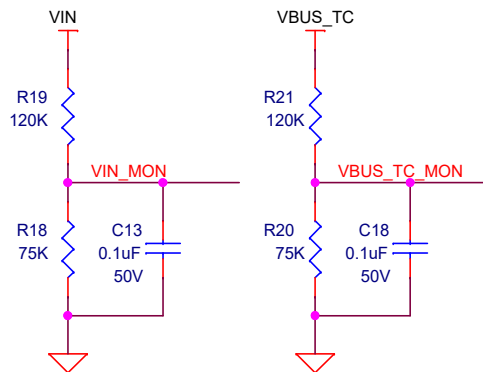
Decoupling Capacitors



LiPo battery charger pull-up



VBUS and VIN voltage monitoring



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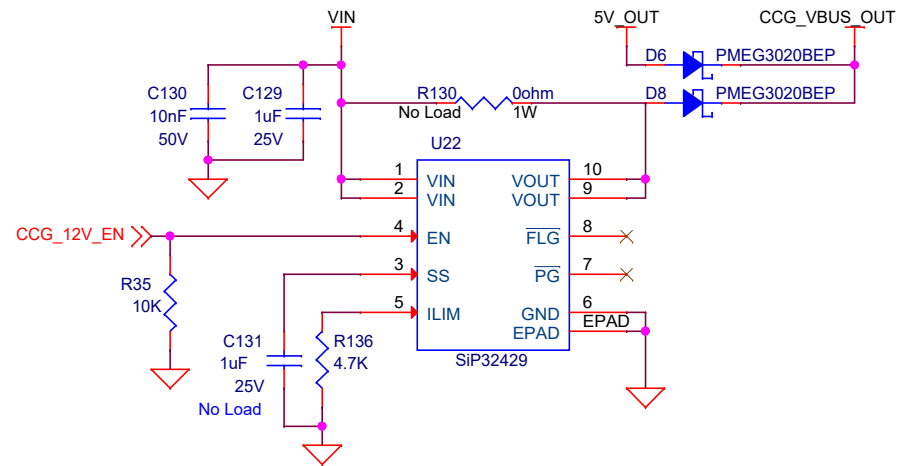
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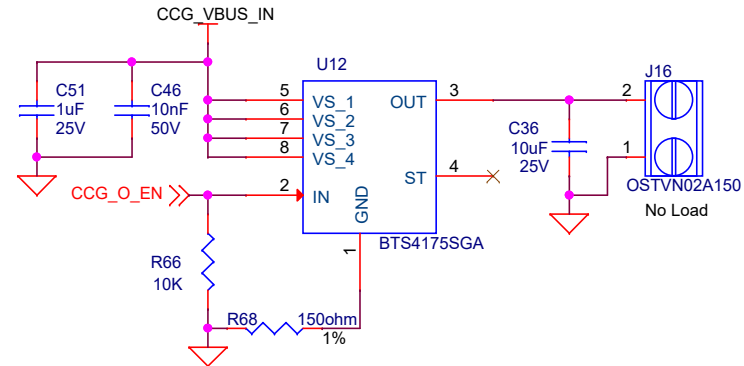
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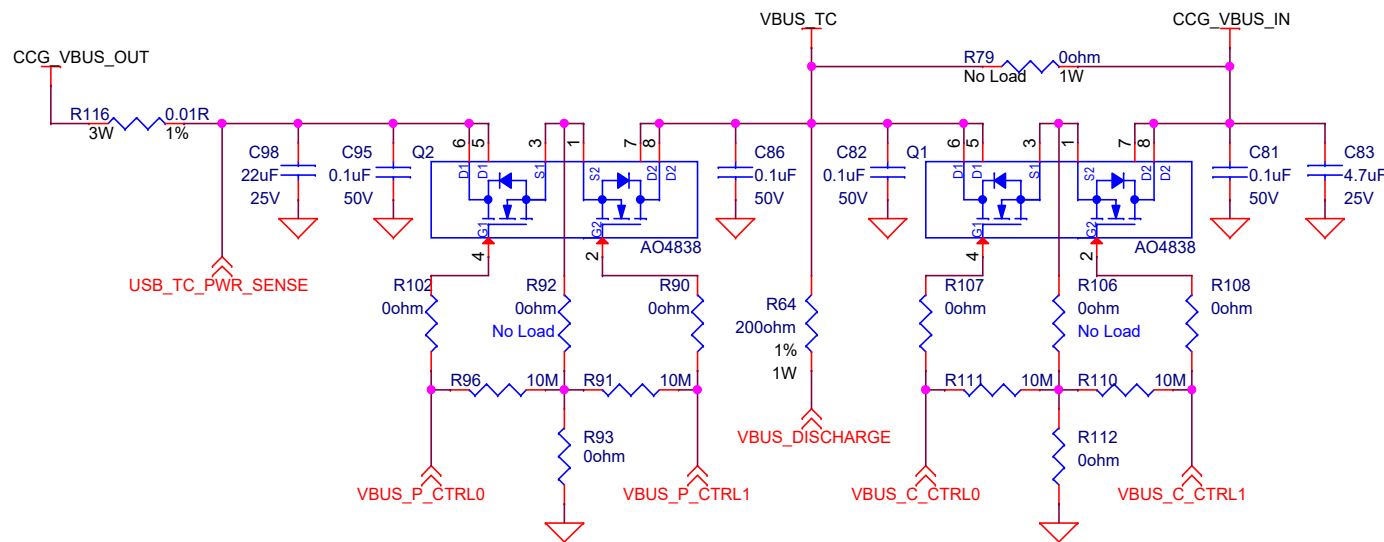
CCG3 Provider path 'OR'ing



CCG3 12V @1A Output



VBUS Provider/Consumer Path



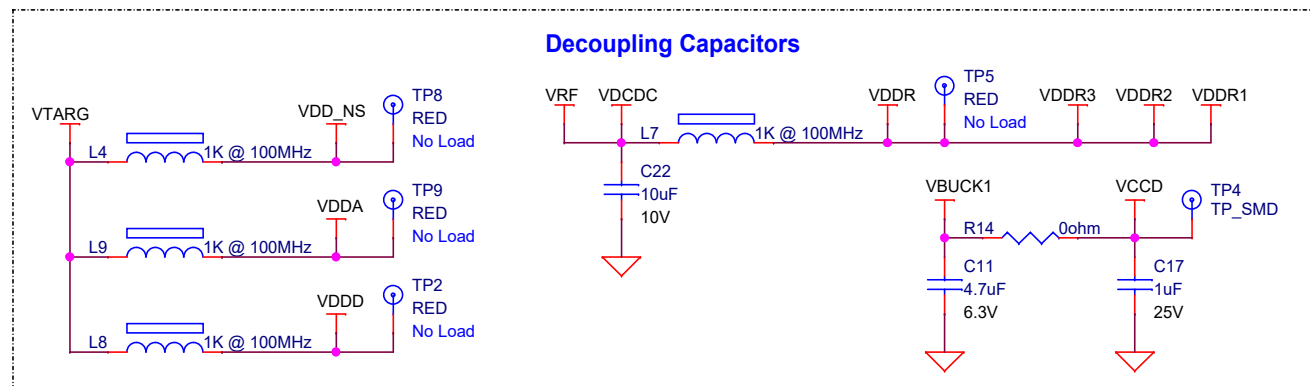
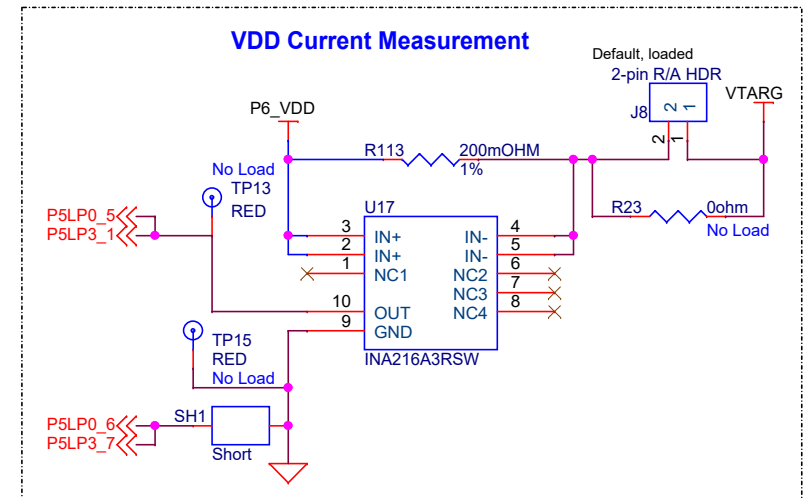
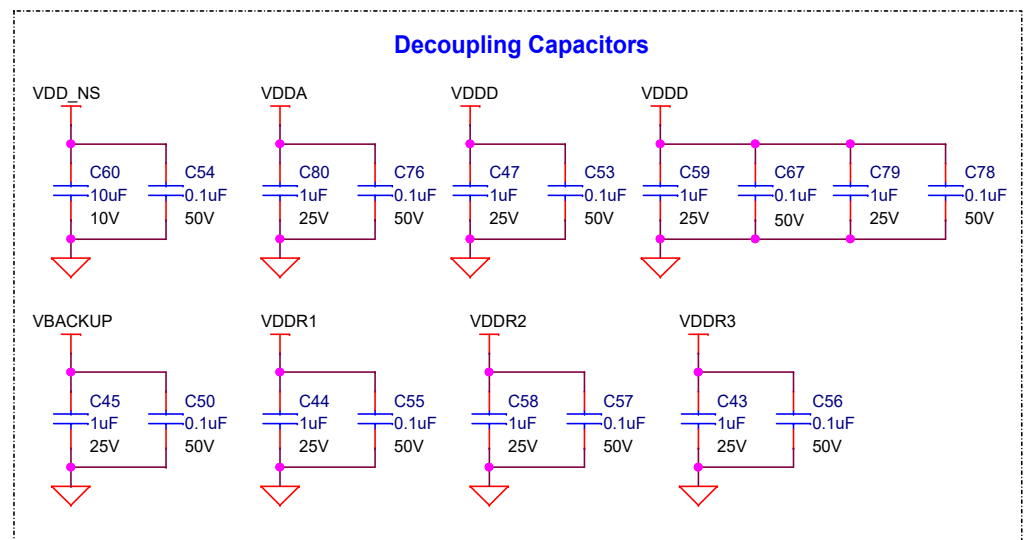
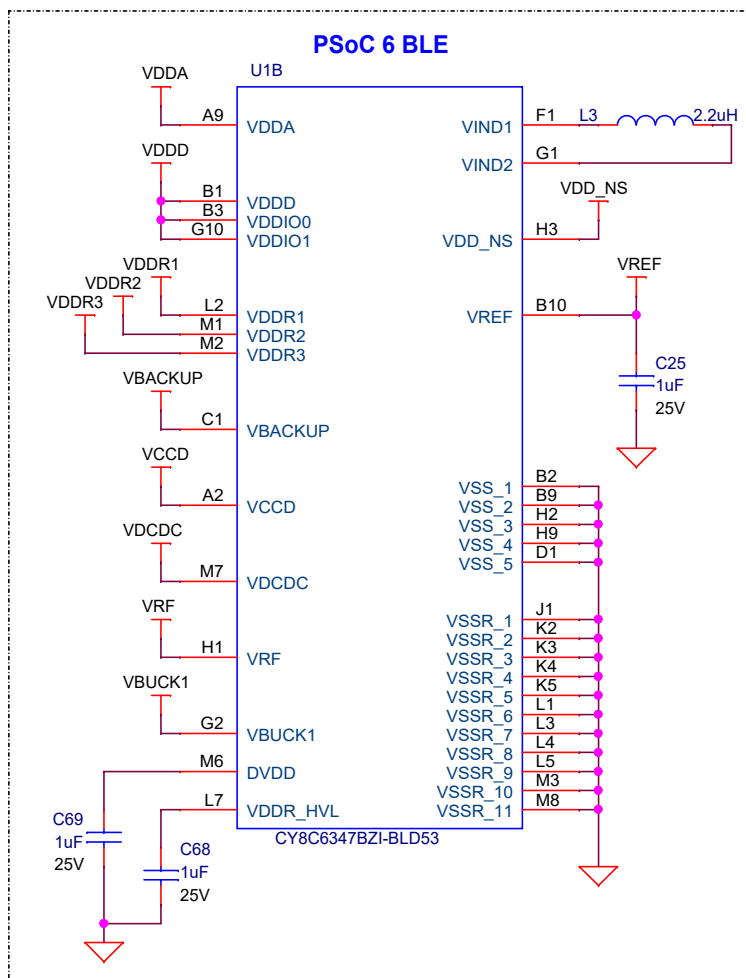
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Page Title : Type-C Power Path

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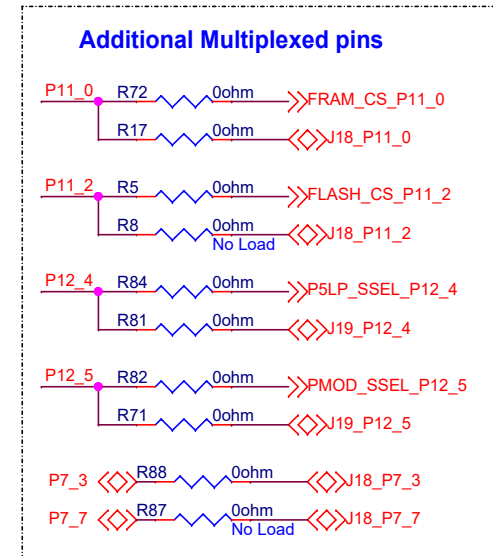
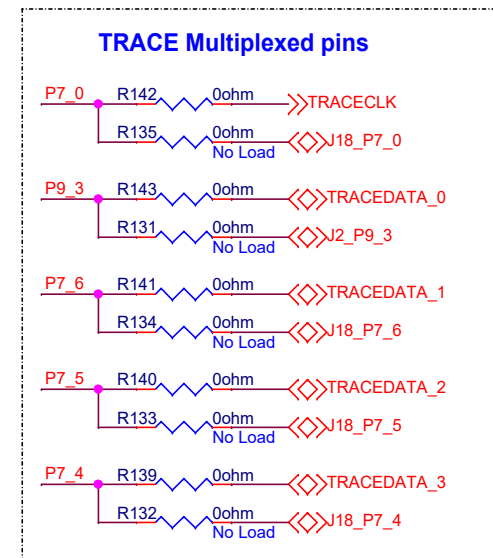
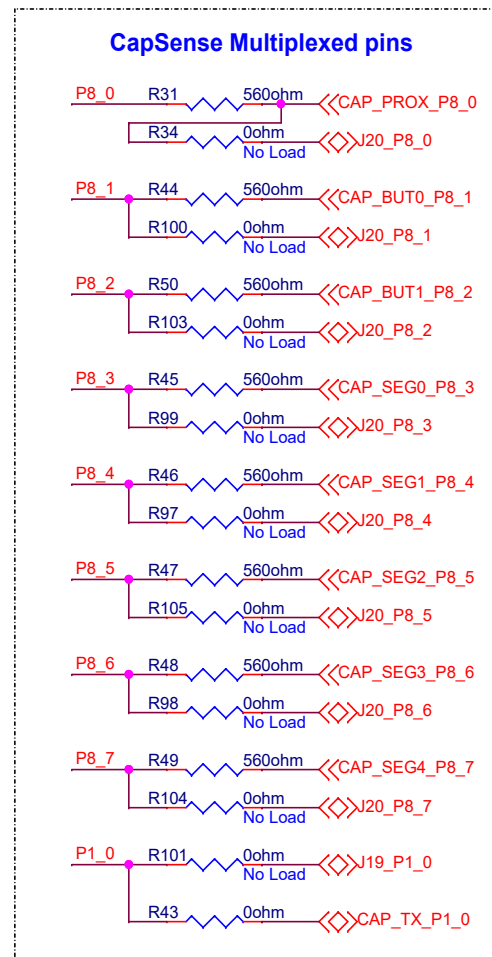
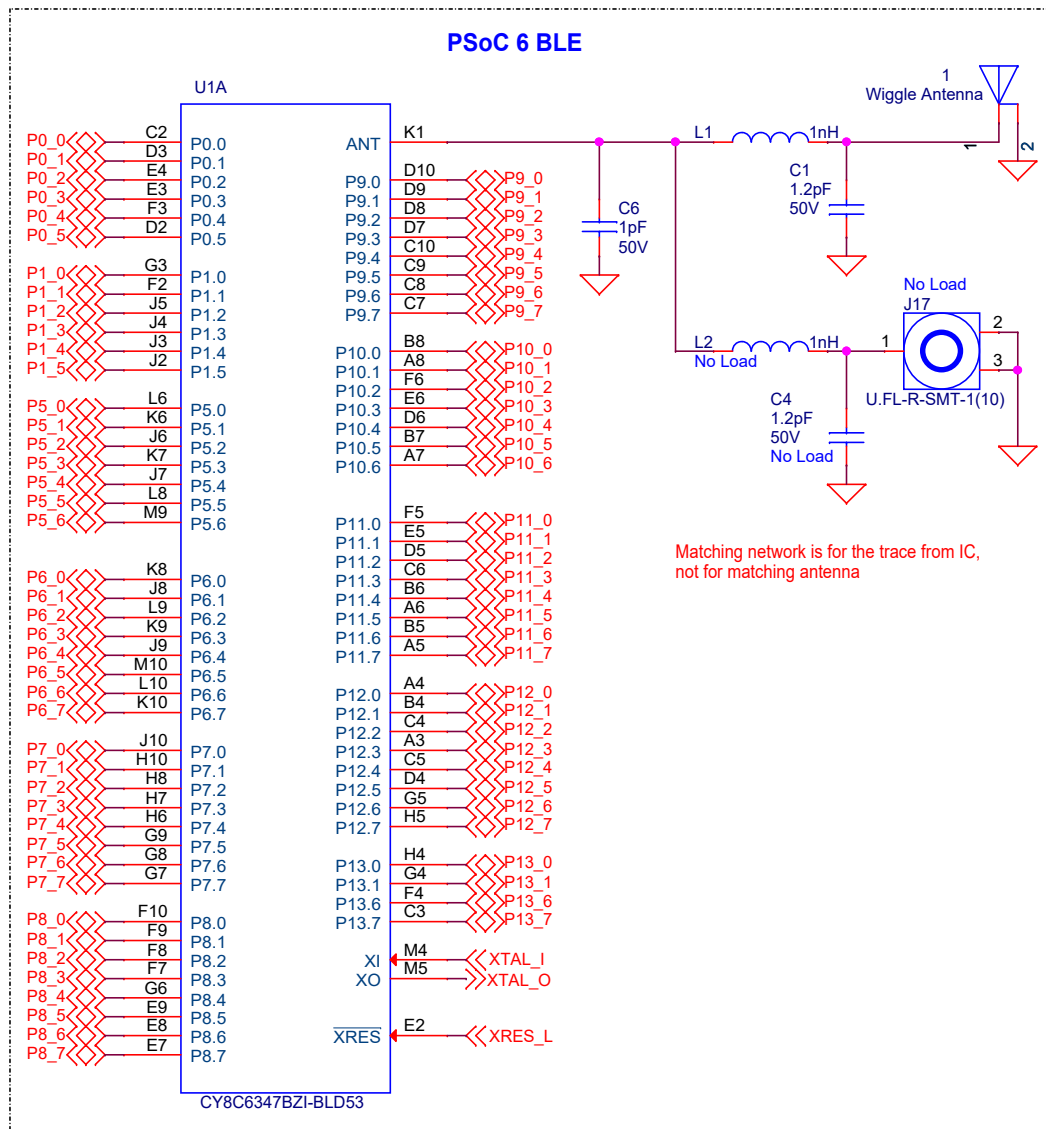
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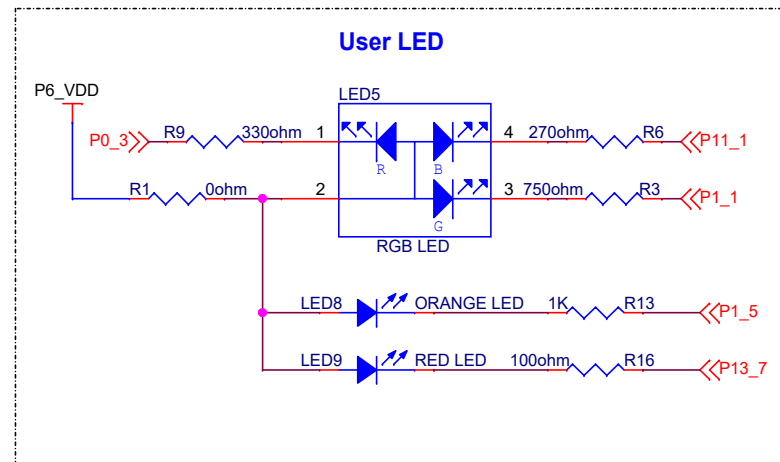
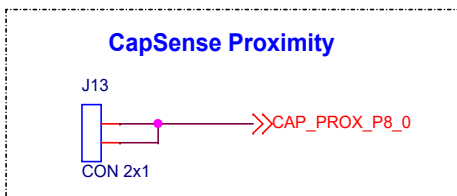
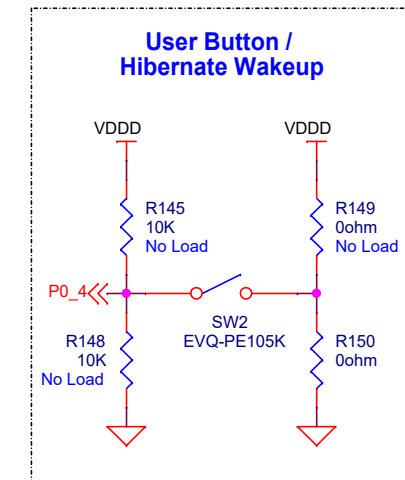
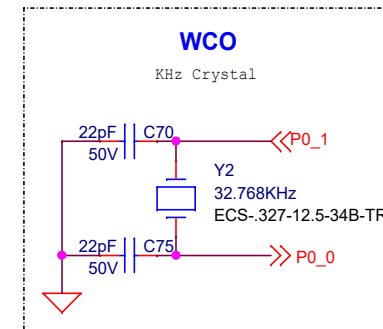
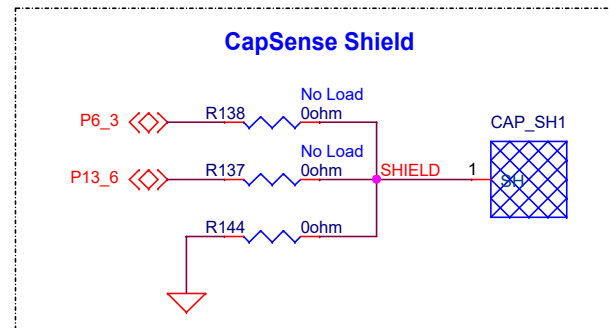
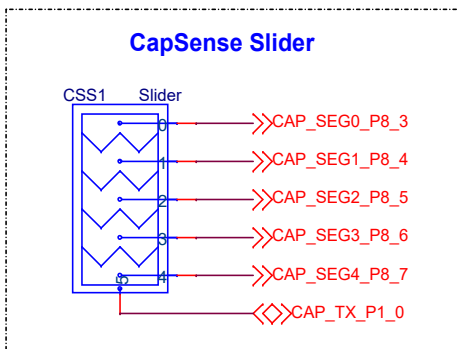
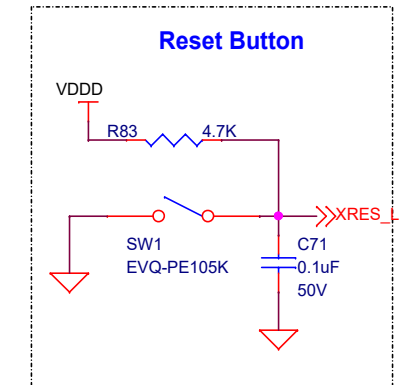
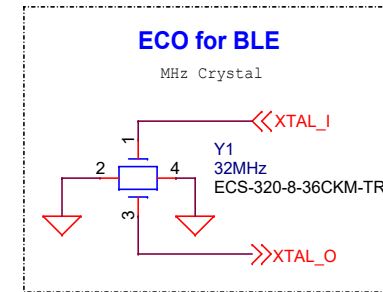
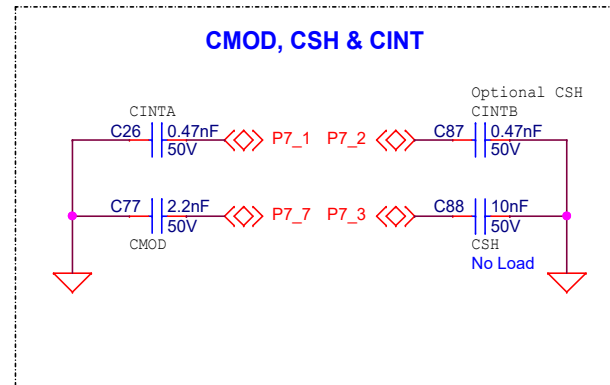
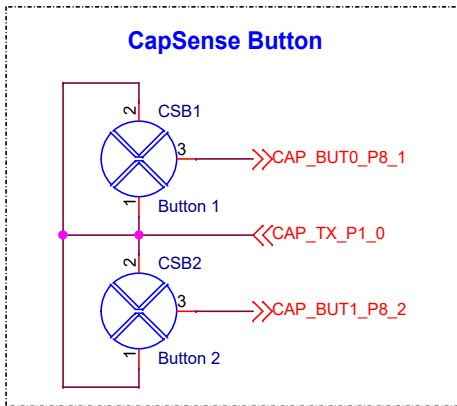
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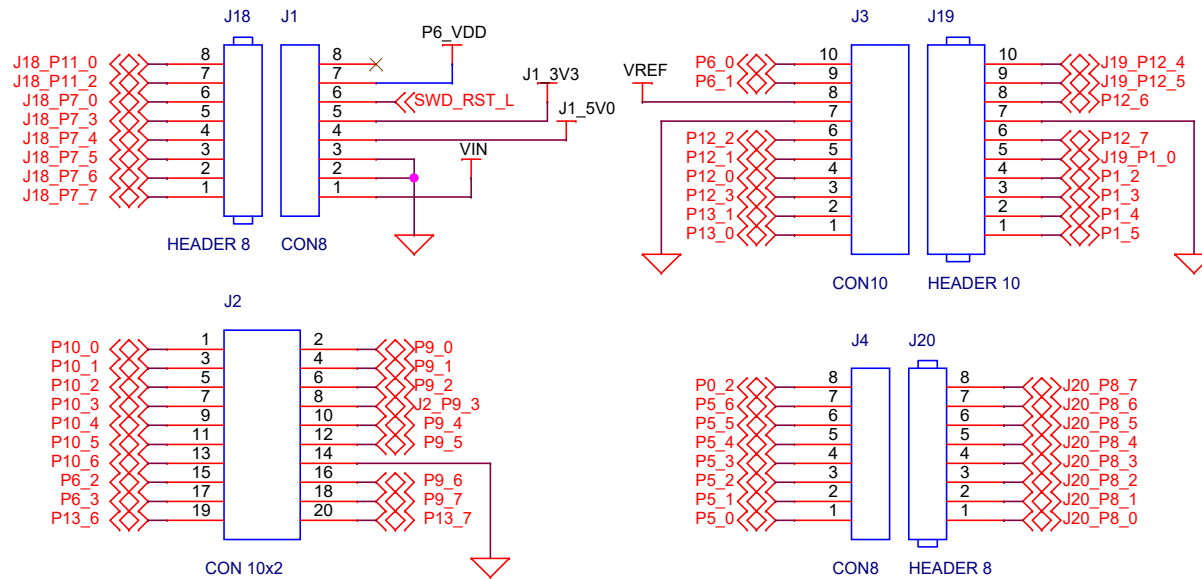
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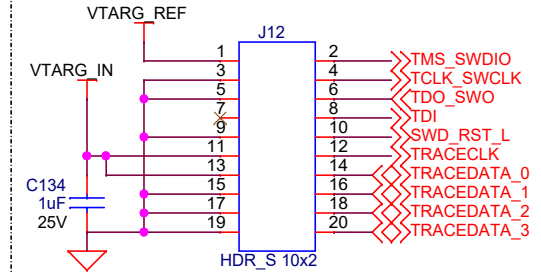




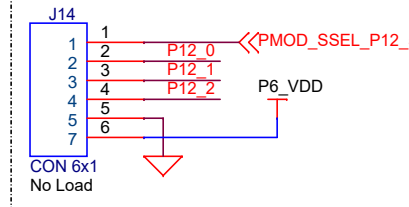
Arduino Headers



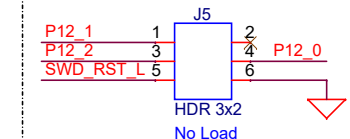
ETM Header



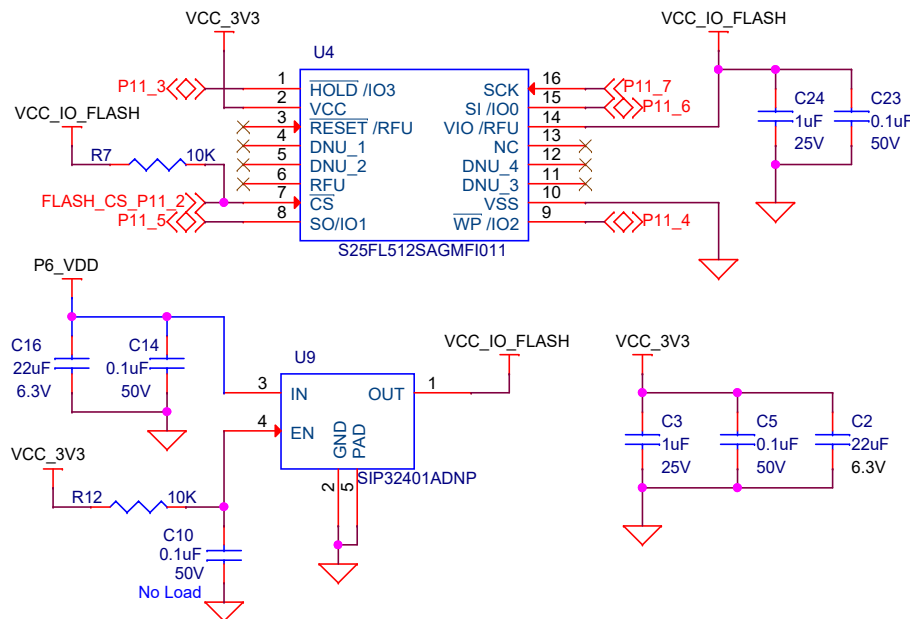
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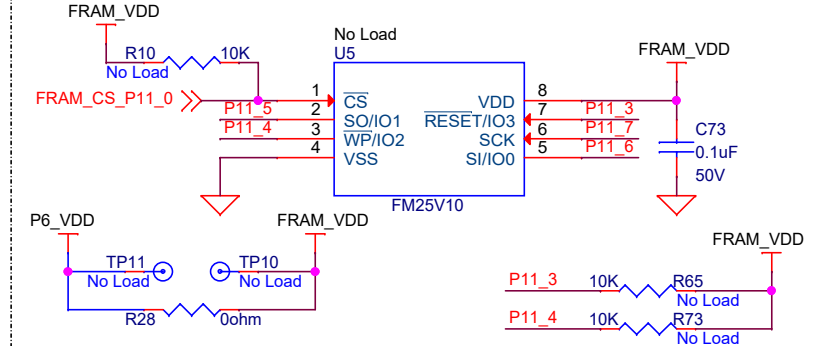
ICSP Header



QSPI Flash (SMIF)



F-RAM



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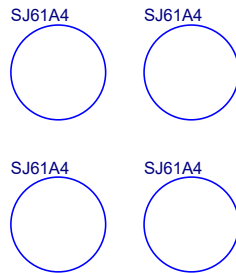
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Page Title : Expansion headers & Memory

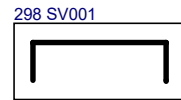
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Accessories

Cylindrical Bumper



USB connector bracket



Acrylic Overlay

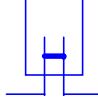
Acrylic Overlay

Color: Clear, Transparent
Finish: Matt

40 x 34 x 1mm

Jumper Shunt

SPC02SYAN



PCBA label

LBL PCA Label

LBL QR Code



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Page Title : Accessories & PCB Artwork

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Revision History

Rev	Description	Date
01	Initial internal release	08/29/2016
02	Internal release	10/03/2016
03	Internal release	11/25/2016
04	Initial external release under NDA	01/19/2017
05	3.3V protection diode changed to load SW (U59) Added PMIC control section (U58, U56, U57 and passives) Removed series resistor for SPI and UART HW flow control Voltage Monitoring domain and Protection Circuit o/p changed to P6_VDD Changed trace data line connection (to 9.3, 7.6, 7.5, 7.4) C22 Cap on VRF changed to 10uF Removed SW8 and connected CINT directly, CMOD connection changed to 7.7 Shield # changed to 13.6/6.3 Added additional LED on pin 13.7 and 1.5 Added option to change logic for user SW and added pull-up/down Back annotated from layout	5/2/2017
06	Changed WCO load caps C70 and C75 to 22pF	1/21/2020



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