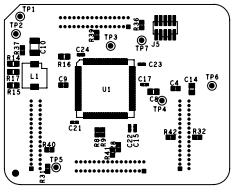
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
2	ALPHA RELEASE	10/7/08	LKO
3	ALPHA 2 RELEASE	11/12/08	DFK
3.3	ALPHA 3 RELEASE	2/13/09	DFK
4	BETA 2 RELEASE	04/02/09	DFK
**	PRODUCTION RELEASE	07/22/09	DFK



UNLESS OTHERWISE SPECIFIED	SIGNATURES	DATE
DIMENSIONS ARE IN INCHES		
TOLERANCES ON; ANGLES +/- 2°	DRAWN DFK	2/13/09
2 PL DECIMALS +/- .010	CHECKED DFK	2/13/09
3 PL DECIMALS +/- .005	ENGRG LKO	10/27/08
THIRD ANGLE PROJECTIONS	ISSUED	

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CYPRESS SEMICONDUCTOR
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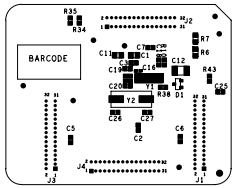
(208) 489-9303

ASSEMBLY DRAWING
CY8C38 FAMILY
PROCESSOR MODULE

SIZE	FSCM NO	DWG NO	REV
C		121R-49400	**

SCALE: 1/1 SHEET: 1 OF 2



CYPRESS SEMICONDUCTOR

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ASSEMBLY DRAWING
CY8CFAMILY LEOPARD
PROCESSOR MODULE

SIZE	FSCM NO	DWG NO	REV
C		121R-49400	**

SCALE: 1/1

SHEET: 2 OF 2