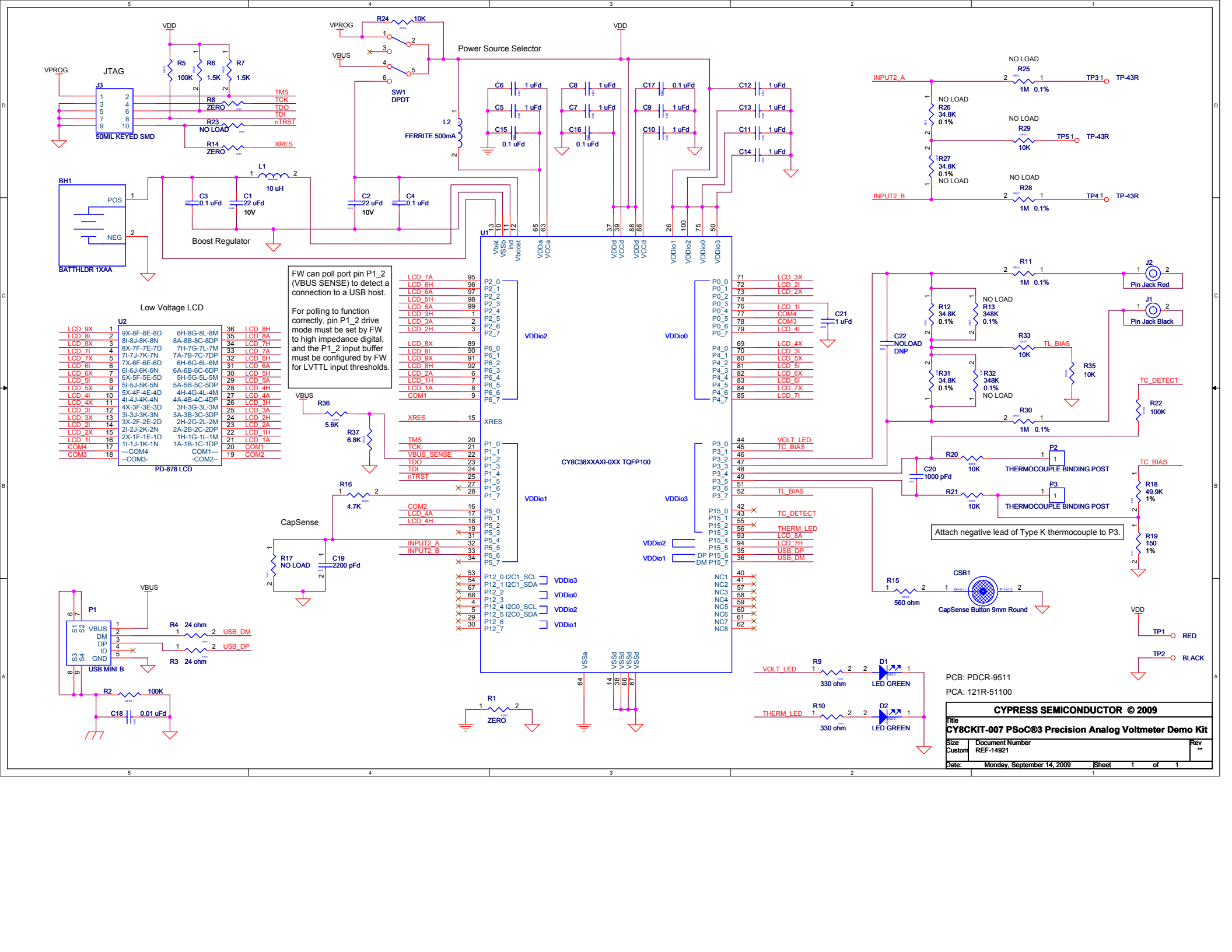

JTAG
50MIL KEYED SMD
Power Source Selector
SW1
DPDT
FERRITE 500mA
Boost Regulator
BATTHLDR 1XAA
Low Voltage LCD
PD-878 LCD
FW can poll port pin P1_2 (VBUS SENSE) to detect a connection to a USB host.
For polling to function correctly, pin P1_2 drive mode must be set by FW to high impedance digital, and the P1_2 input buffer must be configured by FW for LVTTL input thresholds.
CY8C38XXAXI-0XX TQFP100
CapSense
CapSense Button 9mm Round
THERMOCOUPLE BINDING POST
Attach negative lead of Type K thermocouple to P3.
USB MINI B
LED GREEN
Pin Jack Red
Pin Jack Black
PCB: PDCR-9511
PCA: 121R-51100
CYPRESS SEMICONDUCTOR © 2009
CY8CKIT-007 PSoC®3 Precision Analog Voltmeter Demo Kit
Document Number REF-14921
Monday, September 14, 2009