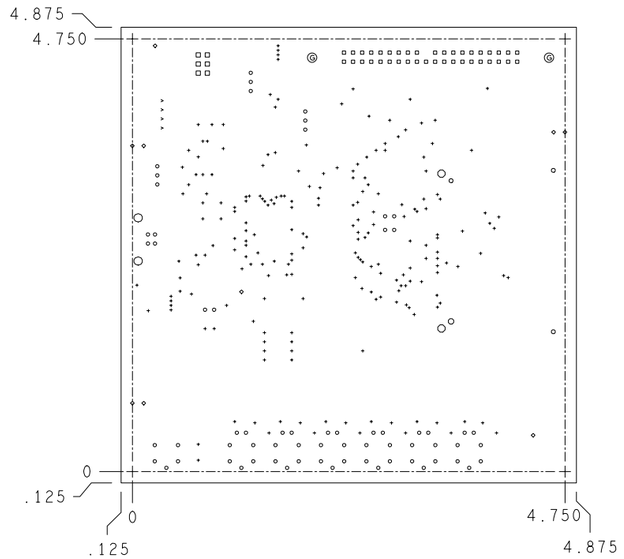
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
**	RELEASE FOR MANUFACTURING	09/09/04	SYK
*A	ADDED J16,J17&R67. CHANGED LAYER STACK UP DETAIL.	10/04/04	SYK

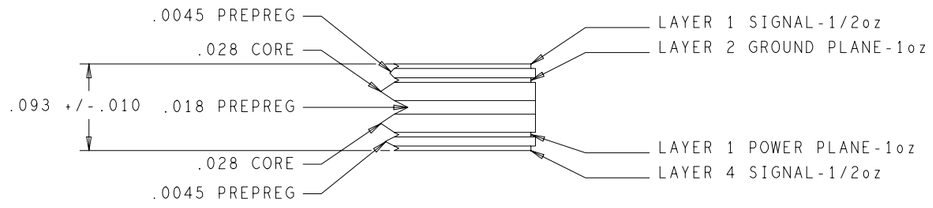


DRILL CHART			
All Units are in mils			
FIGURE	SIZE	PLATED	QTY
·	10.0	PLATED	204
·	31.0	PLATED	10
▫	35.0	PLATED	39
·	37.0	PLATED	4
◦	40.0	PLATED	66
◇	43.0	PLATED	9
▫	47.0	PLATED	6
○	94.0	PLATED	2
◦	44.0	NOT PLATED	3
○	64.0	NOT PLATED	1
○	87.0	NOT PLATED	2
◎	106.0	NOT PLATED	2
+	125.0	NOT PLATED	4

+.003
-.010

NOTES:UNLESS OTHERWISE SPECIFIED

1. MATERIAL:
   A. EPOXY GLASS LAMINATE AND PREPREG, TYPE FR-4.
   B. USE HTE COPPER 1/2 oz. COPPER FOIL EXTERNAL LAYERS AND 1 oz. COPPER FOIL FOR INTERNAL LAYERS.UNLESS OTHERWISE SPECIFIED IN STACK VIEW.
   C. OVERALL METAL TO METAL THICKNESS .093 +/- 10%
2. DRILLING:
   A. DIAMETERS IN DRILL TABLE ARE FINISHED HOLE SIZES +/- .003. UNLESS OTHERWISE SPECIFIED IN DRILL TABLE.
   B. TEARDROP ALLOWED ON ENTRY OF VIA ON EVERY TRACE LAYER.
3. PLATING:
   A. COPPER PLATING IN THRU-HOLES .001 min.
4. MARKING:
   A. SILKSCREEN IN WHITE NON-CONDUCTIVE EPOXY INK ON PRIMARYSIDE OF THE BOARD OR BOTH SIDES IF APPLICABLE.
   B. FABRICATIOR TO ETCH DATE CODE AND LOGO ON SECONDARY SIDE.
5. FINAL FABRICATION:
   A. SOLDERMASK PRIMARY AND SECONDARY SIDE OF BOARD USING LIQUID PHOTOIMAGABLE MASK MATERIAL OVER BARE COPPER PER IPC-SM-840. MASK ARTWORKS PROVIDED ARE 1:1 SOLDERMASK COLOR **GREEN**.
6. WHEN REQUIRED, COPPER FINISH SHALL BE FINISHED TIN-LEAD (60-40 MIN) HASL.
7. BOARDS SHALL BE PURCHASED FROM UL RECOGNIZED VENDORS ONLY AND SHALL BE MARKED IN COPPER ON SECONDARY SIDE OF BOARD WITH VENDORS UL IDENTIFICATION. FLAMABILITY RATING (94-VO). AND DATE CODE (WWYY).
8. MANUFACTURE BOARD TO BE IN ACCORDANCE WITH PERFORMACE STANDARD IPC-6011/6012 CLASS 2 BOARD TO BE INSPECTED PER IPC-600-A CLASS 2.
9 MAXIMUM WRAP OR TWIST SHALL NOT EXCEED .010 in/in.



NOTE:9 MIL PAIRED TRACES ON LAYER 1 DIFFERENTIAL IMPEDANCE CONTROLLED TO 90 OHMS +/- 10% TESTED

UNLESS OTHERWISE SPECIFIED	SIGNATURES	DATE
DIMENSIONS ARE IN INCHES	DRAWN ZMK	09/09/04
TOLERANCES ON; ANGLES +/- 2°	CHECKED AEJ	09/09/04
2 PL DECIMALS +/- .010	ENGRG SYK	09/09/04
3 PL DECIMALS +/- .005	ISSUED	
THIRD ANGLE PROJECTIONS		

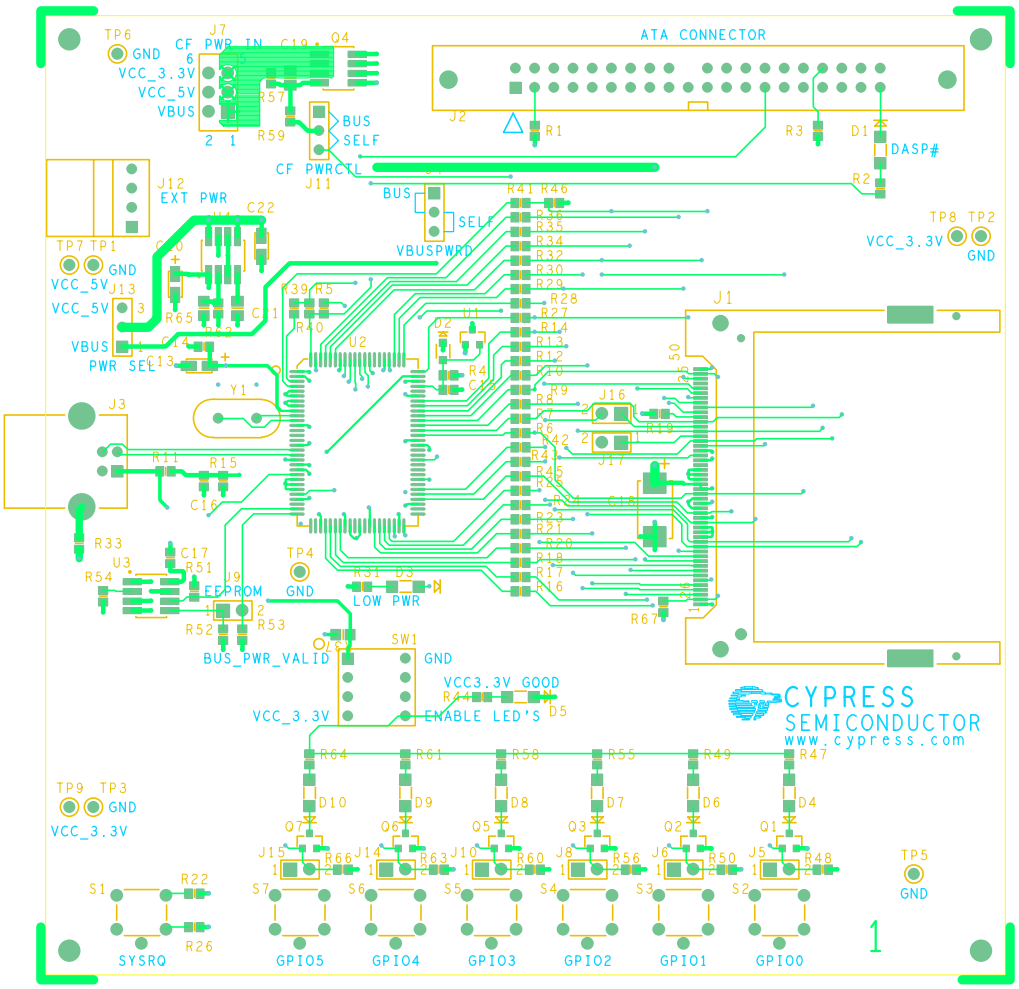
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FABRACATION DRAWING AT2LP DEVELOPMENT KIT BOARD

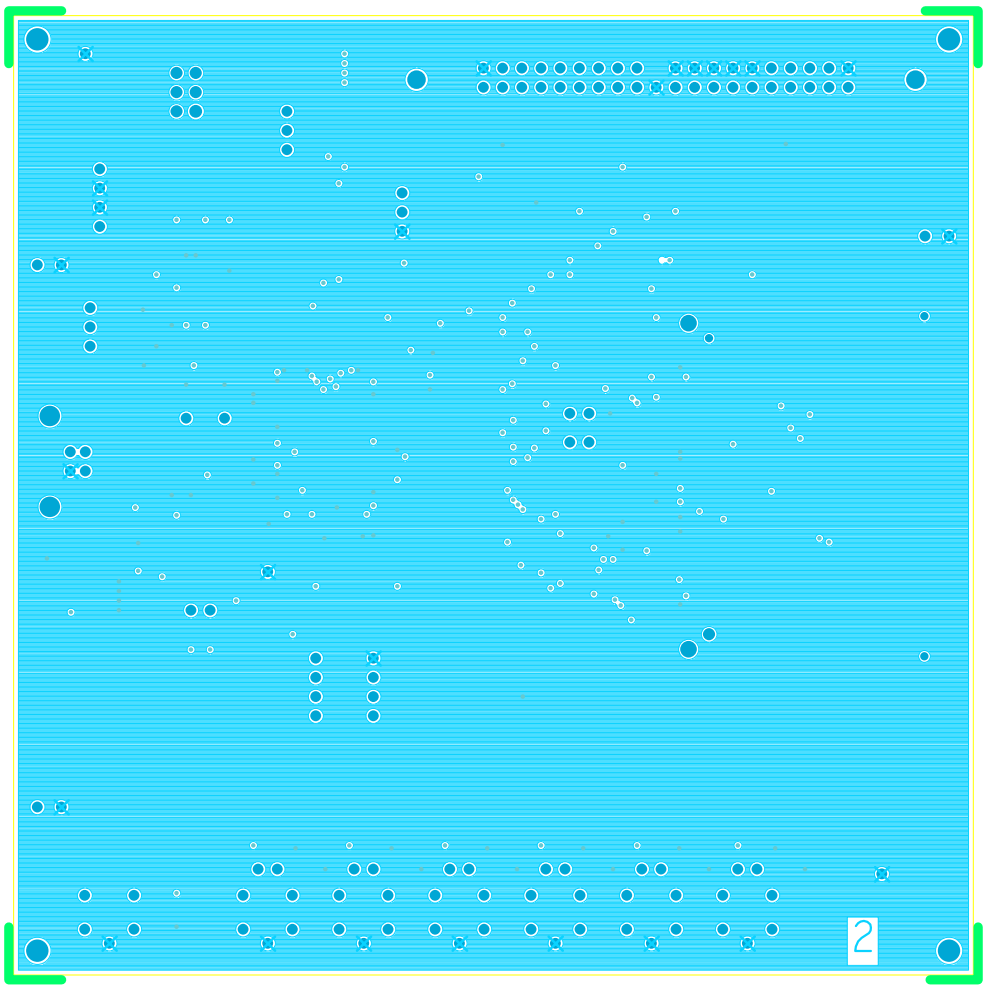
SIZE	FSCM NO	DWG NO	REV
C		PDC-9265	*A

SCALE: 1/1 SHEET:

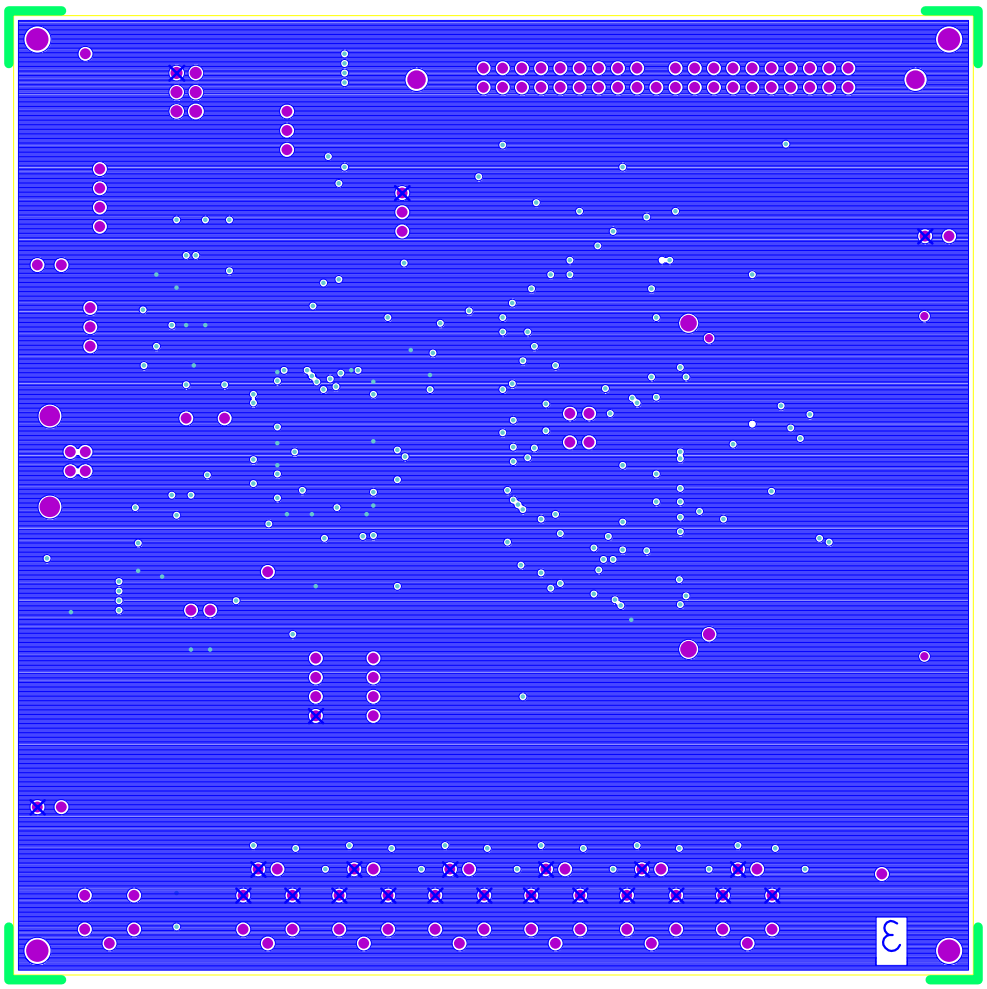


PDC-9265 REV *A PRIMARY SIDE

PDC-9265 REV *A PRIMARY SILKSCREEN



PDC-9265 REV*A LAYER 2 GND



PDC-9265 REV*A LAYER 3 VCC

PDC-9265 REV *A SECONDARY SIDE

PDC-9265 REV *A SECONDARY SILKSCREEN