

# CIPURSE™4move

## Datasheet

CIPURSE™-based dedicated security controller for cost-optimized tickets, cards, and wearables in transport ticketing, physical access, micro-payment, and multi-applications

## Key features

- **Open Standard, CIPURSE™S Profile** compliant
  - Up to **8 CIPURSE™ applications** configurable
  - Up to **8 128-bit AES keys** may be assigned to the CIPURSE™ ADF
  - **4 PxSE ADF** configurable
  - **Secured communication** using AES-128 and session key derivation
  - **Mutual authentication** using AES-128
- **1/2/4 KB user memory** for application data storage
- **Ready-to-use for personalization**
- Support of **legacy systems**:
  - Optional 1 KB and 4 KB block oriented memory with NRG™ operation
  - **Legacy to CIPURSE™ migration (L2C)**
- **Limited refund** offering a decrease/increase of the Value Record file limited to the value of the preceding increase/decrease operation
- **ISO/IEC 14443 Type A contactless interface**
- **Chip capacitance values of 27/56/78 pF** supporting various antenna form factors
- **CC EAL 5+ (high), CIPURSE™ certified**

## Potential applications

Optimized for **secure multi-application smart city and mobility cards**

## About this document

### Scope and purpose

This document describes the features, functionality, and operational characteristics of SLS 32TLC00xS(M).

### Intended audience

This document is primarily intended for system and application designers.

*Note: For more details, CIPURSE™4move Extended Datasheet available under NDA can be requested from Infineon Technologies.*

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## 1 Introduction

### 1 Introduction

CIPURSE™4move is a dedicated security controller for cost-optimized tickets, cards, and wearables in transport ticketing, physical access, micro-payment, and multi-applications featuring CIPURSE™ functionality and optional block oriented memory with NRG™ operation. It is therefore the ideal migration product to migrate existing NRG™ systems towards more advanced and state of the art CIPURSE™ security based on AES-128.

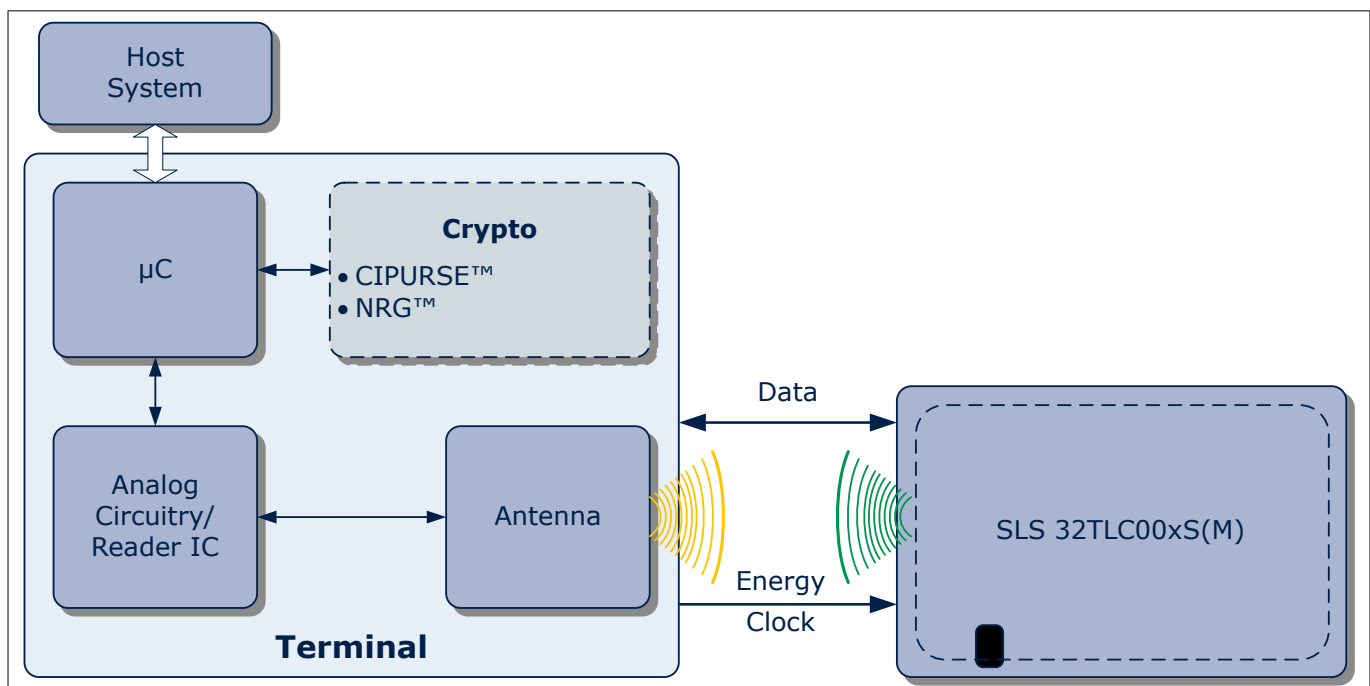
#### 1.1 System overview

CIPURSE™4move is designed to operate both in a CIPURSE™ and in an NRG™ system. The product, in the following also denoted as proximity integrated circuit card (PICC), is connected to a terminal, in the following also denoted as proximity coupling device (PCD), via contactless interface providing both energy for operation and data exchange. The terminal is application specific and may be either connected to a host system (online terminal) or work standalone (offline terminal).

After anticollision and selection as per ISO/IEC 14443-3 [9], the PCD may proceed as follows:

- Enter the NRG™ operation state by performing the authentication procedure to any of the sectors by sending the command AUTHENTICATE
- or
- Enter ISO/IEC 14443-4 [10] transmission protocol processing (T=CL) by sending a request for answer to select (RATS) command

See [Chapter 4.1](#) for details on further steps to operate in CIPURSE™ or NRG™ mode.



**Figure 1** System overview

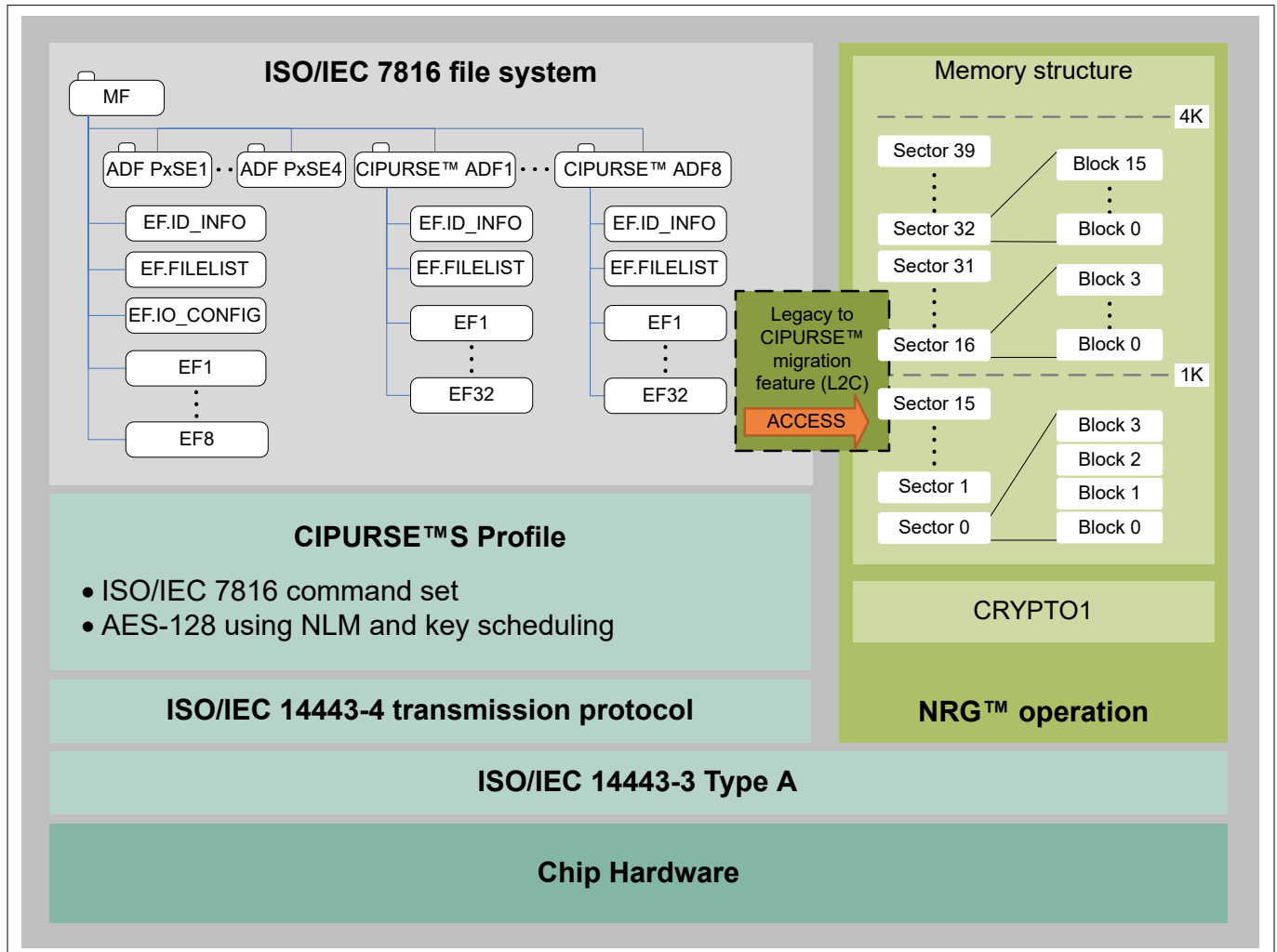
#### 1.2 Product overview

CIPURSE™4move is a cost-efficient implementation and designed for use in automatic fare collection systems, micro-payment, as access control token, and other smart card security applications. As a migration product, it also offers 1 KB and 4 KB block oriented memory with NRG™ operation. It is operated using the ISO/IEC 14443 Type A contactless interface.

The product allows handling a typical ticketing transaction in less than 100 ms. It is also suited for use in multi-application schemes, for example combining a transportation fare collection scheme and a ticketing

## 1 Introduction

system such as stadium ticketing. Further, the product offers robust contactless transmission which means that the card with CIPURSE™4move may also remain in the wallet of the user even if there are coins in it.



**Figure 2** Block diagram of CIPURSE™4move

### General features

- Support of 1/2/4 KB of user memory
- Optional support of 1 KB and 4 KB block oriented memory with NRG™ operation
- 27/56/78 pF chip input capacitance
- Operating temperature range: -25°C to +85°C (for chip)<sup>1)</sup>
- Storage temperature range: -40°C to +125°C (for chip)<sup>1)</sup>

### CIPURSE™application security

CIPURSE™4move supports:

- Up to 8 128-bit Advanced Encryption Standard (AES) keys can be assigned to each application dedicated file (ADF)
- Flexible access rights and secure messaging rules configurable for each file
- Mutual authentication using AES-128
- Secure messaging with AES-message authentication code (MAC) and AES-encryption (ENC)

<sup>1</sup> For modules according to module specification

## 1 Introduction

- Secure messaging mode configurable for each data exchange
- Secure channel protocol inherently differential power analysis (DPA) and differential fault analysis (DFA) resistant, offering AES-MAC, AES-ENC and sequence integrity protection for application protocol data units (APDUs)
- Administrative functionality
  - 8 128-bit AES keys available for master file (MF) administration
  - MF security architecture is same as CIPURSE™ ADF security architecture

### ISO/IEC 7816-4 file system

CIPURSE™4move implements a CIPURSE™ compliant file system based on ISO/IEC 7816-4 [4]:

- Files are organized logically in form of two-level dedicated file (DF) tree structure (as described in [Chapter 3.1](#))
- The MF forms the root of this structure. The MF hosts some predefined elementary files (EFs), up to 8 custom EFs, and up to 8 custom ADFs
- Support up to 4 ADF proximity system environments (PxSEs), in addition to 8 custom ADFs under the MF
- A CIPURSE™ application is represented by an ADF identified by its file identifier (FID) and DF name application identifier (AID). The ADF can host up to 32 custom EFs for application specific data
- Elementary file types supported are binary files, linear record files, cyclic record files, and linear value-record files
  - File size up to 4 KB
  - Up to 254 records per record oriented file
  - Record length up to 228 bytes
- Security attributes defining the access rights and secure messaging rules can be assigned to each ADF, to the MF, and to each EF
- Up to 64 bytes for proprietary security information per ADF
- Up to 64 bytes for proprietary security information for MF
- Up to 1/2/4 KB user memory is available to store an application data. Customers can configure the number of available ADFs, EFs, and the corresponding file size. The maximum file size of one EF is 4 KB

### Block oriented memory with NRG™ operation features

As a migration product, CIPURSE™4move is designed to operate in an NRG™ system to support the migration towards more advanced CIPURSE™ security based on AES-128. In addition, the support of NRG™ can be modified (see [Chapter 3.1.4.3](#)).

- **SLS 32TLC00xS/SLS 32TLC00xS5/SLS 32TLC00xSA** – NRG™ operation not supported
- **SLS 32TLC00xS1/SLS 32TLC00xS6/SLS 32TLC00xSB** – supporting 1 KB block oriented memory with NRG™ operation
  - 16 sectors of 64 bytes (4 blocks)
- **SLS 32TLC00xS4/SLS 32TLC00xS9/SLS 32TLC00xSE** – supporting 4 KB block oriented memory with NRG™ operation
  - 32 sectors of 64 bytes (4 blocks)
  - 8 sectors of 256 bytes (16 blocks)
- Two keys per sector
- Mutual three pass authentication
- Encrypted data transfer

### Near field communication (NFC) Forum Type 4 Tag

Supports NFC Forum Type 4 Tag functionality, see [Chapter 3.1.2.3](#).



## **1 Introduction**

### **CIPURSE™ command set**

- Multi-level commands
  - SELECT
- Commands for personalization of file system oriented PICCs
  - CREATE\_FILE
  - DELETE\_FILE
  - FORMAT\_ALL
- Commands for object management
  - ACTIVATE\_FILE (ADF)
  - DEACTIVATE\_FILE (ADF)
- Commands for file attribute management
  - READ\_FILE\_ATTRIBUTES
  - UPDATE\_FILE\_ATTRIBUTES
  - UPDATE\_KEY
  - UPDATE\_KEY\_ATTRIBUTES
- Security-related commands
  - MUTUAL\_AUTHENTICATE
  - GET\_CHALLENGE
- Commands for file data management
  - READ\_BINARY
  - UPDATE\_BINARY
  - READ\_RECORD
  - UPDATE\_RECORD
  - APPEND\_RECORD
  - READ\_VALUE
  - INCREASE\_VALUE
  - DECREASE\_VALUE
  - LIMITED\_INCREASE\_VALUE
  - LIMITED\_DECREASE\_VALUE

### **Contactless interface**

- Initialization and anticollision according to ISO/IEC 14443-3 [\[9\]](#) Type A using 4-byte reused-ID, 7-byte unique identifier (UID) (Double-Size UID), 10-byte UID (Triple-Size UID), or 4-byte random identification (ID) as defined in ISO/IEC 14443-3 [\[9\]](#)
- Transmission protocol according to ISO/IEC 14443-4 [\[10\]](#)
- Data rates in both directions up to 848 kbit/s

### **Security features**

- Active shield technology
- Anti-snooping features
- Security attack countermeasures for all critical operations using both hardware and software controls
- Access limitation for manufacturer-specific data (configurable)

### **Certification level**

- CIPURSE™V2 certification
- CC EAL 5+ (high)

## **1 Introduction**

### **1.3 Coding and notation conventions**

All lengths are represented in bytes, unless otherwise specified.

Each byte is represented by bits  $b[8:1]$ , where  $b[8]$  is the most significant bit and  $b[1]$  is the least significant bit, unless otherwise specified.

Multi-byte fields and values are presented in big endian order, unless otherwise specified.

Binary values are specified in brackets with suffix "B" (For example,  $0101_B$ ).

Hexadecimal values are specified with suffix "H" (For example,  $B4_H$ ).

## 2 Ordering and packaging information

## 2 Ordering and packaging information

*Note:* The ordering codes for the individual sales code and package combination (For example, SLS 32TLCxxx – MCC8) are available on request.

**Table 1**                      **Ordering information**

Type <sup>1)</sup>	Package
<b>No block oriented memory with NRG™ support, 27 pF chip capacitance</b>	
SLS 32TLC00xS – MCC8	P-MCC8-2-6 <sup>2)</sup>
SLS 32TLC00xS – MCS8	P-MCS-8-2-1 <sup>3)</sup>
SLS 32TLC00xS – NB	Unsawn/Sawn wafer, NiAu bump <sup>4)</sup>
SLS 32TLC00xS – C	Unsawn/Sawn wafer, without bump <sup>5)</sup>
<b>No block oriented memory with NRG™ support, 56 pF chip capacitance</b>	
SLS 32TLC00xS5 – MCC8	P-MCC8-2-6 <sup>2)</sup>
SLS 32TLC00xS5 – MCS8	P-MCS-8-2-1 <sup>3)</sup>
SLS 32TLC00xS5 – NB	Unsawn/Sawn wafer, NiAu bump <sup>4)</sup>
SLS 32TLC00xS5 – C	Unsawn/Sawn wafer, without bump <sup>5)</sup>
<b>No block oriented memory with NRG™ support, 78 pF chip capacitance</b>	
SLS 32TLC00xSA – MCC8	P-MCC8-2-6 <sup>2)</sup>
SLS 32TLC00xSA – MCS8	P-MCS-8-2-1 <sup>3)</sup>
SLS 32TLC00xSA – NB	Unsawn/Sawn wafer, NiAu bump <sup>4)</sup>
SLS 32TLC00xSA – C	Unsawn/Sawn wafer, without bump
<b>1 KB block oriented memory with NRG™ support, 27 pF chip capacitance</b>	
SLS 32TLC00xS1 – MCC8	P-MCC8-2-6 <sup>2)</sup>
SLS 32TLC00xS1 – MCS8	P-MCS-8-2-1 <sup>3)</sup>
SLS 32TLC00xS1 – NB	Unsawn/Sawn wafer, NiAu bump <sup>4)</sup>
SLS 32TLC00xS1 – C	Unsawn/Sawn wafer, without bump <sup>5)</sup>
<b>1 KB block oriented memory with NRG™ support, 56 pF chip capacitance</b>	
SLS 32TLC00xS6 – MCC8	P-MCC8-2-6 <sup>2)</sup>
SLS 32TLC00xS6 – MCS8	P-MCS-8-2-1 <sup>3)</sup>
SLS 32TLC00xS6 – NB	Unsawn/Sawn wafer, NiAu bump <sup>4)</sup>
SLS 32TLC00xS6 – C	Unsawn/Sawn wafer, without bump <sup>5)</sup>
<b>1 KB block oriented memory with NRG™ support, 78 pF chip capacitance</b>	
SLS 32TLC00xSB – MCC8	P-MCC8-2-6 <sup>2)</sup>
SLS 32TLC00xSB – MCS8	P-MCS-8-2-1 <sup>3)</sup>
SLS 32TLC00xSB – NB	Unsawn/Sawn wafer, NiAu bump <sup>4)</sup>
SLS 32TLC00xSB – C	Unsawn/Sawn wafer, without bump <sup>5)</sup>
<b>4 KB block oriented memory with NRG™ support, 27 pF chip capacitance</b>	
SLS 32TLC00xS4 – MCC8	P-MCC8-2-6 <sup>2)</sup>
SLS 32TLC00xS4 – MCS8	P-MCS-8-2-1 <sup>3)</sup>

**(table continues...)**

## 2 Ordering and packaging information

**Table 1** (continued) Ordering information

Type <sup>1)</sup>	Package
SLS 32TLC00xS4 – NB	Unsawn/Sawn wafer, NiAu bump <sup>4)</sup>
SLS 32TLC00xS4 – C	Unsawn/Sawn wafer, without bump <sup>5)</sup>
<b>4 KB block oriented memory with NRG™ support, 56 pF chip capacitance</b>	
SLS 32TLC00xS9 – MCC8	P-MCC8-2-6 <sup>2)</sup>
SLS 32TLC00xS9 – MCS8	P-MCS-8-2-1 <sup>3)</sup>
SLS 32TLC00xS9 – NB	Unsawn/Sawn wafer, NiAu bump <sup>4)</sup>
SLS 32TLC00xS9 – C	Unsawn/Sawn wafer, without bump <sup>5)</sup>
<b>4 KB block oriented memory with NRG™ support, 78 pF chip capacitance</b>	
SLS 32TLC00xSE – MCC8	P-MCC8-2-6 <sup>2)</sup>
SLS 32TLC00xSE – MCS8	P-MCS-8-2-1 <sup>3)</sup>
SLS 32TLC00xSE – NB	Unsawn/Sawn wafer, NiAu bump <sup>4)</sup>
SLS 32TLC00xSE – C	Unsawn/Sawn wafer, without bump <sup>5)</sup>

- 1) x indicates the user memory size of 1 KB or 2 KB or 4 KB, respectively  
2) Pure contactless module (MCC8): for standard thickness inlays (330 µm)  
3) Pure contactless module (MCS8): for very thin inlays (< 250 µm)  
4) Wafer thickness: 55 µm, 75 µm, and 150 µm with NiAu bump 20 µm  
5) Wafer thickness: 55 µm, 75 µm, and 150 µm

**Table 2** UID configuration

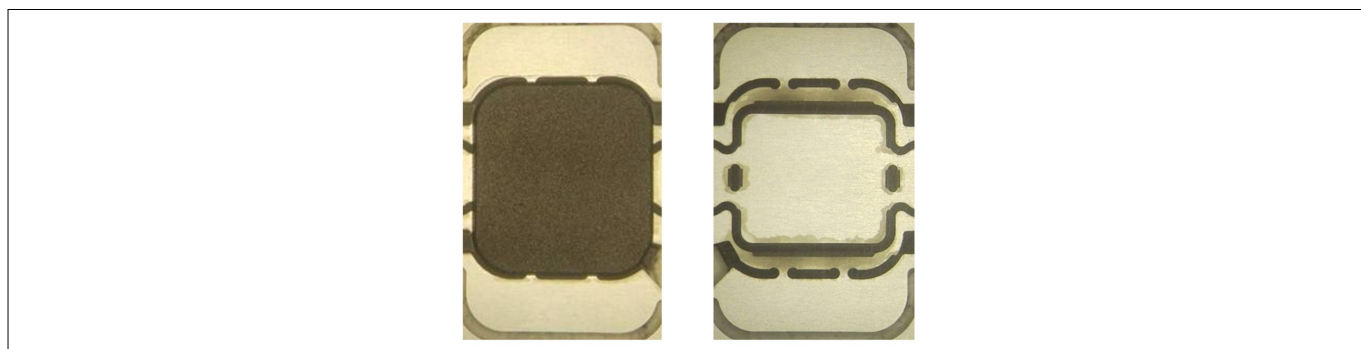
Type	Delivery state	User configurable <sup>1)</sup>
SLS 32TLC00xS/ SLS 32TLC00xS5/ SLS 32TLC00xSA	7-byte UID	7-byte UID, 10-byte UID, and 4-byte random ID
SLS 32TLC00xS1/ SLS 32TLC00xS6/ SLS 32TLC00xSB	4-byte reused-ID (xM band <sup>2)</sup> )	4-byte reused-ID, 7-byte UID, 10-byte UID, and 4-byte random ID
SLS 32TLC00xS4/ SLS 32TLC00xS9/ SLS 32TLC00xSE	4-byte reused-ID (xM band <sup>2)</sup> )	4-byte reused-ID, 7-byte UID, 10-byte UID, and 4-byte random ID

- 1) The other UID variants can be configured by the customer. For more details, see [Chapter 3.1.4.3](#).  
2) M = 1, 5, 7, 9. Other values might be applicable without further notice

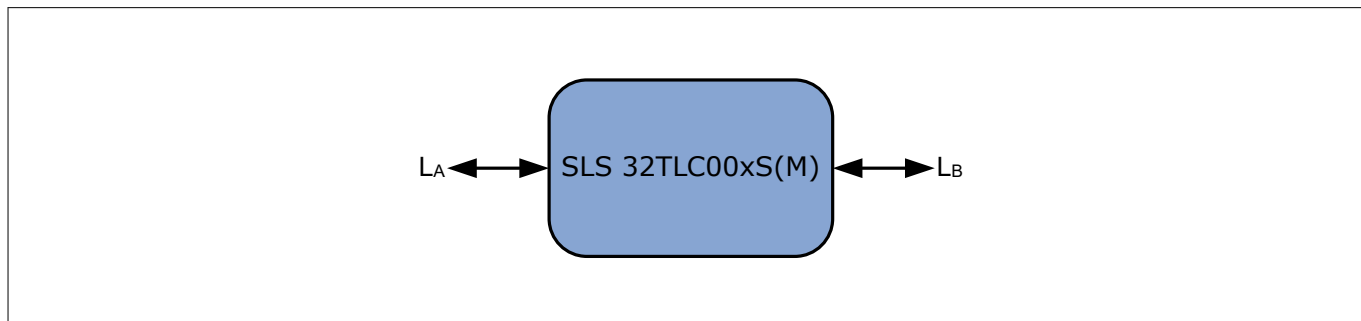
## 2 Ordering and packaging information



**Figure 3**      **Module contactless card - P-MCC8-2-6**



**Figure 4**      **Module contactless card - P-MCS-8-2-1 (top/bottom view)**



**Figure 5**      **Pin configuration**

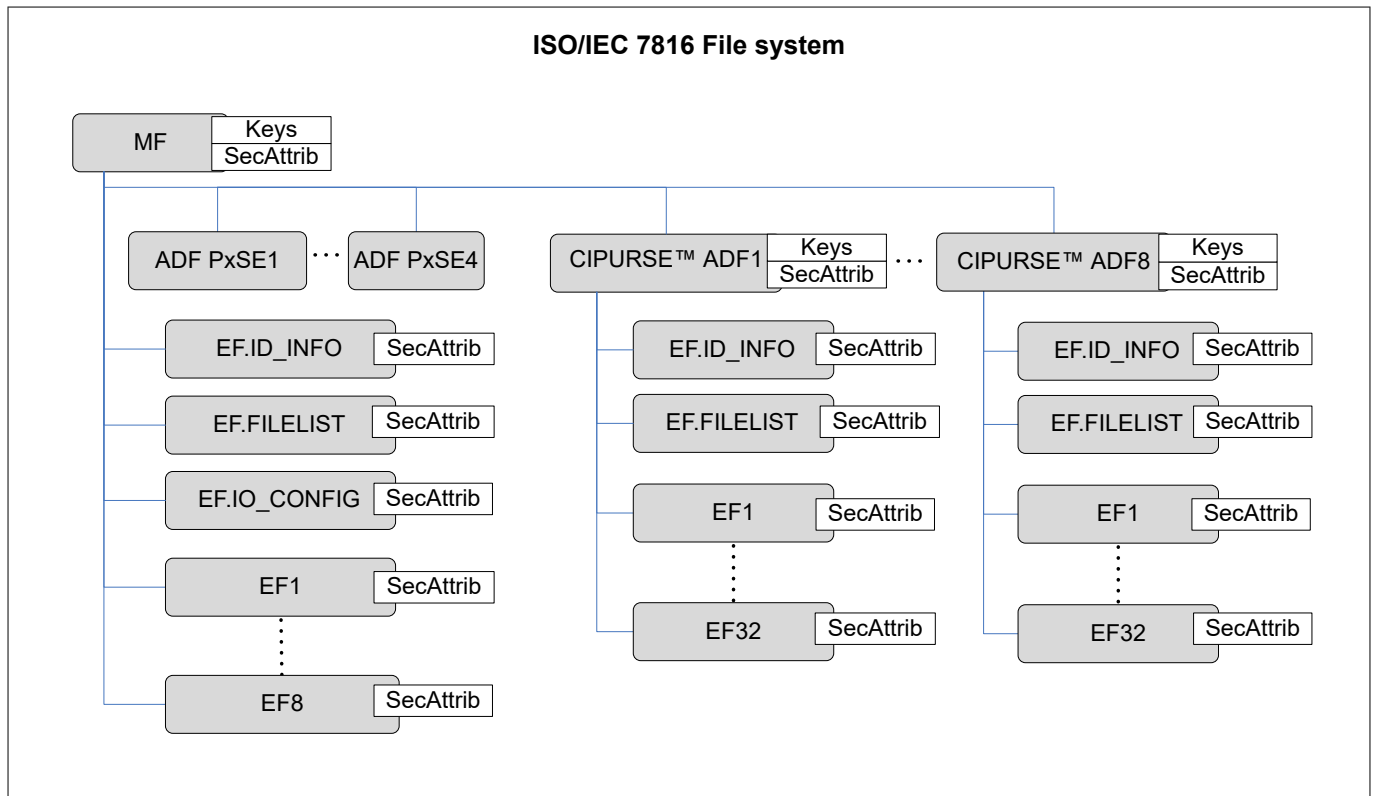
**Table 3**      **Pin definitions and functions**

Symbol	Function
$L_A$	Coil connection pin $L_A$
$L_B$	Coil connection pin $L_B$

## 3 CIPURSE™ application support

### 3.1 File system of the PICC

The file system implemented by the product is compliant to the file system specified in ISO/IEC 7816-4 [4]. For example, Figure 6 shows the structure of the file system containing a number of CIPURSE™V2 applications and up to 4 PxSE applications.



**Figure 6 Example of a CIPURSE™4move file system structure**

For application operation, the files in the file system are organized logically in a form of two-level dedicated file (DF) tree structure. The MF forms the root of this structure.

The MF hosts three predefined EFs and 8 128-bit AES keys and it allows creation of up to 8 custom EFs, up to 4 ADF PxSEs, and up to 8 custom ADFs excluding ADF PxSEs (if created).

A CIPURSE™ application is represented by an ADF identified by its FID and AID. The ADF hosts two predefined EFs and up to 8 128-bit AES keys and it allows creation of up to 32 EFs.

A PxSE ADF is a specific application, which is created without child files and security attributes.

Security attributes defining the access rights and secure messaging rules may be assigned to each CIPURSE™ ADF, to the MF, and to each EF. The file system offers up to 4 KB memory to store the user data.

#### 3.1.1 Master file

MF consists of keys, security attributes, and hosts custom ADFs (see Chapter 3.1.2) in addition to pre-defined EFs (see Chapter 3.1.4) and custom EFs (see Chapter 3.1.3).

The PICC supports implicit selection of the MF as a result of radio frequency (RF) initialization and anticollision process.

MF supports the following commands:

- CREATE\_FILE (ADF/EF)

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**3 CIPURSE™ application support**

- DELETE\_FILE (ADF/EF)
- FORMAT\_ALL
- GET\_CHALLENGE
- MUTUAL\_AUTHENTICATE
- UPDATE\_KEY
- UPDATE\_KEY\_ATTRIBUTES
- READ\_FILE\_ATTRIBUTES
- UPDATE\_FILE\_ATTRIBUTES
- SELECT (by FID/AID)

**3.1.2 Application dedicated files**

An ADF is identified by its AID or by its FID.

PICC supports three type of ADFs:

- CIPURSE™ ADF
- PxSE ADF
- NFC Type 4 Tag ADF

CIPURSE™4move allows CIPURSE™ ADF or NFC Type 4 Tag ADF to access NRG™ sectors, assigned during the creation of the respective ADF by providing NRG™ sector assignment information (5-byte bitmap for 4 KB NRG™ and 2-byte bitmap for 1 KB NRG™). The product allows multiple ADFs to access same NRG™ sector.

READ\_FILE\_ATTRIBUTE on the ADF assigned with NRG™ sectors returns the assigned sector information (bitmap) as part of the ADF file attributes.

**3.1.2.1 CIPURSE™ ADF**

CIPURSE™ ADF consists of keys and security attributes, and it hosts the EFs with application-specific data as described in [Chapter 3.1.3](#) in addition to pre-defined EFs (see [Chapter 3.1.4](#)).

CIPURSE™ ADF can be secured or unsecured based on the security attributes defining access conditions and secure messaging, and key values as described in [Chapter 3.2](#).

CIPURSE™ ADF supports two operational states:

- ACTIVATED
- DEACTIVATED

Command ACTIVATE\_FILE (ADF) activates the referenced CIPURSE™ ADF (and inherently all its child EFs) from its deactivated state.

An activated CIPURSE™ ADF supports the following commands:

- CREATE\_FILE (EF)
- DELETE\_FILE (this ADF/EF)
- GET\_CHALLENGE
- MUTUAL\_AUTHENTICATE
- UPDATE\_KEY
- UPDATE\_KEY\_ATTRIBUTES
- READ\_FILE\_ATTRIBUTES
- UPDATE\_FILE\_ATTRIBUTES
- SELECT (by FID/AID)
- DEACTIVATE\_FILE (ADF)

Command DEACTIVATE\_FILE (ADF) deactivates the activated CIPURSE™ ADF (and implicitly all its child EFs).

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**3 CIPURSE™ application support**

A deactivated CIPURSE™ ADF supports the following operational commands:

- SELECT (by FID/AID)
- ACTIVATE\_FILE (subject to access condition)
- GET\_CHALLENGE
- MUTUAL\_AUTHENTICATE

**3.1.2.2 PxSE ADF**

PxSE application registers the segment specific CIPURSE™ applications such as dedicated to transport applications, event ticketing applications, and facility access applications.

PxSE application supports the SELECT (by AID) command only.

The response to SELECT PxSE provides the list of AIDs corresponding to its registered CIPURSE™ applications in ACTIVATED state and one of its registered applications might be implicitly selected.

**3.1.2.3 NFC Type 4 Tag ADF**

The product supports an NFC Type 4 Tag ADF [\[11\]](#) with the same functionality as a CIPURSE™ ADF with the following exceptions during ADF creation:

- EF.ID\_INFO is not automatically created
- EF.FILELIST is not automatically created

The creation of EF with the same FID as EF.ID\_INFO or EF.FILELIST is not allowed.

**3.1.3 Supported elementary file types**

EFs are used to store data and are identified by its FID or by short file identifier (SFID).

The file system supports the following elementary file types:

- Binary file
- Linear record file
- Cyclic record file
- Linear value-record file
- NRG™ mapped linear record file

EFs can be secured or unsecured based on the security attributes as described in [Chapter 3.2](#).

The commands READ\_FILE\_ATTRIBUTES and UPDATE\_FILE\_ATTRIBUTES can be used to read and update the EF attributes.

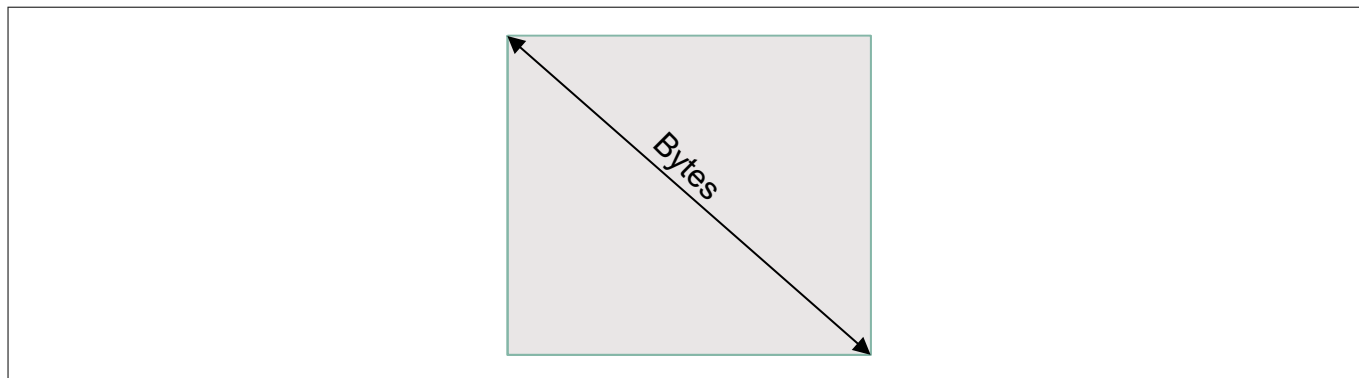
**Binary file:**

A binary file represents a series of sequential bytes without specific inner structure. Size of the file is defined at file creation.

On file creation, the data are created and initialized with zeros. The commands READ\_BINARY and UPDATE\_BINARY can be used to read and update the records.

The maximum size of the binary file is restricted to 4 KB.





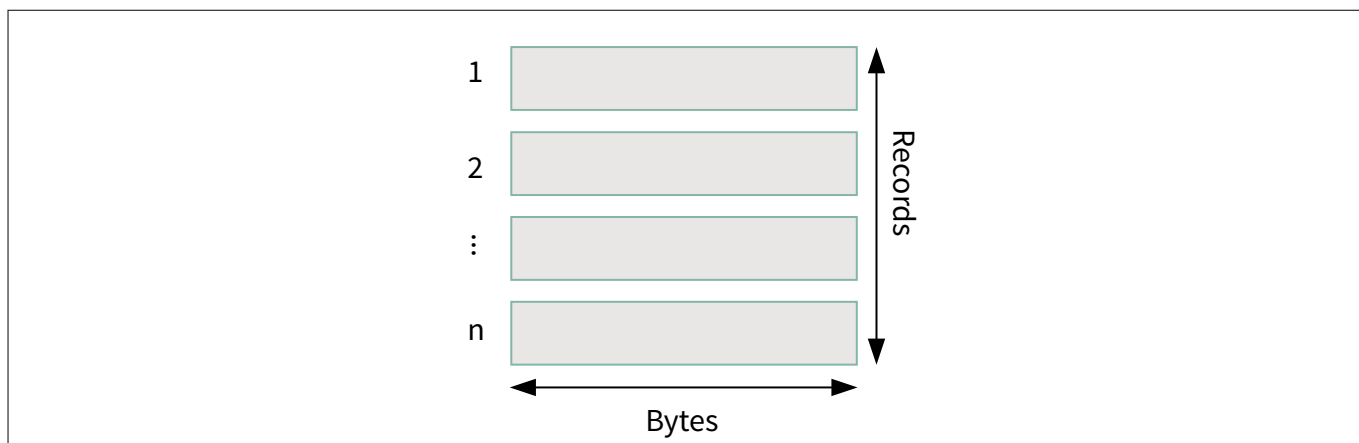
**Figure 7**      **Binary file**

**Linear record file:**

A linear record file represents a linear sequence of records of same size. Size and number of records are defined at file creation.

On file creation, all records are created and initialized with zeros. The commands READ\_RECORD and UPDATE\_RECORD can be used to read and update the records.

The maximum size of a record is 228 bytes. A file can contain maximum of 254 records. The maximum size of the linear record file (size of record x number of records) is restricted to 4 KB.



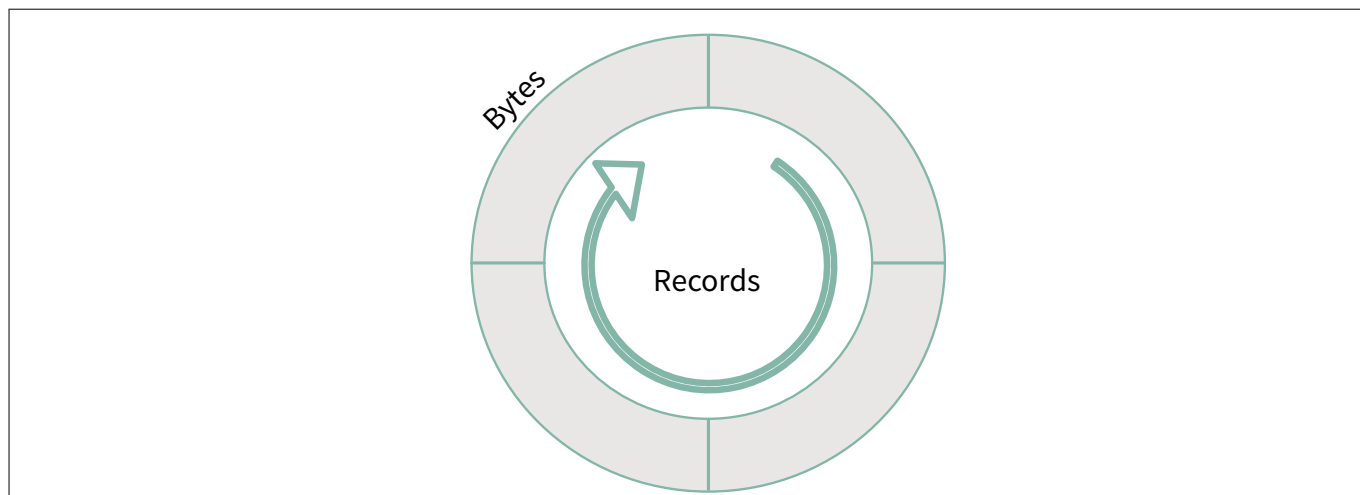
**Figure 8**      **Linear record file**

**Cyclic record file:**

A cyclic record file represents a cyclic sequence of records, where the oldest data will be overwritten, in case the list is full. The size and number of the records are defined at file creation.

On file creation, only the memory is reserved. No further initialization is performed. Each record must be created and initialized using command APPEND\_RECORD before it can be read or updated. The commands READ\_RECORD and UPDATE\_RECORD can be used to read and update the records.

The maximum size of a record is 228 bytes. A file can contain maximum of 254 records. The maximum size of the cyclic record file (size of record x number of records) is restricted to 4 KB.



**Figure 9**      **Cyclic record file**

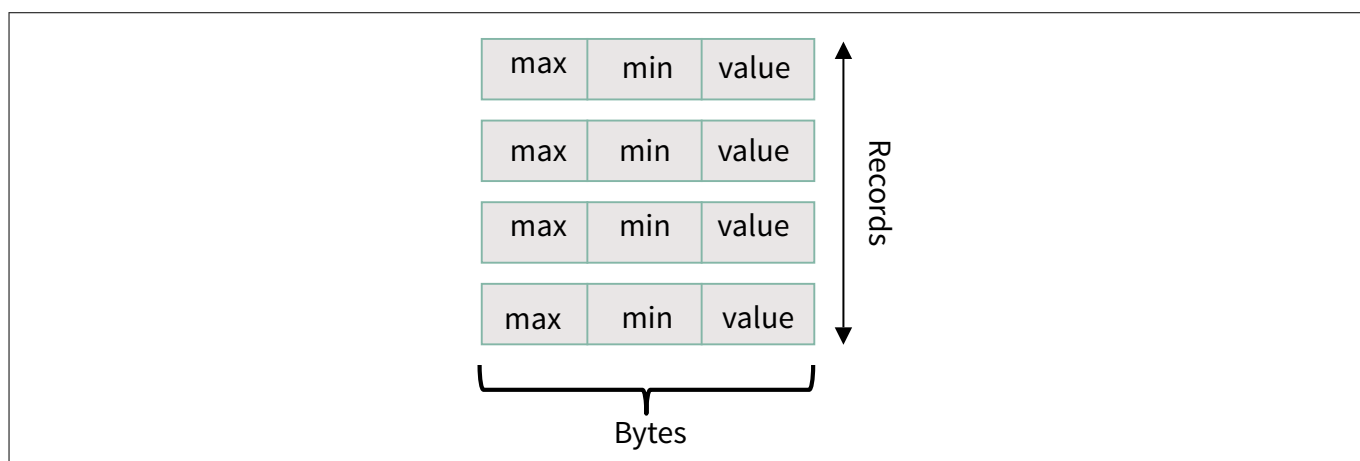
#### **Value-record file:**

A value-record file represents a linear sequence of records of 12 bytes. Each value-record contains maximum and minimum limit and a counter value field. Number of records is defined at file creation.

On file creation, all records are created and initialized with 0000 0000<sub>H</sub> (counter value), 7FFF FFFF<sub>H</sub> (maximum limit), and 8000 0000<sub>H</sub> (minimum limit). The commands READ\_RECORD and UPDATE\_RECORD can be used to read and update the records. The commands READ\_VALUE, INCREASE\_VALUE, and DECREASE\_VALUE can be used to read and manipulate the counter values. If modification of the value violates the limits, the command will be rejected.

The commands LIMITED\_INCREASE\_VALUE and LIMITED\_DECREASE\_VALUE can be used to offer a refund functionality that is limited to the number of tokens decreased/increased in last transaction. The value record remembers the last increase or decrease operation and enables refund up to the value that existed before increase or decrease. The commands UPDATE\_RECORD, LIMITED\_INCREASE\_VALUE, and LIMITED\_DECREASE\_VALUE will reset the information granting limited refund functionality.

A file can contain maximum of 254 records.



**Figure 10**      **Value-record file**

#### **NRG™ mapped linear record file**

NRG™ mapped linear record file represents a linear sequence of records of 16 bytes. Each record is mapped to an NRG™ block of NRG™ sector assigned under the ADF. Such mapping can be done during the elementary file creation by providing the list of assigned NRG™ block addresses. Mapped blocks must belong to the NRG™ sectors which are assigned to the parent ADF of the EF else the creation command is rejected.

### 3 CIPURSE™ application support

Mapping an NRG™ block to multiple NRG™ mapped files and to multiple records with in an NRG™ mapped file is supported.

The READ\_FILE\_ATTRIBUTE command can be used to read the NRG™ block mapping information as part of the file attributes.

A file can contain maximum of 243 records.

#### 3.1.4 Predefined elementary files

Predefined EFs under the MF are present at delivery state, need not be created and cannot be deleted. The security attributes can be modified.

Predefined EFs under the ADF are implicitly created during ADF creation. Deletion is only possible by deleting the parent ADF. The security attributes can be modified.

**Table 4 List of predefined EFs**

File name	File type	Description
EF.FILELIST	Binary	Read-only file under the MF/ADF providing list of files under the MF/ADF
EF.ID_INFO	Binary	Read-only file under the MF/ADF providing information about the supported CIPURSE™ version and the features valid for all ADFs as well as PICC-unique manufacturer specific information
EF.IO_CONFIG	Binary	File under the MF providing information about the parameters used for contactless communication

##### 3.1.4.1 EF.FILELIST

The EF.FILELIST (under the MF/ADF) is read-only file and provides a 4-byte file information for each file present under the MF/ADF. The size of EF.FILELIST varies depending on the number of files currently present in the MF/ADF.

**Table 5 Structure and contents of EF.FILELIST**

EF.FILELIST	Type: Binary, read-only		
Content		Length [byte]	Description
File #1	FID	2	File identifier of File #1
	SFID	1	Short file identifier of File #1
	FD	1	File descriptor byte of File #1
		Var.	Further FID    SFID    FD fields...
File #n	FID	2	File identifier of File #n
	SFID	1	Short file identifier of File #n
	FD	1	File descriptor byte of File #n

##### 3.1.4.2 EF.ID\_INFO

The predefined file EF.ID\_INFO is a read-only file and is available under the MF and CIPURSE™ ADF. EF.ID\_INFO files are identical across all applications in one PICC.

### 3 CIPURSE™ application support

The structure and content of the EF.ID\_INFO file are as described [Table 6](#).

**Table 6**                      **Structure and content of EF.ID\_INFO**

EF.ID_INFO	Type: Binary, Read-only
Offset	Description
0-7	CIPURSE™ version and file system oriented personalization features are supported
8	Integrated circuit manufacturer, as per ISO/IEC 7816-6 <a href="#">[5]</a> : <ul style="list-style-type: none"> <li>05<sub>H</sub>: Infineon Technologies</li> </ul>
9-23	Chip identification data
24-32	Reserved for further manufacturer information
33	Specifies whether 1 KB or 4 KB block oriented memory with NRG™ operations are supported
34-36	Software version
37-39	Product identifier

#### 3.1.4.3 EF.IO\_CONFIG

The EF.IO\_CONFIG file under the MF contains IO configuration parameters as defined in the [Table 7](#). The IO interface configuration of the product can be modified by updating the parameters in this file.

**Table 7**                      **Structure and contents of EF.IO\_CONFIG**

EF.IO_CONFIG	Type: Binary
Offset	Description
0-1	Tag and length for contactless I/O parameters
2	Protocol type and configurable UID mode <sup>1)</sup>
3	Configuration state of block oriented memory with NRG™ operation: <ul style="list-style-type: none"> <li>Block oriented memory with NRG™ operation is deactivated</li> <li>Support for 1 KB block oriented memory with NRG™ operation is activated</li> <li>Support for 4 KB block oriented memory with NRG™ operation is activated</li> </ul>
4	Reserved for future use (RFU)
5	Interface bytes for Type A and frame size for proximity card integer (FSCI)
6	Baudrate
7	Frame waiting time integer (FWI) and start-up frame guard time integer
8	Node address (NAD) and card identifier (CID) support indicator
9-10	Tag for additional parameters. Length of this tag indicates the length of the historical bytes returned as part of answer to select (ATS). This value can be configured to be in the range 0 to 15 bytes. Default value is set to 7 bytes
11-17	Initial historical bytes: <ul style="list-style-type: none"> <li>Controller control byte</li> <li>Product identifier bytes</li> <li>Software version bytes</li> </ul>
18-25	Additional bytes to allow extending historical bytes. It is recommended to set these bytes to 00 <sub>H</sub>

1) 4-byte reused-ID, 7-byte fixed UID, 4-byte random ID, and 10-byte fixed UID

### **3.1.5 File referencing methods**

To access the data, the files in a CIPURSE™ conforming PICC can be selected by using the following methods (Explicit selection or Implicit selection).

**Explicit selection:**

- A SELECT command is used for explicit selection mode
- A different combination of the parameters along with the SELECT command will perform the explicit selection such as:
  - For explicit selection of MF, the SELECT command with FID 3F00<sub>H</sub> can be used
  - For explicit selection of ADF, the SELECT command with AID or an FID can be used
  - For explicit selection of EF, the SELECT command with FID or a command supporting addressing by SFID can be used

**Implicit selection:**

- RF initialization and anticollision process is used for implicit selection of MF
- Selection of a PxSE application may result in implicit selection of one of its registered ADFs
- Implicit selection of EF is not supported

### **3.1.6 Reserved file identifiers**

Some of the FIDs are reserved to serve a special purpose such as file identifiers of MF and pre-defined EFs.

## **3.2 Security architecture**

The security architecture of this product consists of keys representing the various roles, an authentication mechanism to check the availability of a key, and the file security attributes to grant access to entitled roles only.

The security architecture is intended to restrict the access and operations on the application's data to authorized entities only.

Before executing a command on a secured object, the PICC checks if the security requirements are met in terms of file security attributes which are access rights and secure messaging rules.

### **3.2.1 Keys**

AES-128 bit keys are used for authentication. Keys are associated to ADF/MF.

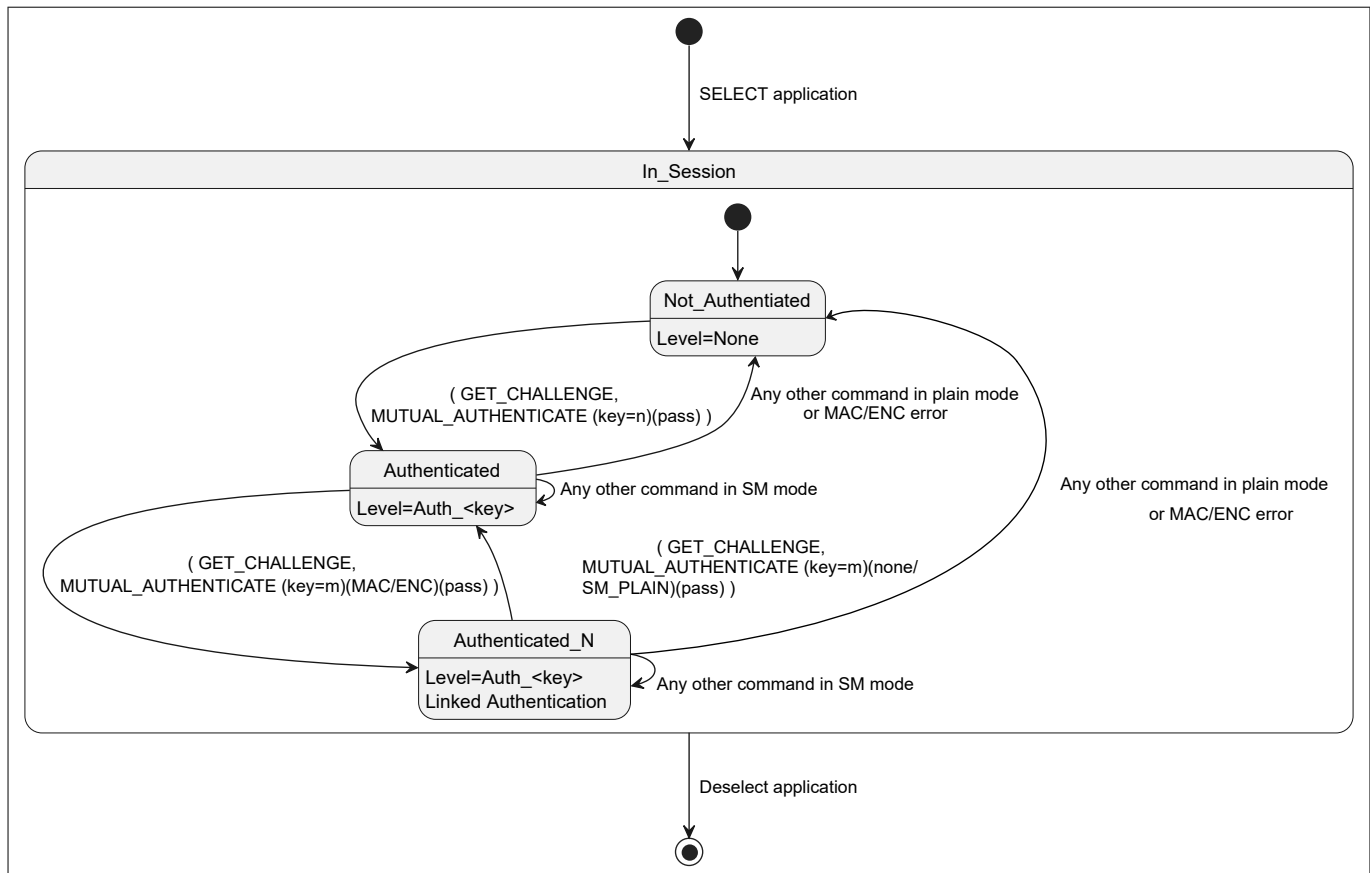
Each key has a set of secure and non-secure attributes as defined below:

- Secure key attributes are used to control the operations permissible with/on this key such as if the key can be updated or is immutable, and if the key is valid or invalid
- Non-secure key attributes hold an additional key information and cryptographic algorithm identifier

### **3.2.2 Mutual authentication and security state**

Figure 11 shows the states and resulting security levels reached when a terminal sends the commands GET\_CHALLENGE and MUTUAL\_AUTHENTICATE to mutually authenticate both terminal and PICC.

### 3 CIPURSE™ application support



**Figure 11 Authentication states and security level**

After selection of the application owning the keys, the application is in Not\_Authenticated state with security level none.

- A GET\_CHALLENGE command followed by MUTUAL\_AUTHENTICATE command with valid cryptogram results in a transition to Authenticated state with security level Auth\_<key> referencing the key number used for authentication

In Authenticated state, all commands must be transmitted in secure channel mode.

- A GET\_CHALLENGE command followed by a MUTUAL\_AUTHENTICATE command with valid cryptogram, received in SM\_MAC or SM\_ENC mode, and referencing a new key will result in Authenticated\_N state with "linked authentication" where the previous state's security level Auth\_<key> is retained and the security level will change from Auth\_<old key> to Auth\_<new key>

In Authenticated\_N state, all commands must be transmitted in secure channel mode.

- A GET\_CHALLENGE command followed by a MUTUAL\_AUTHENTICATE command with valid cryptogram, received without secure channel or secure messaging with plain data (SM\_PLAIN), will result in Authenticated state with no "linked authentication" where the security level will reset to Auth\_<new key>

Any command received in plain mode or in secure messaging (SM) mode with invalid cryptogram will reset the state to Not\_Authenticated with security level none.

When a security level Auth\_<key> is reached, the terminal acquires the right to execute the commands that are granted to this security level, as described in [Chapter 3.2.3](#).

### 3.2.3 Access rights

Access rights grant each security level rights to execute various commands respective to a file type. Also, it defines unconditional access ("ALWAYS") to enable PCDs to execute commands irrespective of the security level reached and the secure messaging rules assigned to the file, see [Chapter 3.2.4](#).

If none of the rights are enabled, the commands cannot be executed irrespective of the security level.

### 3 CIPURSE™ application support

#### 3.2.4 Secure messaging rules

Secure messaging rules (SMR) define for a file, the minimum secure messaging levels required to execute various commands respective to a file type.

There are three different secure messaging levels available, as follows:

- SM\_PLAIN: Data is sent in plain and the transferred command does not include an integrity protection field
- SM\_MAC: Integrity-protected communication with a field of MAC in the transferred command and the data is sent in plain
- SM\_ENC: Confidential communication with encryption of data and integrity protection field in the transferred command

The PCD defines the communication security level applicable for exchanging the messages between PCD and PICC.

The PICC evaluates if the chosen security level is acceptable for the addressed file and operation.

### 3.3 Command set

This section defines all the commands available for operation of CIPURSE™ application.

**Table 8 Overview of CIPURSE™ commands**

Command	Description
<b>Multi-level commands</b>	
SELECT	Selects the file (MF, ADF, or EF)
<b>Commands for personalization of file system oriented PICCs</b>	
CREATE_FILE (ADF, EF)	Creates an ADF or an EF in the PICC file system
DELETE_FILE (ADF, EF)	Deletes an ADF or an EF from the PICC file system
FORMAT_ALL	Formats the file system to its initial data state The MF keys, MF key attributes, and the content and attributes of predefined EFs under the MF are not formatted
<b>Commands for object management</b>	
ACTIVATE_FILE (ADF)	Activates an ADF in the PICC file system
DEACTIVATE_FILE (ADF)	Deactivates an ADF in the PICC file system
<b>Commands for file attribute management</b>	
READ_FILE_ATTRIBUTES	Reads the MF, DF, or EF file attributes
UPDATE_FILE_ATTRIBUTES	Updates the MF, DF, or EF file attributes
UPDATE_KEY	Updates the value of a key in the PICC
UPDATE_KEY_ATTRIBUTES	Updates the attributes of a key in the PICC
<b>Security related commands</b>	
MUTUAL_AUTHENTICATE	Mutual authentication with the PICC
GET_CHALLENGE	Retrieves the challenge information from the PICC in order to proceed with authentication
<b>Commands for file data management</b>	
READ_BINARY	Reads a data from a binary file
UPDATE_BINARY	Updates a data into a binary file
READ_RECORD	Reads a records from a record file or a value record file

**(table continues...)**

**Table 8** (continued) **Overview of CIPURSE™ commands**

<b>Command</b>	<b>Description</b>
UPDATE_RECORD	Updates a data into an existing record in a record file or a value record file
APPEND_RECORD	Appends a record to a cyclic record file that is not already full
READ_VALUE	Reads a value from a value record file
INCREASE_VALUE	Increases the value in a value record file
DECREASE_VALUE	Decreases the value in a value record file
LIMITED_INCREASE_VALUE	Increases the value in a value record file within a limited range defined by the previous DECREASE_VALUE operation
LIMITED_DECREASE_VALUE	Decreases the value in a value record file by a limited amount



## 4 Contactless I/O functionality

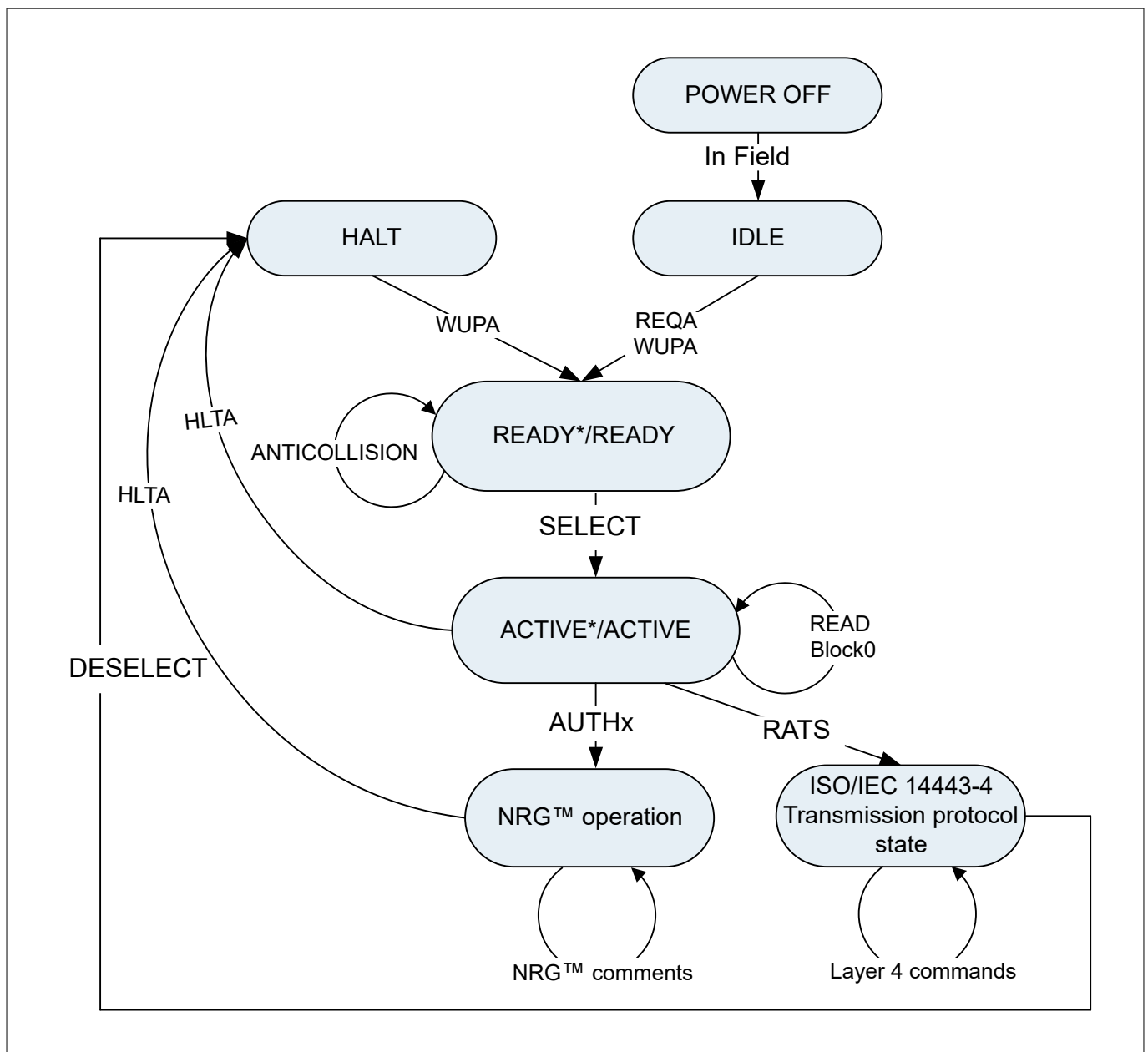
CIPURSE™4move supports contactless I/O communication according to ISO/IEC 14443-3 [9] and ISO/IEC 14443-4 [10] and as configured in EF.IO\_CONFIG at the time of manufacturing of the product.

### 4.1 Communication principle

All operations on the PICC are initiated by an appropriate reader and controlled by the internal logic of CIPURSE™4move. Prior to any application specific operations, the PICC has to be selected according to the ISO/IEC 14443-3 [9] Type A anticollision and selection scheme.

After selection, the PCD may proceed as follows:

- Enter the NRG™ operation state (CIPURSE™4move devices supporting NRG™ operation only) or
- Enter ISO/IEC 14443-4 [10] transmission protocol processing (T=CL) by sending a RATS command



**Figure 12 CIPURSE™4move communication state diagram according to ISO/IEC 14443-3 Type A**

---

## **4 Contactless I/O functionality**

### **4.2 ISO/IEC 14443 feature set**

The following features and types of commands are available:

- Commands for radio frequency (RF) initialization and bit frame anticollision as per ISO/IEC 14443-3 [\[9\]](#), Type A
- Commands for operating the half-duplex block transmission protocol as per ISO/IEC 14443-4 [\[10\]](#), with the following feature profile:
  - Card identifier (CID) is supported, which enables the PCD to select and operate more than one PICC simultaneously
  - PICC and PCD chaining is supported
  - Node address (NAD) is supported
  - Power level indication inside the CID is not supported
- The error handling is performed as defined in ISO/IEC 14443-3 [\[9\]](#) and ISO/IEC 14443-4 [\[10\]](#)

## 5 Block oriented memory with NRG™

Block oriented memory communicating via ISO/IEC 14443-3 [9] Type A, and offers a proprietary command set for application operation. It features the confidential CRYPTO1 [12] stream cipher. Mutual authentication according to ISO/IEC 9798-2 [6] is used to set up the stream ciphering, which applies to the whole subsequent data exchanged over the RF link.

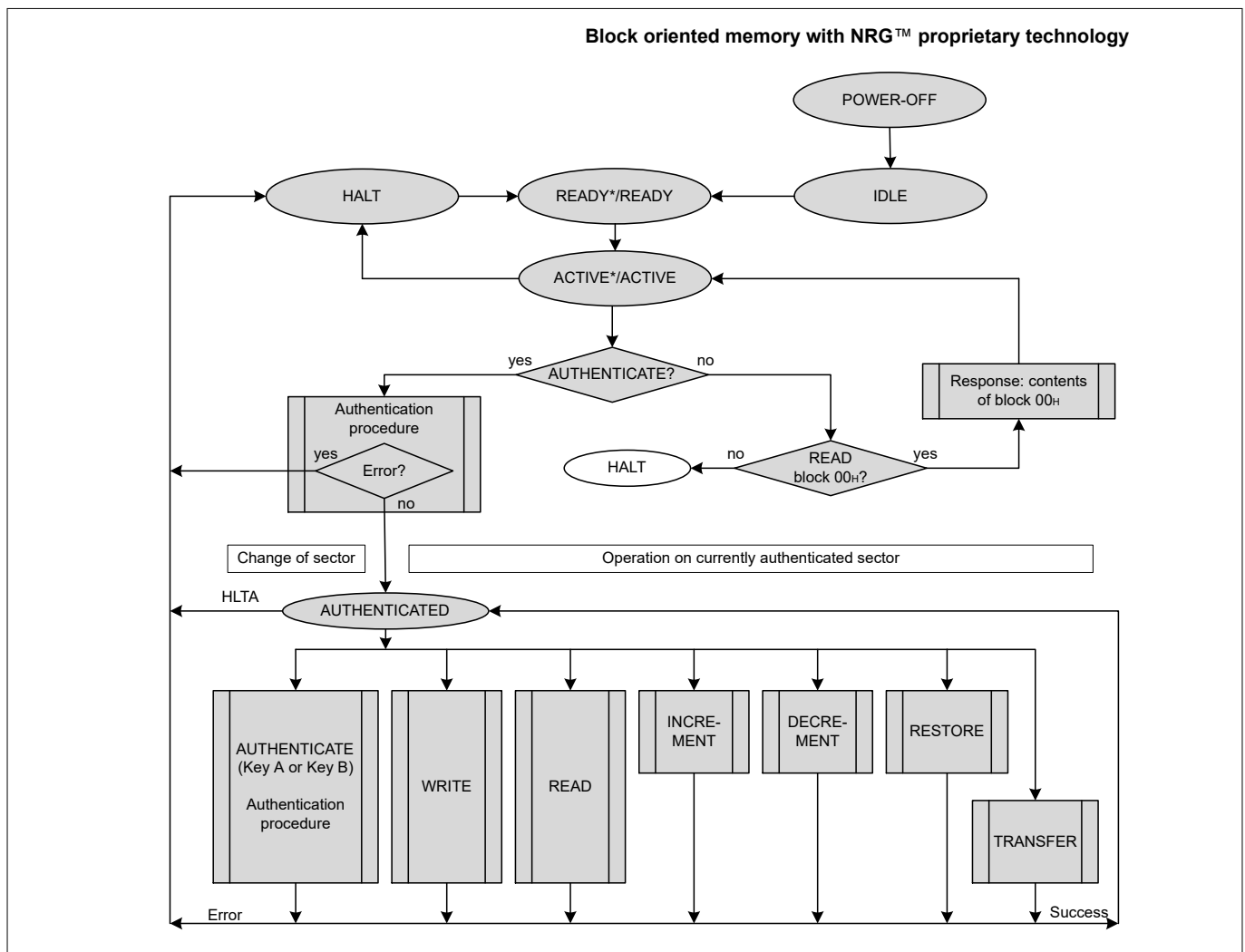
### 5.1 Operation of a block oriented memory with NRG™

The PCD and PICC must use a bit rate of  $128/f_C$  (~106 kbit/s) in both directions for all commands and responses, with the characteristics as specified by ISO/IEC 14443-3 [9].

First, the PCD and the PICC perform the initialization and anticollision procedure as described in ISO/IEC 14443-3 [9].

With the PICC in ACTIVE/ACTIVE\* state, the PCD can initiate the authentication procedure by sending the AUTHENTICATE command or send the READ block 00<sub>H</sub> command plain (unencrypted) once or multiple times before initiating the authentication procedure. After completion of the authentication procedure, the PICC enters the authenticated state. So, all further communication in this state must be encrypted by the CRYPTO1 stream cipher.

The PICC exits the authenticated state on reception of the encrypted HLTA command, performing its transition to the HALT state, or in case of error.



**Figure 13** Block oriented memory with NRG™ operation (initialization and anticollision procedure with 4-byte UID)

## 5 Block oriented memory with NRG™

Note: State transitions due to successful command execution are shown in this diagram.

### 5.2 Memory organization

Memory accessible in NRG™ mode is organized into blocks of 16 bytes. These blocks are accessible as elementary data units using the NRG™ command set (see [Chapter 5.3](#)) and thus no single byte level access is allowed. Further on, blocks are grouped into sectors as described below:

- SLS 32TLC00xS1/SLS 32TLC00xS6/SLS 32TLC00xSB (1 KB block oriented memory with NRG™)
  - 16 sectors of 4 blocks each
- SLS 32TLC00xS4/SLS 32TLC00xS9/SLS 32TLC00xSE (4 KB block oriented memory with NRG™)
  - 32 sectors of 4 blocks each
  - 8 sectors of 16 blocks each

#### 5.2.1 1 KB non-volatile memory

This section describes the PICCs offering 1 KB of non-volatile memory (NVM) available for the purpose of NRG™ operation.

#### Structure and properties

Sector Number	Block Address	Block Number	Byte Number within a Block																Description
			0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	
15	3F <sub>H</sub>	3	Authentication Key A						Access Bits		RFU	Authentication Key B (optional) or Data						Sector Trailer	
	3E <sub>H</sub>	2																Data	
	3D <sub>H</sub>	1																Data	
	3C <sub>H</sub>	0																Data	
14	3B <sub>H</sub>	3	Authentication Key A						Access Bits		RFU	Authentication Key B (optional) or Data						Sector Trailer	
	3A <sub>H</sub>	2																Data	
	39 <sub>H</sub>	1																Data	
	38 <sub>H</sub>	0																Data	
•	•	•																	•
•	•	•																	•
•	•	•																	•
1	07 <sub>H</sub>	3	Authentication Key A						Access Bits		RFU	Authentication Key B (optional) or Data						Sector Trailer	
	06 <sub>H</sub>	2																Data	
	05 <sub>H</sub>	1																Data	
	04 <sub>H</sub>	0																Data	
0	03 <sub>H</sub>	3	Authentication Key A						Access Bits		RFU	Authentication Key B (optional) or Data						Sector Trailer	
	02 <sub>H</sub>	2																Data	
	01 <sub>H</sub>	1																Data	
	00 <sub>H</sub>	0																Manufacturer Data	

**Figure 14 Memory structure of 1 KB of NVM with NRG™**

The memory of PICC offering 1 KB of NVM with NRG™ is structured as described below:

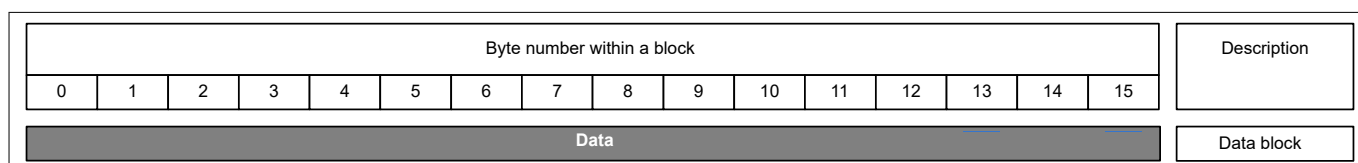
- The memory is organized in 16 sectors, each with 4 blocks with 16 bytes of data each. A block is the elementary unit addressable by NRG™ commands. The numbering of sectors and blocks is in ascending order of their addresses, as shown in [Figure 14](#)
- A successful authentication procedure to the sector where the addressed block is located must be carried out to allow the PCD to apply the appropriate commands to the block

## 5 Block oriented memory with NRG™

- Blocks 0, 1, and 2 of each sector are available for application data, configurable in two ways:
  - Arbitrarily usable data blocks as specified in [Data block](#)
  - Blocks formatted as specified in [Value block](#)
- Block 3 of each sector (denoted as "sector trailer") has the following properties:
  - This block contains either one or two cryptographic keys of 6 byte each (Key A is mandatory, Key B is optional) for authentication to get access to the blocks in this sector, and 3 bytes of access bits forming the access conditions for all blocks in this sector as specified in [Sector trailer](#)

### Data block

Data blocks offer to store the bytes in a sequential order. READ and WRITE commands are applicable to the data blocks.



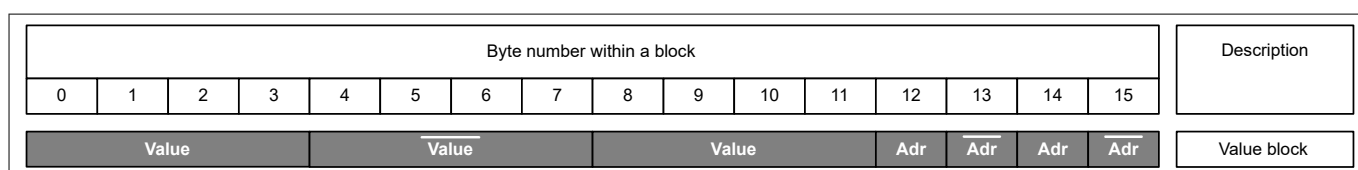
**Figure 15**      **Structure of a data block**

### Value block

Value blocks offer to store and manage the dedicated "arithmetic values". The "Value" inside a value block is 4 bytes in length and stored two times in normal and one time in bit-inverted manner. Values must be stored in little endian order.

The arithmetic instructions INCREASE, DECREASE, and RESTORE are applicable to value blocks, where the result is temporarily stored in a volatile transfer buffer. To store the result in the NVM, the TRANSFER command must be used. Besides these commands, READ and WRITE commands are applicable to the value blocks.

*Note: For the purpose of this document, the term "transfer buffer" is used in the command set description of the arithmetic instructions. This represents a volatile memory location in the PICC to perform the manipulation of arithmetic values. It cannot be directly accessed with any of the NRG™ commands.*



**Figure 16**      **Structure of a value block**

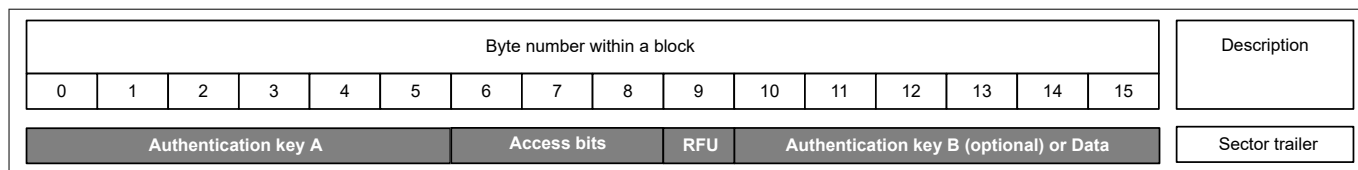
### Sector trailer

The sector trailer contains the authentication keys and the access bits as described below:

- Keys of each 6 byte (Key A is mandatory, Key B is optional) for authentication to all blocks in this sector
- 3 bytes of access bits forming the access condition information for the associated sector, that is access to the blocks along with the sector trailer
- 1 byte is reserved for future use and should not be used for other application data

For more details about sector trailer, see chapter 5.3.2 in [\[3\]](#).

## 5 Block oriented memory with NRG™



**Figure 17**      **Structure of a sector trailer**

### Access condition

Depending on the access condition, the right to execute a particular command to the block results in one of the following conditions:

- Never: Command not granted
- Key A: Command granted when successfully authenticated with Key A of this sector
- Key B: Command granted when successfully authenticated with Key B of this sector
- Key A/B: Command granted when successfully authenticated with Key A or Key B of this sector

The access condition for blocks 0 to 2 and the sector trailer, of the associated sector are formed by the access bits.

Access bits define four access groups: one group for the sector trailer and the remaining groups for data or value blocks each.

### 5.2.2      4 KB non-volatile memory

This section describes PICCs offering 4 KB of NVM available for the purpose of NRG™ operation. Unless otherwise specified, the description in [Chapter 5.2.1](#) also apply to PICCs offering 4 KB of NVM.

The extensions of such PICCs compared to PICCs offering 1 KB of NVM available for the purpose of NRG™ operation. Unless otherwise mentioned, the description in [Chapter 5.2.1](#) apply also to PICCs offering 4 KB of NVM.

## 5 Block oriented memory with NRG™

Sector Number	Block Address	Block Number	Byte Number within a Block																Description
			0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	
39	FF <sub>H</sub>	15	Authentication Key A						Access Bits			RFU	Authentication Key B (optional) or Data						Sector Trailer
	FE <sub>H</sub>	14																Data	
	.	.																	.
	.	.																	.
	.	.																	.
	F1 <sub>H</sub>	1																Data	
	F0 <sub>H</sub>	0																Data	
.	.	.																	.
.	.	.																	.
.	.	.																	.
32	8F <sub>H</sub>	15	Authentication Key A						Access Bits			RFU	Authentication Key B (optional) or Data						Sector Trailer
	8E <sub>H</sub>	14																Data	
	8D <sub>H</sub>	13																Data	
	.	.																	.
	.	.																	.
	.	.																	.
	84 <sub>H</sub>	4																Data	
	83 <sub>H</sub>	3																Data	
	82 <sub>H</sub>	2																Data	
	81 <sub>H</sub>	1																Data	
	80 <sub>H</sub>	0																Data	
31	7F <sub>H</sub>	3	Authentication Key A						Access Bits			RFU	Authentication Key B (optional) or Data						Sector Trailer
	7E <sub>H</sub>	2																Data	
	7D <sub>H</sub>	1																Data	
	7C <sub>H</sub>	0																Data	
.	.	.																	.
.	.	.																	.
.	.	.																	.
0	03 <sub>H</sub>	3	Authentication Key A						Access Bits			RFU	Authentication Key B (optional) or Data						Sector Trailer
	02 <sub>H</sub>	2																Data	
	01 <sub>H</sub>	1																Data	
	00 <sub>H</sub>	0																Manufacturer Data	

**Figure 18 Memory structure for CIPURSE™4move providing 4 KB NRG™**

The memory of an NRG™ PICC offering 4 KB of NVM is structured as described below:

- The memory is organized in 40 sectors, 32 of them consisting of 4 blocks with 16 bytes of data, and 8 of them consisting of 16 blocks with 16 bytes of data. The numbering of sectors and blocks are in ascending order of their addresses, as shown in [Figure 18](#)
- For sectors 0 to 31 (sectors consisting of 4 blocks), the same definitions as for sectors 0 to 15 as specified by [Figure 14](#) are applicable
- For sectors 32 to 39 (sectors consisting of 16 blocks),
  - Blocks 0 to 14 are available for application data, configurable in the same way as for sectors consisting of 4 blocks
  - Block 15 of each sector (denoted as "sector trailer") has similar properties as block 3 for sectors in 1 KB NRG™ (see [Chapter 5.2.1](#)) but four access groups are defined by the access bits forming access conditions: one group for the sector trailer and the remaining groups for 5 data or value blocks each

### 5.3 NRG™ command set

This section describes the commands supported by CIPURSE™4move when it is in NRG™ operation state.

## 5 Block oriented memory with NRG™

**Table 9 Overview of NRG™ commands**

Command <sup>1)</sup>	Description
AUTHENTICATE with Key A	Authentication with Key A to the sector in which the addressed block is located
AUTHENTICATE with Key B	Authentication with Key B to the sector in which the addressed block is located
READ	Reads out 16 bytes from memory block via NRG™
WRITE	Writes 16 bytes into memory block via NRG™
DECREMENT	Arithmetic instruction Loads the actual value from the addressed value block decremented by the operand into the transfer buffer
INCREMENT	Arithmetic instruction Loads the actual value from the addressed value block incremented by the operand into the transfer buffer
RESTORE	Arithmetic instruction Loads the actual value of the addressed value block into the transfer buffer
TRANSFER	Transfers the actual value in the transfer buffer to the addressed value block
HLTA	Transition to HALT state as per ISO/IEC 14443-3 [9]

1) For more details about the NRG™ command set, see chapter 7 in [3].

### 5.4 NRG™ to CIPURSE™ migration

Migration from NRG™ data structure to CIPURSE™ oriented file system requires access to block oriented NRG™ memory from both NRG™ and CIPURSE™ interfaces.

NRG™ supports multiple applications which require mapping each of the applications to dedicated sectors. To access, one of these applications from a CIPURSE™ ADF, these dedicated sectors must be accessible while access to other sectors (belonging to other applications) is prevented by assigning dedicated NRG™ sectors to CIPURSE™ ADF.

*Note: On CIPURSE™ products supporting MF, application creation is done under the MF security domain, EF creation is done within the CIPURSE™ ADF security domain.*

*Note: Changes to the NRG™ mapped memory area are not protected by command level atomicity.*



## 6 Operational characteristics

### 6.1 Absolute maximum ratings

Stresses above those listed may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this data sheet is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability, including electrically erasable programmable read-only memory (EEPROM) data retention and write/erase endurance.

**Table 10 Absolute maximum ratings**

Parameter	Symbol	Values			Unit	Note or test condition
		Min.	Typ.	Max.		
Junction temperature	$T_J$	-40		+110	°C	
Storage temperature	$T_{stg}$	-40		+125	°C	For chip. For modules according to module specification
ESD protection	$V_{ESD}$	-2		+2	kV	EIA/JESD22-A114-B

### 6.2 Electrical characteristics

**Table 11 Operation range**

Parameter	Symbol	Values			Unit	Note or test condition
		Min.	Typ.	Max.		
Ambient temperature	$T_A$	-25		+85	°C	$T_J$ must not be exceeded
Endurance (write/erase cycles) <sup>1)</sup>		$10^5$				
Data retention (years) <sup>1)</sup>		10				$T_A = 25^\circ\text{C}$

1) Values are temperature dependent. For further information please refer to your Infineon Technologies office or representative.

**Table 12 Contactless interface characteristics**

Parameter	Symbol	Values			Unit	Note or test condition
		Min.	Typ.	Max.		
Operating conditions	H	1.5		7.5	A/m	Reference setup according to ISO/IEC 14443-2 [8] and ISO/IEC 10373-1 [7]
Carrier frequency	$f_C$		13.56		MHz	±7 kHz
Chip input capacitance	$C_{AB}$		27/56/78		pF	
Recommended target resonance frequency	$f_{res}$		16.5		MHz	ID1 (Class 1) card size

## References

### CIPURSE™/OSPT

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### ISO/IEC

- [4] ISO/IEC 7816-4:2020: *Identification cards - Integrated circuit cards - Part 4: Organization, security and commands for interchange (Fourth edition)*; 2020-05
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- [7] ISO/IEC 10373-1:2020-10: *Cards and security devices for personal identification – Test methods - Part 1: General characteristics (Third edition)*; 2020-10
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### NFC Forum

- [11] NFC Forum: *Type 4 Tag Technical Specification (Version 1.1)*; 2019-12-12

### Siemens

- [12] Siemens AG: *Semiconductors HL CC PD ID: Crypto-Unit CRYPTO1.DOC*; 1997

## **Glossary**

### **ADF**

*application dedicated file (ADF)*

### **AES**

*Advanced Encryption Standard (AES)*

The standard for the encryption of electronic data established by the U.S. National Institute of Standards and Technology (NIST) in 2001. The algorithm described by AES is a symmetric-key algorithm (i.e. the same key is used for both encryption and decryption).

### **AID**

*application identifier (AID)*

Used to reference (select) an application.

### **APDU**

*application protocol data unit (APDU)*

The communication unit between a smart card reader and a smart card.

### **ATS**

*answer to select (ATS)*

### **CC**

*Common Criteria for Information Technology Security Evaluation (CC)*

An international standard (ISO/IEC 15408) for computer security certification.

### **CID**

*card identifier (CID)*

### **CIPURSE™**

Open security standard for transit fare collection systems. CIPURSE™ is a trademark of the Open Standard for Public Transport Alliance.

### **DFA**

*differential fault analysis (DFA)*

A class of side channel attacks in the field of cryptography, specifically cryptographic analysis. Faults are induced into cryptographic implementations with the intention of revealing information about their internal states.

### **DF**

*dedicated file (DF)*

### **DPA**

*differential power analysis (DPA)*

A class of attacks against smart cards and secure cryptographic tokens. The attack involves monitoring how much power a microprocessor uses as it functions, then using advanced statistical methods to determine secret keys or personal identification numbers involved in the computations.

### **EAL**

*evaluation assurance level (EAL)*

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**Glossary**

**EEPROM**

*electrically erasable programmable read-only memory (EEPROM)*

**EF**

*elementary file (EF)*

A file system component containing (user) data.

**EIA**

*Electronic Industry Alliance (EIA)*

**ENC**

*encryption (ENC)*

**ESD**

*electrostatic discharge (ESD)*

The sudden draining of electrostatic charge. Even with small charges, it poses a considerable risk to small semiconductor structures, in particular MOS structures. It is therefore essential to take precautions when dealing with unprotected semiconductors.

**FD**

*file descriptor (FD)*

Defines the file type (MF, ADF, type of EF).

**FID**

*file identifier (FID)*

Used to reference an elementary file.

**FWI**

*frame waiting time integer (FWI)*

**ID**

*identification (ID)*

**IEC**

*International Electrotechnical Commission (IEC)*

The international committee responsible for drawing up electrotechnical standards.

**ISO**

*International Organization for Standardization (ISO)*

**MAC**

*message authentication code (MAC)*

Used to prove message integrity.

**MCC**

*module contactless card (MCC)*

**MF**

*master file (MF)*

The root of the CIPURSE™ file system.

**NAD**

*node address (NAD)*

## **Glossary**

### **NFC**

*near field communication (NFC)*

### **NRG™**

ISO/IEC 14443-3 type A with CRYPTO1

### **NVM**

*non-volatile memory (NVM)*

### **OSPT**

*Open Standard for Public Transport (OSPT)*

### **PCD**

*proximity coupling device (PCD)*

A reader device for NFC cards.

### **PICC**

*proximity integrated circuit card (PICC)*

A contactless smart card which can be read without inserting it into a reader device.

### **PxSE**

*proximity system environment (PxSE)*

A generic term for various system-environment applications that are specific to the application family.

### **RATS**

*request for answer to select (RATS)*

### **RF**

*radio frequency (RF)*

### **RFU**

*reserved for future use (RFU)*

### **SFID**

*short file identifier (SFID)*

### **SMG**

*secure messaging group (SMG)*

This belongs to the file security attributes. Commands are clustered into SMGs, where each of them lists one or more commands.

### **SMR**

*secure messaging rules (SMR)*

Object-specific messaging rules combining four SMGs.

### **SM**

*secure messaging (SM)*

A secure channel that is established between the secure element and a communication partner to ensure confidentiality and authenticity of the exchanged data.

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**Glossary**

**SM\_PLAIN**

*secure messaging with plain data (SM\_PLAIN)*

Communication with endpoint internal preparation for integrity verification. Data are sent plain, and the transferred frame does not include an integrity protection field.

**UID**

*unique identifier (UID)*

## Revision history

Reference	Description
<b>Revision 1.0, 2023-01-05 – Valid for product version 1.0.2 and higher</b>	
All	Initial release

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**Email:**  
[CSSCustomerService@infineon.com](mailto:CSSCustomerService@infineon.com)

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