XENSIV™ sensor chip for 16-slice CT detector

Key benefits & features

The benefits and features of XENSIV™ AS5950, sensor chip for 16-slice CT detector, are listed in Table 1.

Table 1 Added value of using AS5950

Benefits	Features
Ultra-low input related noise down to 0.20 fC	High sensitive photodiode and ADC in one integrated sensor
Fast integration time down to 200 µs	Calibration mode for external linearity calibration
Low power dissipation down to 0.65 mW per channel	Adjustable full scale range, resolution, integration time and active sensor area
High ADC linearity of ±300 ppm of reading and ±600 ppm including the photodiode	Adaptive array enables selection of total sensor dimension of 16 mm or 32 mm
Up to 25-bit resolution	Customization of pixel dimensions on request

Applications

- Medical, industrial and security CT detector modules
- 8-slice and 16-slice CT detectors

General description

AS5950 is a sensor chip for 16-slice CT detectors that combines the photodiodes and the readout circuit on a single CMOS chip. This sensor solution, which includes an array of 64 photodiodes with ultra-low dark current and a 64-channel ADC side-by-side, allows the assembly of the pixel array on three adjacent edges of the device. Two AS5950 ICs can be placed in Z-direction enabling the design of 16-slice detectors for cost optimized CT machines.

The total sensor dimension in Z-direction is selectable between 16 mm or 32 mm because of its adaptive array concept, enabling two standard pixel sizes of 1x1 mm^2 and 1x2 mm^2. It allows either a high resolution or a large sensor size on the detector. Pixel dimensions can be customized on request, the available AS5950B comes with a pixel dimension of 0.98x0.98 mm^2. The sensor can be directly assembled on a substrate using a wire bonding process for manufacturing of a CT module.

Superior image quality can be achieved because the input-related noise is very low, down to as little as typ. 0.20 fC in high-resolution mode. This ultra-low noise figure is the result of the very low capacitance of the photodiode and the parasitic capacitance of the interconnect between the photodiode and its corresponding ADC channel through metal layers on the same silicon.

The low power consumption of typ. 0.65 mW per channel reduces self-heating effects and reduces the overall cost of cooling the system. The digital data readout can be accessed via SPI interface. It is also used to configure parameters such as pixel resolution, input current range, active sensor area and enabling the calibration mode. An integrated temperature sensor enables monitoring of the junction temperature. Featuring on-chip photodiodes, the AS5950 offers a cost-optimized solution and is delivered as die on foil.

Table of contents

Key benefits & features 1
Applications 1
General description 1
Table of contents 2
1 Cross section 3
2 Ordering information 4
3 Revision history 5
Disclaimer 6

1 Cross section

The AS5950 integrates photodiodes and a 64-channel ADC in one single CMOS device. The cross section of this CT detector device is shown below. Figure 1 shows the assembly on a substrate through bonding wires for a CT module.

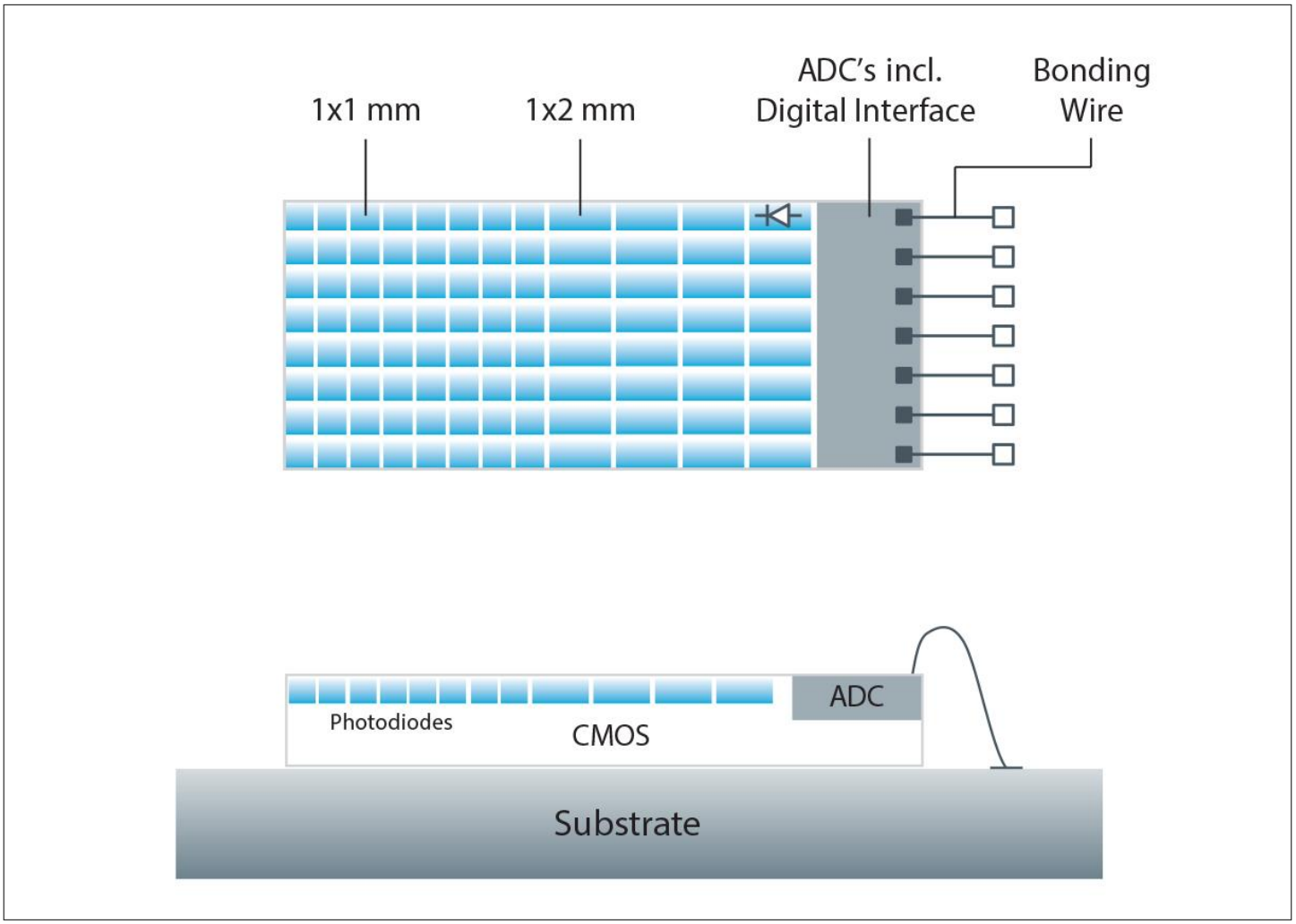


Figure 1 Cross section of AS5950

2 Ordering information

Product type (Infineon)	Product type (ams OSRAM)	Ordering code	Package	Marking	Delivery form	Availability
AS5950B-V2-SD-F	AS5950B-CSDF-0.98x0.98	SP006310477	Sorted Die	Digital register readout	Wafer/Die on Foil	On stock

***Note**: Pixel dimensions can be customized on request. Please contact Infineon for more information.*

3 Revision history

Document revision	Date	Description of changes
1.00	2026-10-30	Document rebranded from ams OSRAM to Infineon; technical specifications remain unchanged; added Infineon part numbers in Ordering information table. **Legacy ams OSRAM changes from previous released version to revision v1-00** • Initial version of short datasheet

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