KIT_PSE84_EVAL PSOC™ Edge E84 Evaluation Kit (Rev. *G) release notes

About this document

Scope and purpose

Thank you for your interest in the KIT_PSE84_EVAL PSOC™ Edge E84 Evaluation Kit. This document lists kit contents, installation requirements, kit documentation, limitations, and known issues.

Intended audience

This document is intended for the KIT_PSE84_EVAL PSOC™ Evaluation Kit users.

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1 Release contents

1.1 Kit contents

The KIT_PSE84_EVAL PSOC™ Edge E84 Evaluation Kit box includes the following:

- PSOC™ Edge E84 Evaluation Kit
  - PSOC™ Edge E8 base board
  - PSOC™ Edge E84 SOM (MOD_PSE84_SOMS2)
- USB Type-C to Type-C cable
- 4.3 Inch Capacitive Touch Display
- 0.3 MP USB Camera Module with USB Type-A Cable

2 Tool information

2.1 Software and tools

This kit's code examples require ModusToolbox™ software v3.5 or later. This is available on the ModusToolbox™ software webpage. For more details, see the kit guide.

The KitProg3 firmware v2.80 or later is required to program the PSOC™ Edge E84 MCU on the kit. The ModusToolbox™ installer automatically installs KitProg3 drivers.

2.2 Code example and kit collateral

The kit webpage includes both the documents and hardware files. The code examples are available in the latest installed Early Access pack.

2.3 Installation

All required software installation instructions are provided in the kit guide, which is available on the kit webpage.

2.4 Kit revision

This is the rev. *G version of the KIT_PSE84_EVAL PSOC™ Edge E84 Evaluation Kit.

2.5 Limitations and known issues

The following is the limitation and known issue in this revision (rev. *G) of the KIT_PSE84_EVAL.

- **Issue**: The kit can not be programmed or debugged through the JTAG interface using external programmer/debugger through 10-pin SWD/JTAG header (J16) or 20-pin ETM/TRACE header (J15)

  Workaround: To make the JTAG work using an external programmer or debugger, remove the R187 resistor on the PSOC™ Edge E8 base board as mentioned in the KIT_PSE84_EVAL PSOC™ Edge E84 Evaluation Kit guide (see the "Rework for JTAG Interface using External Programmer/Debugger" section in the kit guide). Note that the onboard KitProg3 JTAG will not work with this rework
- **Issue**: Backpower on the PSOC™ Edge E8 base board and PSOC™ Edge E84 SOM might lead to skewed Deep Sleep power numbers with MCU supply

  Workaround: Remove resistors R187 and R397 on the PSOC™ Edge E8 base board, and R229, R230, and R247 on the PSOC™ Edge E84 SOM, as mentioned in the KIT_PSE84_EVAL PSOC™ Edge E84 Evaluation Kit guide (refer to the "Rework for MCU low power current measurement" section in the kit guide)
- **Issue**: RMII receive (Rx) is non-functional on the KIT_PSE84_EVAL PSOC™ Edge E84 Evaluation Kit (Rev*G).

  Workaround: To resolve this, perform a hardware rework on the PSOC™ Edge E8 base board by removing resistor R93 and connecting TP9 to RX_CTL (RX_DV of the PSOC™ Edge E84 MCU) using a fly wire. For more details, please refer to "Rework for Ethernet subsystem."

2.6 Documentation

The following kit documents are available on the kit webpage:

- KIT_PSE84_EVAL PSOC™ Edge E84 Evaluation Kit guide
- KIT_PSE84_EVAL PSOC™ Edge E84 Evaluation Kit quick start guide
- KIT_PSE84_EVAL PSOC™ Edge E84 Evaluation Kit release notes
- KIT_PSE84_EVAL PSOC™ Edge E84 Evaluation Kit hardware design files

2.7 Technical support

For assistance, go to Infineon support page. If you have any questions, post them in the Infineon developer community.

2.8 Additional information

- For more information about the PSOC™ Edge E84 MCU, associated documentation and software, see the `help` folder in the installed Early Access pack
- For more information about ModusToolbox™ software functionality and releases, see the ModusToolbox™ software webpage
- For a list of trainings on ModusToolbox™ software, see the ModusToolbox™ software training GitHub page

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