Release Notes

CY8CKIT-006 PSoC® 3 LCD Segment Drive Evaluation Kit

Release Date: January 11, 2010

Thank you for your interest in the CY8CKIT-006 PSoC® 3 Segment Drive Evaluation Kit. This document lists installation requirements, limitations, and known issues with Beta release of the kit.

System Requirements and Recommendations

PSoC® Creator™ 1.0 Beta 4.0 (1.0.0.5810)

PSoC® Programmer 3.10 Beta 4.0 (3.10.1.616)

The following minimum configuration is required to run PSoC Creator:

- PC-running Windows operating system: Windows XP SP2
- 2GB RAM
- 1GB of hard disk space
- USB 2.0 (for MiniProg3 programmer module connection)

PSoC Creator requires the following software to be installed:

- PSoC Programmer 3.10
- Windows Installer 3.1
- .Net Framework 2.0 SP1
- Keil Compiler (optional, but needed to build PSoC 3 projects)
- Adobe Reader (optional, but needed to view PDF files)

Installation

To install, insert the kit CD into your PC's CD-ROM drive. If the installer does not automatically start, manually start it by executing cyautorun.exe in the CD's root directory. After the installation begins, follow the instruction during installation.

Notes

- Do not plug in your LCD Kit MiniProg3 to the Host PC USB port until all software installation is complete.
- If you have a previous installation of PSoC 3 LCD Segment Drive Evaluation Kit and PSoC Creator and/or PSoC Programmer, uninstall them before re-installing PSoC 3 LCD Segment Drive Evaluation Kit, PSoC Creator, and PSoC Programmer.

Updates

N/A.

[+] Feedback

Limitations and Known Issues

1. **Low Power** – when the demonstration firmware enters what appears to be Low Power mode (observe blank LCD display with periodic flash of "ZZZ"), the device is not actually in a low power state.
    - This will be completely corrected with the next revision of PSoC 3 silicon, updated PSoC Creator components, and updated LCD Segment EVK demonstration firmware.
2. **Battery Life** – because low power is not actually implemented at this time, battery life is continually reduced even when the display indicates sleep mode. The switch (SW1) disconnects the battery. The battery is also disconnected when a wall supply is plugged into J2 (power jack).
3. **Low Voltage** – the kit does not currently support low voltage operation (single AA cell with boost conversion).
    - This capability will be added after the next revision of PSoC 3 silicon, updated PSoC Creator components, and updated LCD Segment EVK demonstration firmware.
4. **Limited use of PSoC 3 pin P1[3] (SWO, JTAG TDO) as GPIO** – the kit does not currently support the assignment of a Pins component to the PSoC 3 Pin P1[3] (User Pin J4_7 GPIO1) in debug build mode. This will be fixed with final silicon and PSoC creator releases.

Documentation

Kit documents are located in the \Documentation and folder on the kit CD.

Refer to:

- *CY8CKIT-006 PSoC3 LCD Drive Kit Users Guide.pdf*
- *CY8CKIT-006 PSoC3 LCD Drive Kit Quick Start Guide.pdf*
- *LCD Direct Drive Basics.pdf*
- *PSoC 3_CY8C38_Family_Datasheet.pdf*
- *PSoC 3_CY8C38_Family_Silicon_Errata.pdf*

Release notes are located in the *\Release_Notes* subdirectory under the Documentation folder on the kit CD.

Refer to *CY8CKIT-006 PSoC3 LCD Drive Kit Release Notes.pdf*

After installing the PSoC Creator and Programmer3.10, refer to the documentation as needed:

- *PSoC Creator→Help→Documentation*
- *Programmer3.10→Help→Help Topics*

The default location for PSoC Creator documents is:

C:\Program Files\Cypress\PSoC Creator\1.0\PSoC Creator\Documentation

You can also access the PSoC Creator documents from within PSoC Creator under:

- *PSoC Creator→Help→Documentation→Reference Material*

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