Please note that Cypress is an Infineon Technologies Company.
The document following this cover page is marked as “Cypress” document as this is the company that originally developed the product. Please note that Infineon will continue to offer the product to new and existing customers as part of the Infineon product portfolio.

Continuity of document content
The fact that Infineon offers the following product as part of the Infineon product portfolio does not lead to any changes to this document. Future revisions will occur when appropriate, and any changes will be set out on the document history page.

Continuity of ordering part numbers
Infineon continues to support existing part numbers. Please continue to use the ordering part numbers listed in the datasheet for ordering.

Release Notes

CY3215-DK PSoC® In-Circuit Emulator Development Kit

Release Date: December 28, 2016

Thank you for your interest in the CY3215-DK PSoC® In-Circuit Emulator Development Kit. This document lists installation requirements, software and hardware updates, limitations, and known issues with the kit.

System Requirements and Recommendations

Hardware/Operating System Requirements	**Minimum**	**Recommended**
Processor speed	1 GHz	2 GHz
RAM	1 GB	2 GB
Free hard drive space	800 MB	1 GB
Screen resolution	1024×768	1280×1024
CD/DVD drive	✓	✓
USB	Full Speed	2.0 Hi-Speed
Windows XP (SP2 or higher), Vista, or Windows 7	✓	✓
Software Prerequisites	**Minimum**	**Recommended**
Microsoft Internet Explorer	7	
Adobe Reader (for PDF documents)	6	9+
Windows Installer	3.1	
.NET Framework	2.0 SP1	
PSoC Programmer	3.16	Latest*
PSoC Designer	5.3	Latest*

*Go to http://www.cypress.com/go/CY3215-DK for the latest software downloads and documents.

Installation

To install, insert the kit CD into your PC's CD-ROM drive. If the installer does not start automatically, run *cyautorun.exe* in the root directory of the CD/DVD. Follow the installation instructions.

Kit Revision Rev. G – New Feature

The CY3215-DK Rev. G has additional ESD protection to withstand air discharge up to 8 kV on the pod and USB connector.

In this revision of the kit software, code examples are updated to support the latest version of PSoC Designer.

Limitations and Known Issues

CY3215-DK kits that were shipped between June 2007 and August 2007 included defective flex cables (CY3250-FLEXCABLE). When using these flex cables, PSoC Designer shows the 'Invalid Memory Reference' error message and you will not be able to emulate or debug. The cables include date codes 0725 to 0735. Holding the defective cable to light will show that they are transparent and missing the ground plane. For more information on this issue and how to return your defective flex cable, navigate to the knowledge base article at the following link: http://www.cypress.com/?rID=39222.

Documentation

Kit documents are located in the \Documentation folder in the default install location:

`C:\Program Files\Cypress\CY3215-DK\<version>\Documentation`

Documents include:

CY3215-DK_User_Guide.pdf

CY3215-DK_Quick_Start_Guide.pdf

CY3215-DK_Release_Notes.pdf

Silicon Errata

To access the latest versions of the silicon errata, visit http://www.cypress.com/psoc and navigate to Errata.

Technical Support

For assistance, go to http://www.cypress.com/go/support or contact our customer support at +1(800) 541-4736 Ext. 8 (in the USA), or +1 (408) 943-2600 Ext. 8 (International).

Additional Information

- For more information about PSoC Designer functionality and releases, review the user guide and release notes on the PSoC Designer web page: http://www.cypress.com/go/psocdesigner
- For more information about PSoC Programmer, supported hardware, and COM layer, visit the PSoC Programmer web page: http://www.cypress.com/go/psocprogrammer
- For a list of trainings on PSoC Designer, visit http://www.cypress.com/?rID=40543

Cypress Semiconductor
198 Champion Court
San Jose, CA 95134-1709
Phone(USA): 800.858.1810
Phone (Intnl): 408.943.2600
http:// www.cypress.com

Copyrights

© Cypress Semiconductor Corporation, 2010-2016. The information contained herein is subject to change without notice. Cypress Semiconductor Corporation assumes no responsibility for the use of any circuitry other than circuitry embodied in a Cypress product. Nor does it convey or imply any license under patent or other rights. Cypress products are not warranted nor intended to be used for medical, life support, life saving, critical control or safety applications, unless pursuant to an express written agreement with Cypress. Furthermore, Cypress does not authorize its products for use as critical components in life support systems where a malfunction or failure may reasonably be expected to result in significant injury to the user. The inclusion of Cypress products in life-support systems application implies that the manufacturer assumes all risk of such use and in doing so indemnifies Cypress against all charges.

Any Source Code (software and/or firmware) is owned by Cypress Semiconductor Corporation (Cypress) and is protected by and subject to worldwide patent protection (United States and foreign), United States copyright laws and international treaty provisions. Cypress hereby grants to licensee a personal, non-exclusive, non-transferable license to copy, use, modify, create derivative works of, and compile the Cypress Source Code and derivative works for the sole purpose of creating custom software and or firmware in support of licensee product to be used only in conjunction with a Cypress integrated circuit as specified in the applicable agreement. Any reproduction, modification, translation, compilation, or representation of this Source Code except as specified above is prohibited without the express written permission of Cypress.

Disclaimer: CYPRESS MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARD TO THIS MATERIAL, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE. Cypress reserves the right to make changes without further notice to the materials described herein. Cypress does not assume any liability arising out of the application or use of any product or circuit described herein. Cypress does not authorize its products for use as critical components in life-support systems where a malfunction or failure may reasonably be expected to result in significant injury to the user. The inclusion of Cypress' product in a life-support systems application implies that the manufacturer assumes all risk of such use and in doing so indemnifies Cypress against all charges.

Use may be limited by and subject to the applicable Cypress software license agreement.

PSoC is a registered trademark and PSoC Designer and Programmable System-on-Chip are trademarks of Cypress Semiconductor Corp. All other trademarks or registered trademarks referenced herein are property of the respective corporations.

Flash Code Protection

Cypress products meet the specifications contained in their particular Cypress PSoC Data Sheets. Cypress believes that its family of PSoC products is one of the most secure families of its kind on the market today, regardless of how they are used. There may be methods, unknown to Cypress that can breach the code protection features. Any of these methods, to our knowledge, would be dishonest and possibly illegal. Neither Cypress nor any other semiconductor manufacturer can guarantee the security of their code. Code protection does not mean that we are guaranteeing the product as "unbreakable."

Cypress is willing to work with the customer who is concerned about the integrity of their code. Code protection is constantly evolving. We at Cypress are committed to continuously improving the code protection features of our products.