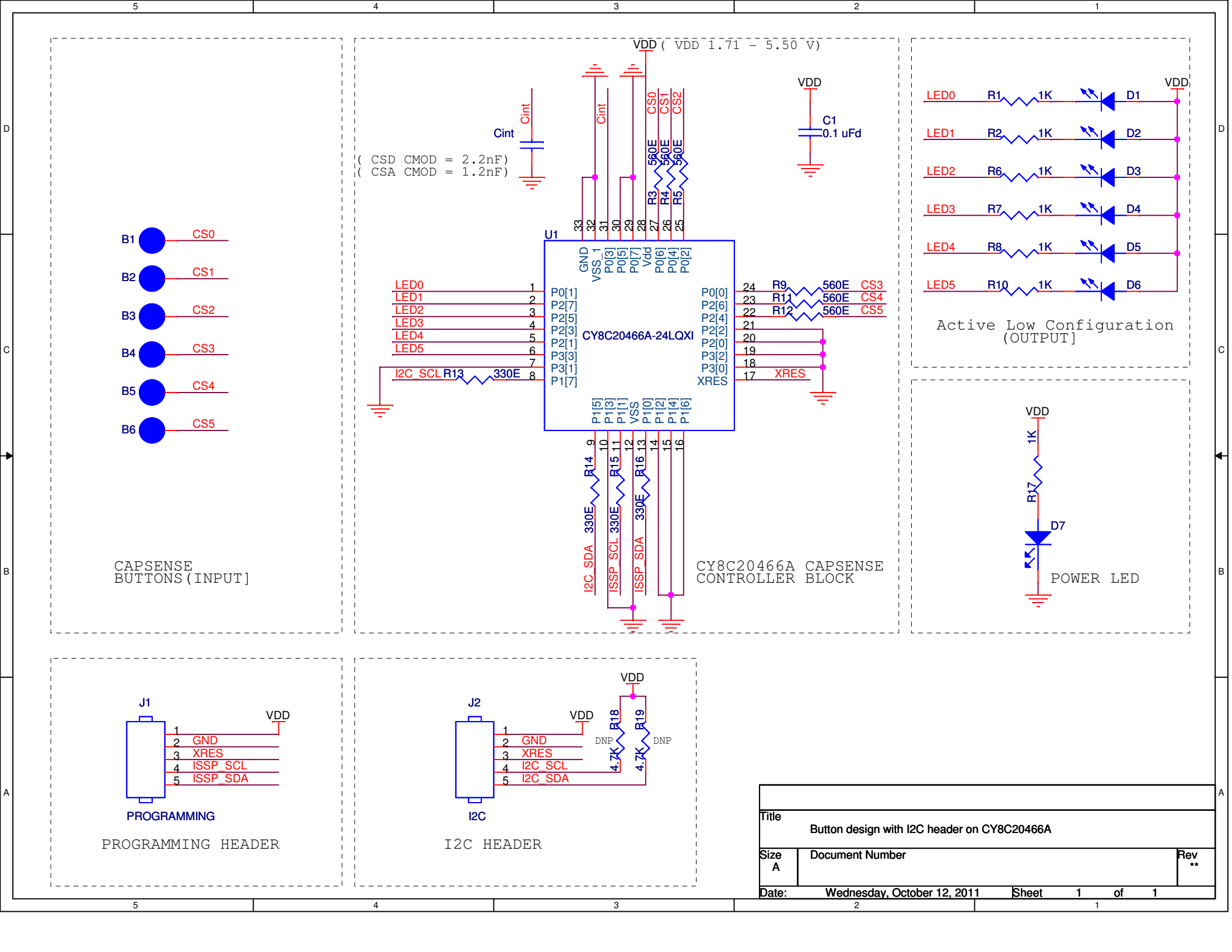
B1
B2
B3
B4
B5
B6
CS0
CS1
CS2
CS3
CS4
CS5
CAPSENSE
BUTTONS(INPUT)
VDD (VDD 1.71 - 5.50 V)
Cint
(CSD CMOD = 2.2nF)
(CSA CMOD = 1.2nF)
C1
0.1 uFd
560E
R3
R4
R5
U1
CY8C20466A-24LQXI
LED0
LED1
LED2
LED3
LED4
LED5
I2C_SCL
R13
330E
R9
R11
R12
XRES
R14
R15
R16
I2C_SDA
ISSP_SCL
ISSP_SDA
CY8C20466A CAPSENSE
CONTROLLER BLOCK
R1
R2
R6
R7
R8
R10
1K
D1
D2
D3
D4
D5
D6
Active Low Configuration
(OUTPUT)
R17
D7
POWER LED
J1
GND
XRES
ISSP_SCL
ISSP_SDA
PROGRAMMING
PROGRAMMING HEADER
J2
I2C_SCL
I2C_SDA
R18
R19
4.7K
DNP
I2C
I2C HEADER
Title
Button design with I2C header on CY8C20466A
Size
A
Document Number
Rev
**
Date:
Wednesday, October 12, 2011
Sheet
1
of
1