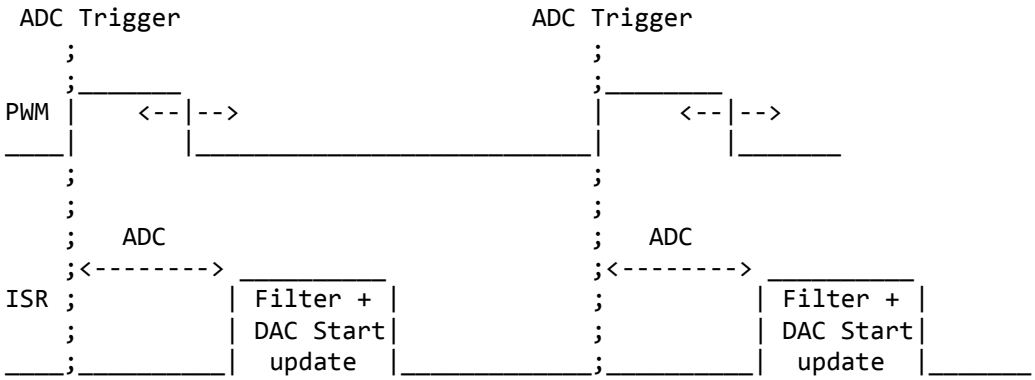
EXAMPLE NAME: BUCK_PCC_FIX_FQ_EXAMPLE_XMC42

OVERVIEW: This example implements a Buck Converter in Peak Current Control mode with the BUCK_PCC_FIX_FQ APP for being used together with the XMC Digital Power Explorer kit.

DESCRIPTION:
This example implements a Buck Converter in Peak Current Control mode for being used together with the XMC Digital Power Explorer kit. Visit www.infineon.com/xmc_dp_exp. The project is implemented using XMC4200 digital power control card, housing a 32-bit ARM Cortex M4 based microcontroller with Floating Point Unit. In addition, it also has dedicated control peripherals such as the HRPWM module, including CSG module. CSG is comprised of an analog comparator and an intelligent DAC capable of creating negative ramps for best slope compensation. HRPWM peripheral in XMC4200 also provides the ability to generate high resolution PWM (150 ps resolution). A Versatile Analog-to-Digital (VADC) module for analog signal measurement, is also included making XMC4200 suitable for applications that require fast calculation such as the Peak Current Control mode.
The current Control loop is implemented by a classic 2 poles 2 zeros filter using floating point values.
The provided filter coefficients have been selected to have the following controller characteristics:

- Switching freq = 200kHz
- Crossover freq = 10kHz
- Phase margin = 50 degrees
- PWM = 80MHz
- ADC resolution = 12 bits

VADC conversion is being continuously triggered by the Compare Match 2 of the CCU8 (set by default to the maximum, period value).
Once the output voltage has being measured by the VADC, an interrupt is generated.
Inside the ISR, the 2 poles 2 zeros controller is applied and new value for the DAC slope start is set.



Note: The example BUCK_PCC_DIGITAL_POWER_EXPLORER_XMC42, also available in DAVE server, implements similar functionality for the same hardware (XMC Digital Power Explorer Kit). The main difference is that in the BUCK_PCC_DIGITAL_POWER_EXPLORER_XMC42 project low level APPs are being used for configuring individually the required peripherals. Compared to using the BUCK_PCC_FIX_FQ APPs, *the low level APPs approach has advantages like the flexibility or the higher performance for the control loop and disadvantages like the higher complexity in terms of configuration and readability of the code.*

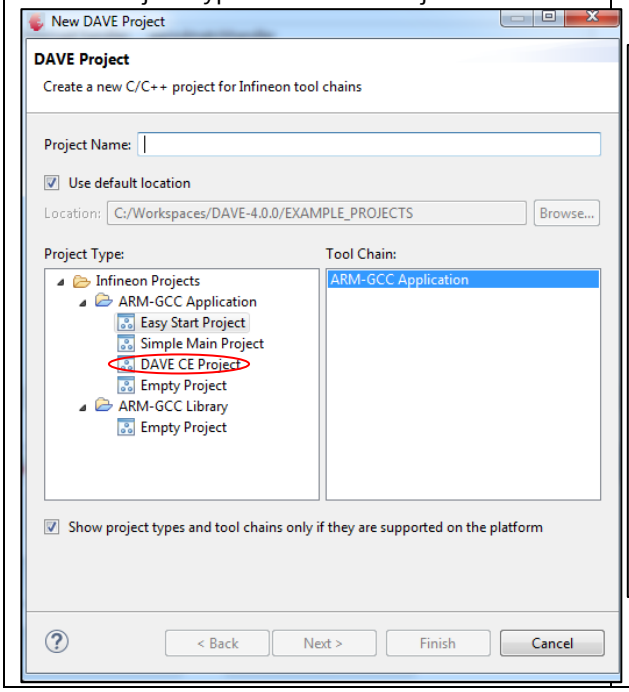
REQUIRMENTS:

XMC4200 Digital Power Control Card and XMC Digital Power Explorer Kit
DAVE Version: 4.2.8

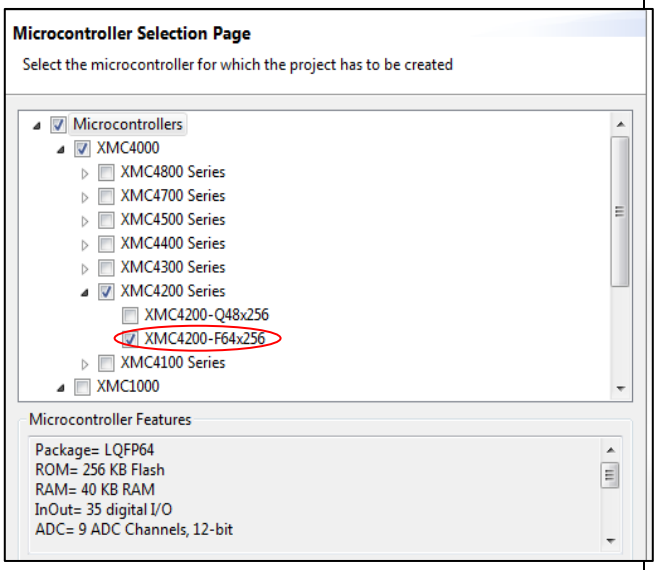
HOW TO CREATE THE PROJECT:

1. Open the DAVE CE and use  "Add IDE New Project Wizard" on the toolbar to add a new DAVE Project.

Enter Project Name
Select "Project Type ➔ DAVE CE Project"

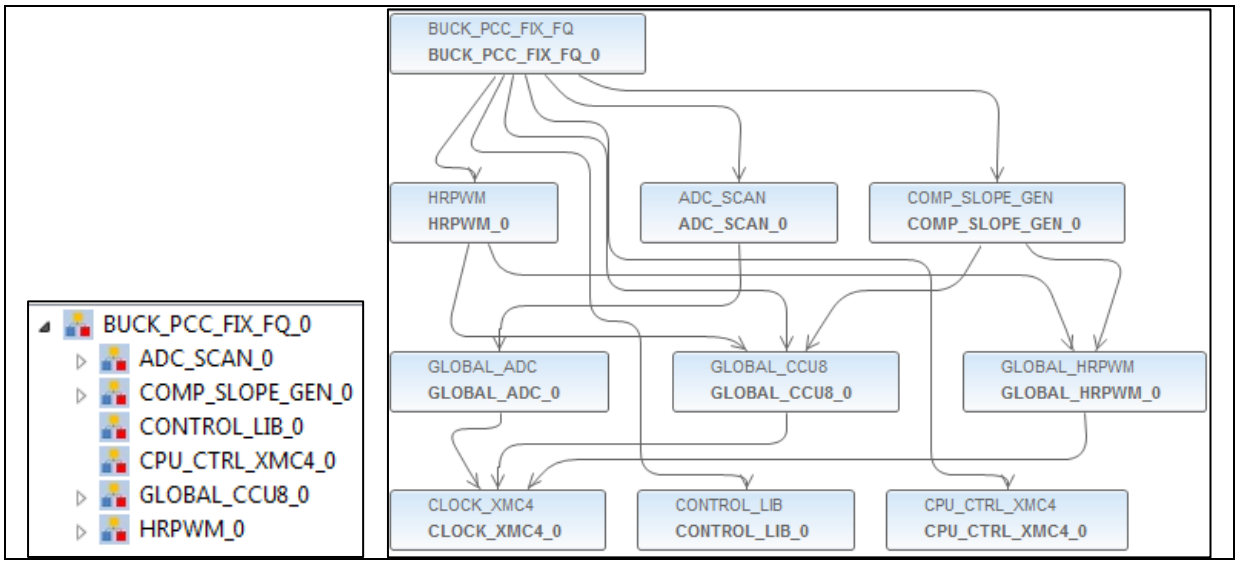


Select Processors Info
➔XMC4200 Series ➔ XMC4200-F64X256



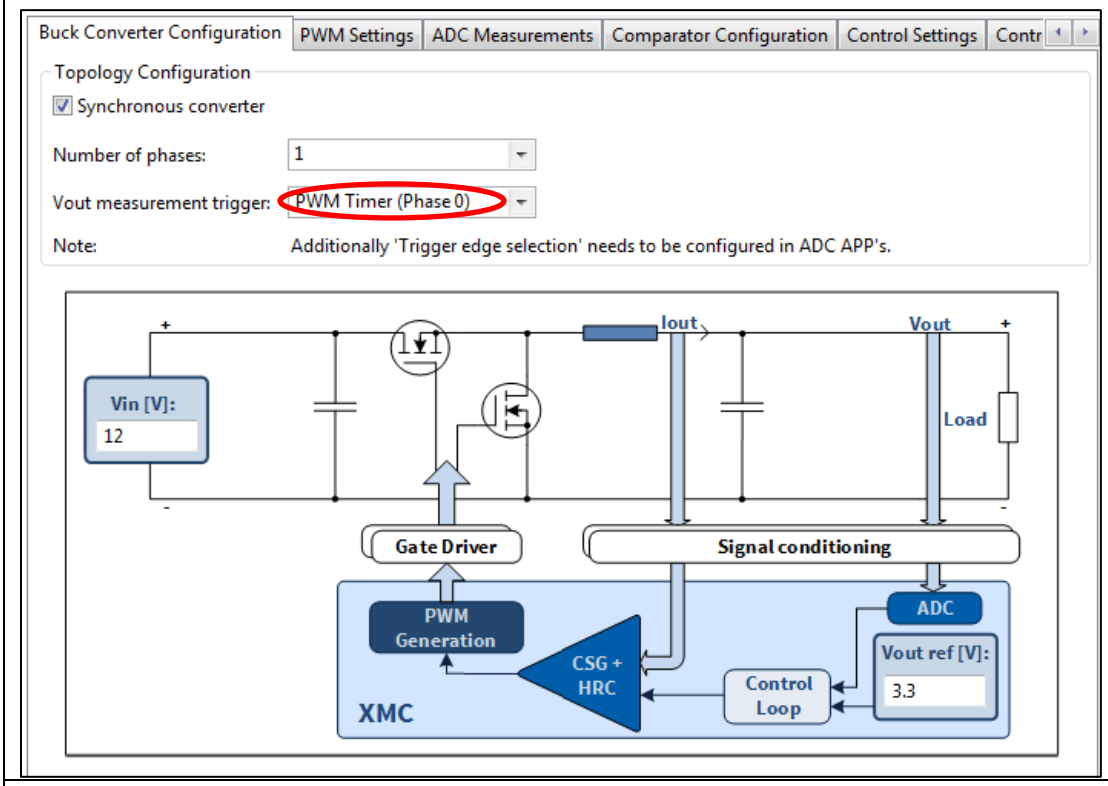
2. Use the  "Add New App" in the toolbar to add 1 instance of BUCK_PCC_FIX_FQ. Configure the APP instance with the following configurations.

- **Views: APP Dependency Tree and APP Dependency**

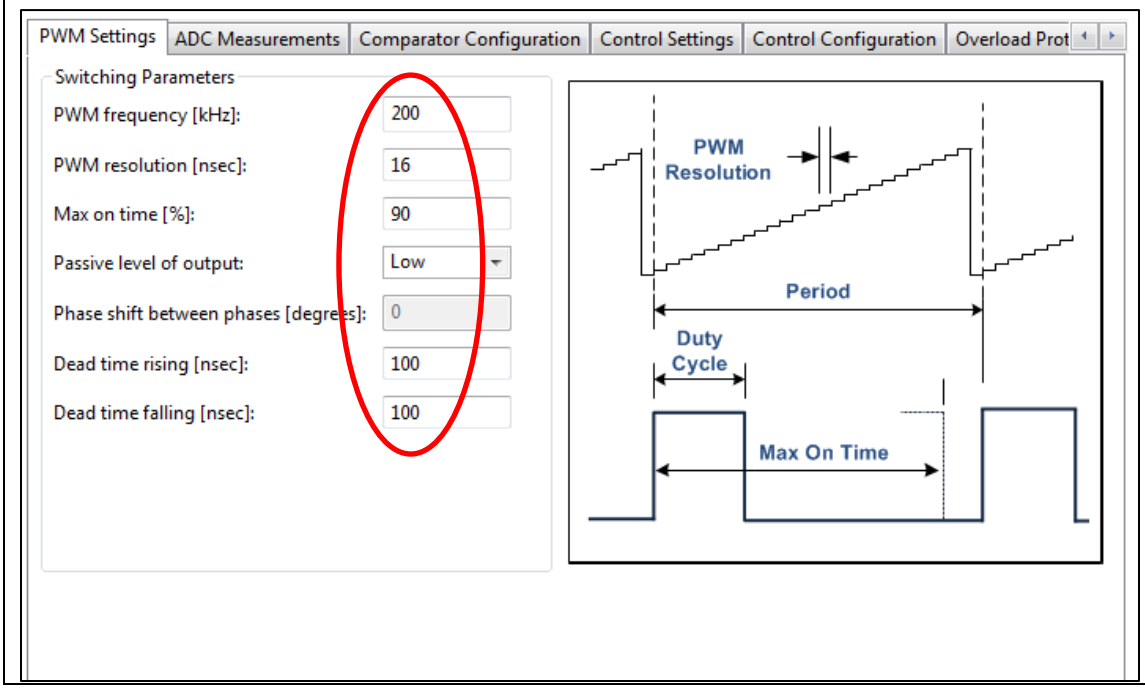


- **BUCK_PCC_FIX_FQ_XMC42:**

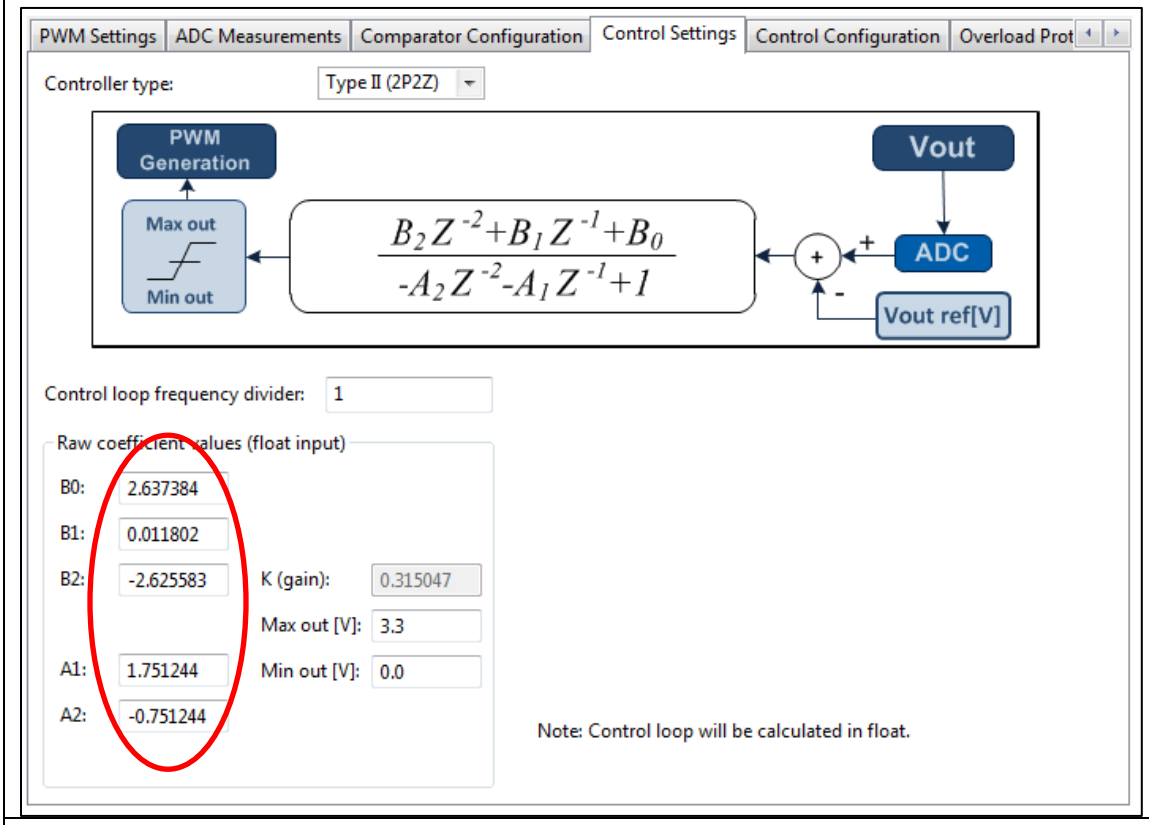
  - Buck Converter Configuration tab :



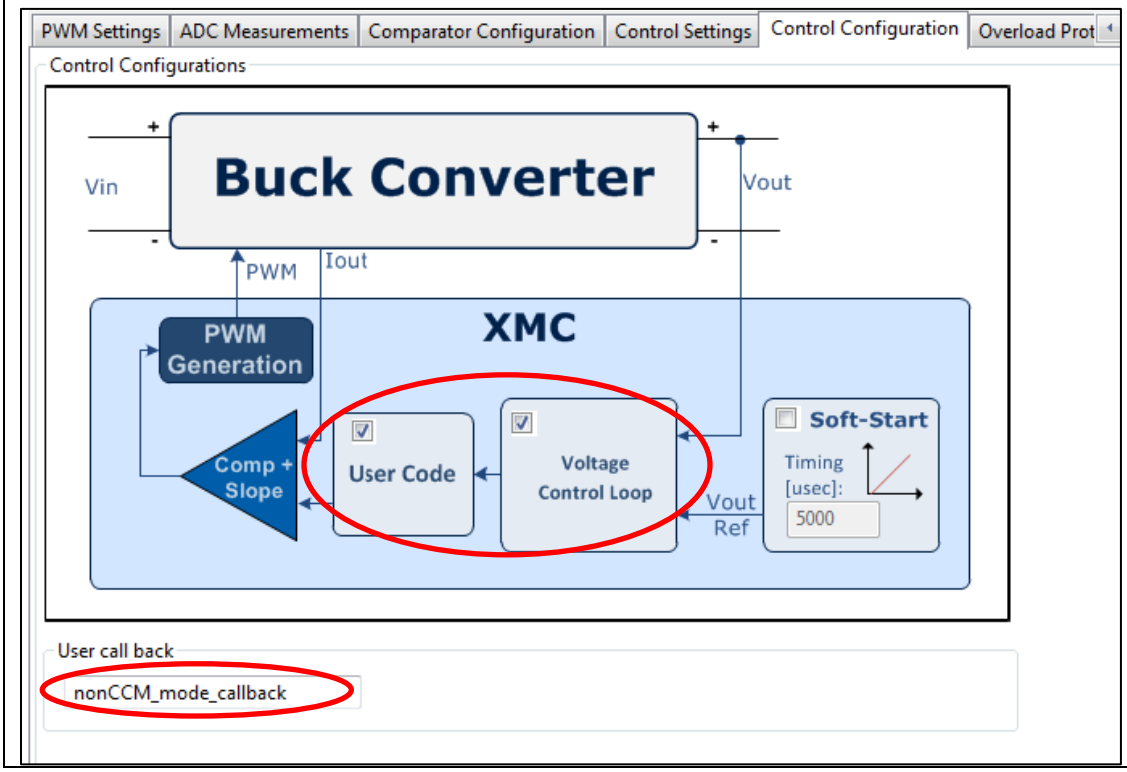
  - PWM Settings tab :



- Control Settings tab :

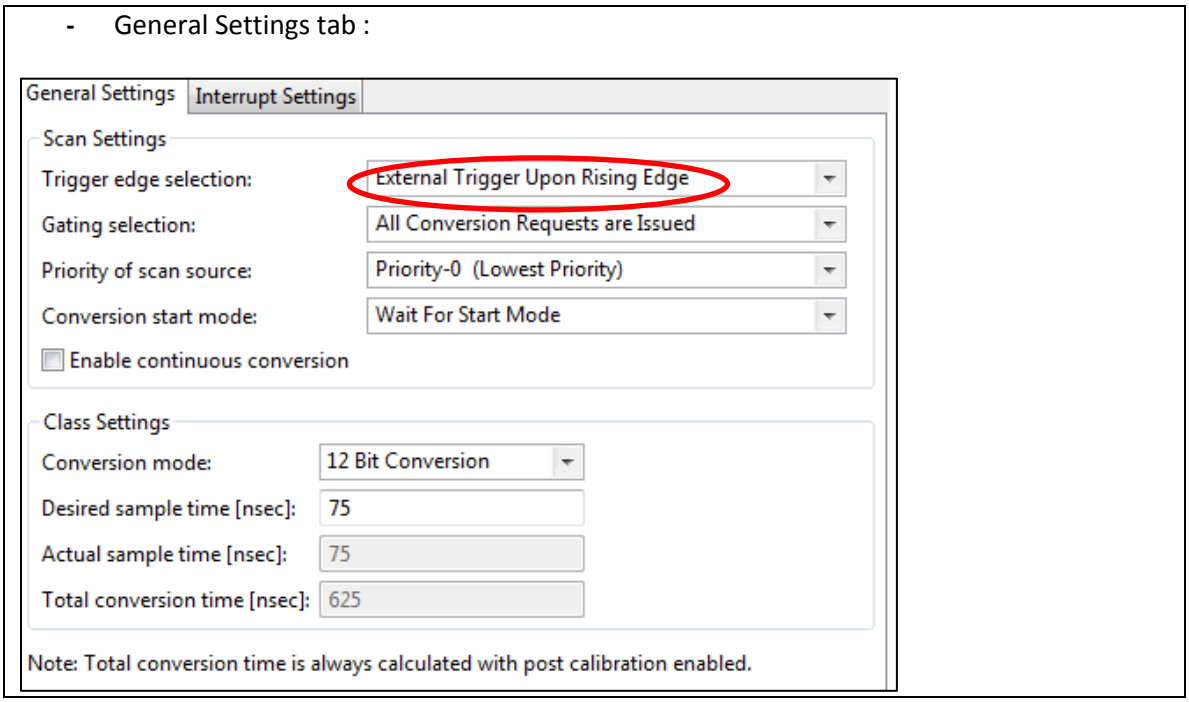


- Control Configuration tab :

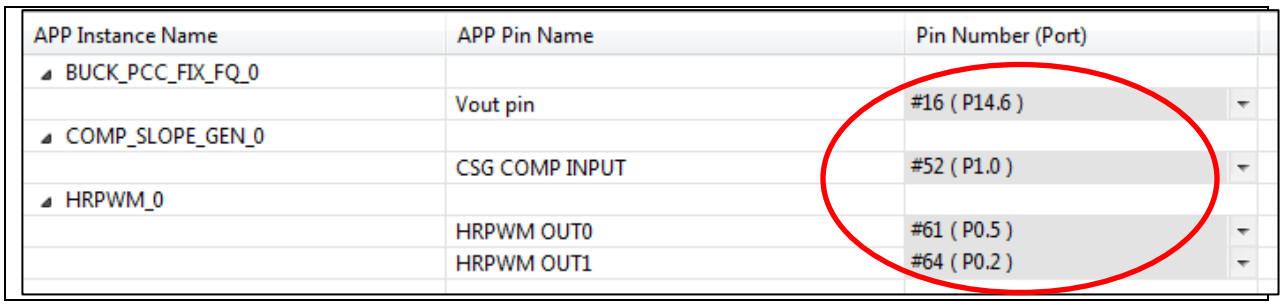


- **ADC_SCAN :**

  - General Settings tab :



3. Use the  "Manual Pin Allocator" found in the toolbar, configure the pins for the VADC, CSG and PWM outputs.



4. Generate the code for the configurations made and change main.c accordingly.

5. Build and download to the microcontroller.

HOW TO TEST:

1. Create the project
2. Prepare board set up: connect XMC4200 Digital Power Control Card into XMC Digital Power Explorer and supply power with included power adapter to it. Make sure power on switch is in the "on" position and that the jumper in power board is in the XMC4000 position

3. Generate code, compile and flash the application onto the device
4. Run the application.
5. Check via oscilloscope the different test points available in XMC Digital Power Explorer. Vout is now controlled to be stable at 3,3 V independent of load switching LEDs next to load switches must turn on when that particular load is activated.
6. If changes are needed in APPs configuration, make sure code is generated again before compiling