FM4 Family

32-BIT Microcontroller
FM4
Peripheral Manual Communication Macro Part

Errata Sheet

Page	Item	Description
Original document code: MN709-00004-2v0-E		
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959	CHAPTER 8-3 5.1.1	"5.1.1 Initial settings" should be corrected as indicated by the shading below. (Error) 2) Set operation permission and clock selection for the HS_SPI controller. Set HSSPIn_MCTRL.SYNCON, CDSS,CSEN, and MEN="1". Set "0" to SYNCON in direct mode. Set "0" (direct) or "1"(command sequencer) to CSEN. (Correct) 2) Set operation permission and clock selection for the HS_SPI controller. Set HSSPIn_MCTRL.SYNCON, CSEN, and MEN="1". Set "0" to SYNCON in direct mode. Set "0" (direct) or "1"(command sequencer) to CSEN.
964	CHAPTER 8-3 5.1.6	"5.1.6 Data read for serial memory (command sequencer mode)" should be corrected as indicated by the shading below. (Error) 6. Access to system space (read access to serial memory) 1) During read access when iHSEL_MEM="H", access serial memory in accordance of setting. (Correct) 6. Access to system space (read access to serial memory) 1) During read access, access serial memory in accordance of setting.
964	CHAPTER 8-3 5.1.7	"5.1.7 Data write for serial memory (command sequencer mode)" should be corrected as indicated by the shading below. (Error) 6. Access to system space (write access to serial memory) 1) During write access when iHSEL_MEM="H", access serial memory in accordance of setting. (Correct) 6. Access to system space (write access to serial memory) 1) During write access, access serial memory in accordance of setting.