Product Qualification Report

BGM787U50

The BGM787U50 is a LNA bank with 7 LNAs (2x Low band, 5x Mid/High band) with a complex 7P7T output cross-switch designed for 4G/5G cellular RX frontends supporting EN_DC, CA and MIMO operation. Low band LNAs support a frequency range from 600 MHz to 1500 MHz, high band LNAs from 1400 MHz to 2690 MHz. Each LNA supports 10 gain modes to be compatible with all relevant modem platforms. The wideband LNA design together with a highly configurable output MUX offers maximum system design flexibility.

Description

This product qualification report describes the characteristics of the product with respect to quality and reliability.

The qualification sample selection was done on production lots which were manufactured and tested on standard production processes and meet the defined requirements.

The qualification test results of those products as outlined in this document are based on **JEDEC** for target applications and may reference existing qualification results of similar products. Such referencing is justified by the structural similarity of the products.

Qualification Assessment

Fully qualified according to **JEDEC** for **Industrial Applications** and assessed as PASS

For further information about comparable products, please contact the nearest Infineon Technologies office (www.infineon.com).

BGM787U50 **qualified 2025**
PG-WF2BGA-50-2
MSL: 3; 260 °C

Electrical Stress Test Results:

Test Description	Abbr.	Condition	Duration	Lots/SS	Fail/Qty	Result
Electrical Parameter Assessment JESD86	ED	-40 °C, 25 °C, 85 °C		3 x 10	0 / 30	PASS
High Temperature Operating Life JESD22-A108	HTOL	T_a = 115 °C Tj ≥ 125 °C $V_{dd} \geq V_{dd_max}$	1000 h	3 x 77	0 / 231	PASS
Electrostatic Discharge Human Body Model JS-001	ESD-HBM	Class 1C 1000 V to < 2000 V		1 x 3	0 / 3	PASS
Electrostatic Discharge Charged Device Model JS-002	ESD-CDM	Class C2a 500 to < 750 V		1 x 3	0 / 3	PASS
Latch Up JESD78	LU	T_a = 85 °C $I_{trigger}$ = 150 mA		1 x 6	0 / 6	PASS

Environmental Stress Test Results:

Test Description	Abbr.	Condition	Duration	Lots/SS	Fail/Qty	Result
High Temperature Storage Life JESD22-A103	HTSL	T_a = 150 °C	1000 h	3 x 25	0 / 75	PASS
Pre-conditioning JESD22-A113	PC	MSL-3: Ta = 30 °C, 60% RH, t = 192 h 5 x reflow at 260 °C		3 x 75	0 / 225	PASS
Temperature Cycling JESD22-A104	TC*	-40 °C to 125 °C	1000 cycles	3 x 25	0 / 75	PASS
Highly Accelerated Temperature and Humidity Stress Test JESD22-A110	HAST*	T_a = 130 °C rh = 85 % $V_{dd} \geq V_{dd_max}$	96 h	3 x 25	0 / 75	PASS
Unbiased Highly Accelerated Stress Test JESD22-A118	UHAST*	T_a = 130 °C rh = 85 %	96 h	3 x 25	0 / 75	PASS

Mechanical Stress Test Results:

Test Description	Abbr.	Condition	Duration	Lots/SS	Fail/Qty	Result
Physical Dimensions JESD22-B100	PD			1 x 30	0 / 30	PASS
Solderability J-STD-002	SD			3 x 22	0 / 66	PASS

Notes:
* For SMD devices reliability stress tests performed after preconditioning test (PC) according to JESD22-A113

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Document reference

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