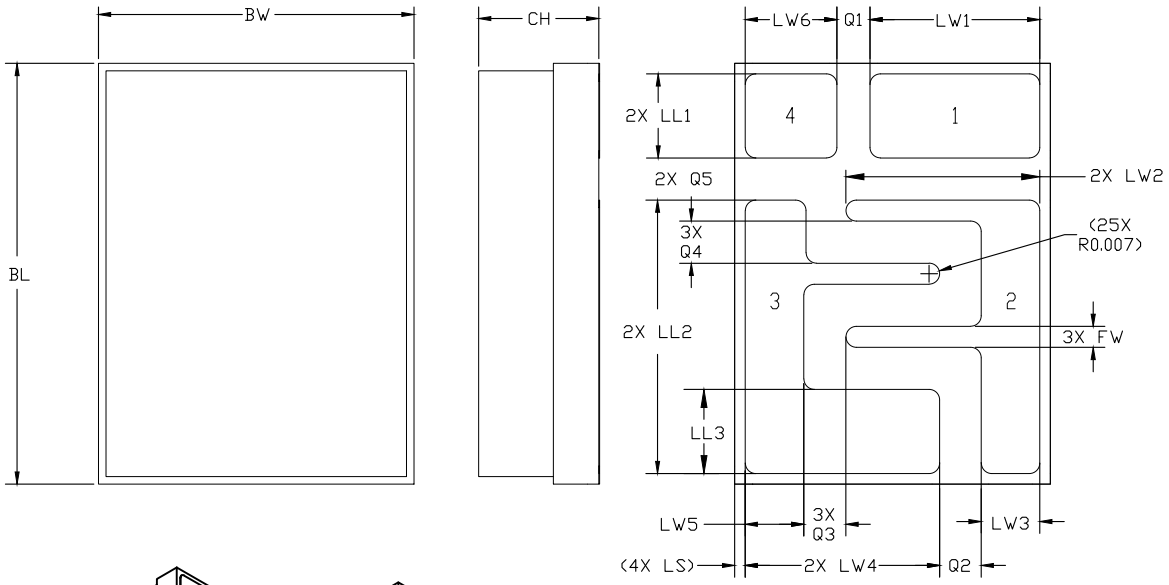
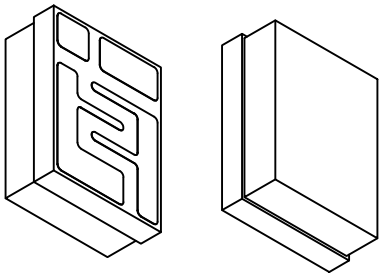
REV.	DESCRIPTION	ECN	DATE
C	CHANGE PER ECN	Z8F80855955	10-14-25





Pin	Function
1	Kelvin Source
2	Drain
3	Source
4	Gate

DIMENSIONS						
	INCHES			MILLIMETERS		
SYMBOL	MIN	NOM	MAX	MIN	NOM	MAX
BL	.275	.280	.285	6.99	7.11	7.24
BW	.205	.210	.215	5.21	5.33	5.46
CH			.100			2.54
LL1	.051	.056	.061	1.30	1.42	1.55
LL2	.177	.182	.187	4.50	4.62	4.75
LL3	.051	.056	.061	1.30	1.42	1.55
LW1	.108	.113	.118	2.74	2.87	3.00
LW2	.124	.129	.134	3.15	3.28	3.40
LW3	.034	.039	.044	0.86	0.99	1.12
LW4	.124	.129	.134	3.15	3.28	3.40
LW5	.034	.039	.044	0.86	0.99	1.12
LW6	.056	.061	.066	1.42	1.55	1.68
Q1	.019			0.48		
Q2−Q5	.024			0.61		
LS		(.007)			(0.18)	
FW	.009	.014	.019	0.23	0.36	0.48

NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M.
2. CONTROLLING DIMENSION: INCHES
3. LEAD PLATING IS GOLD. STANDARD FINAL LEAD FINISH IS SOLDER ALLOY 63Sn/37Pb. DIMENSIONS APPLY BEFORE SOLDER FINISH.

IOR HiRel
An Infineon Technologies Company

TITLE: CASE OUTLINE,
LOW VOLTAGE GaN, PowIR−SMD

DRAWING NO.
D101028G−WEB

REV C