

EVAL_PMG1B1_DRP_KIT

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Drawing Numbers

PCBA	121-60657-01
PCB	600-60657-01
FAB DRA	610-60657-01
ASSY DRA	620-60657-01
SCH DRA	630-60657-01

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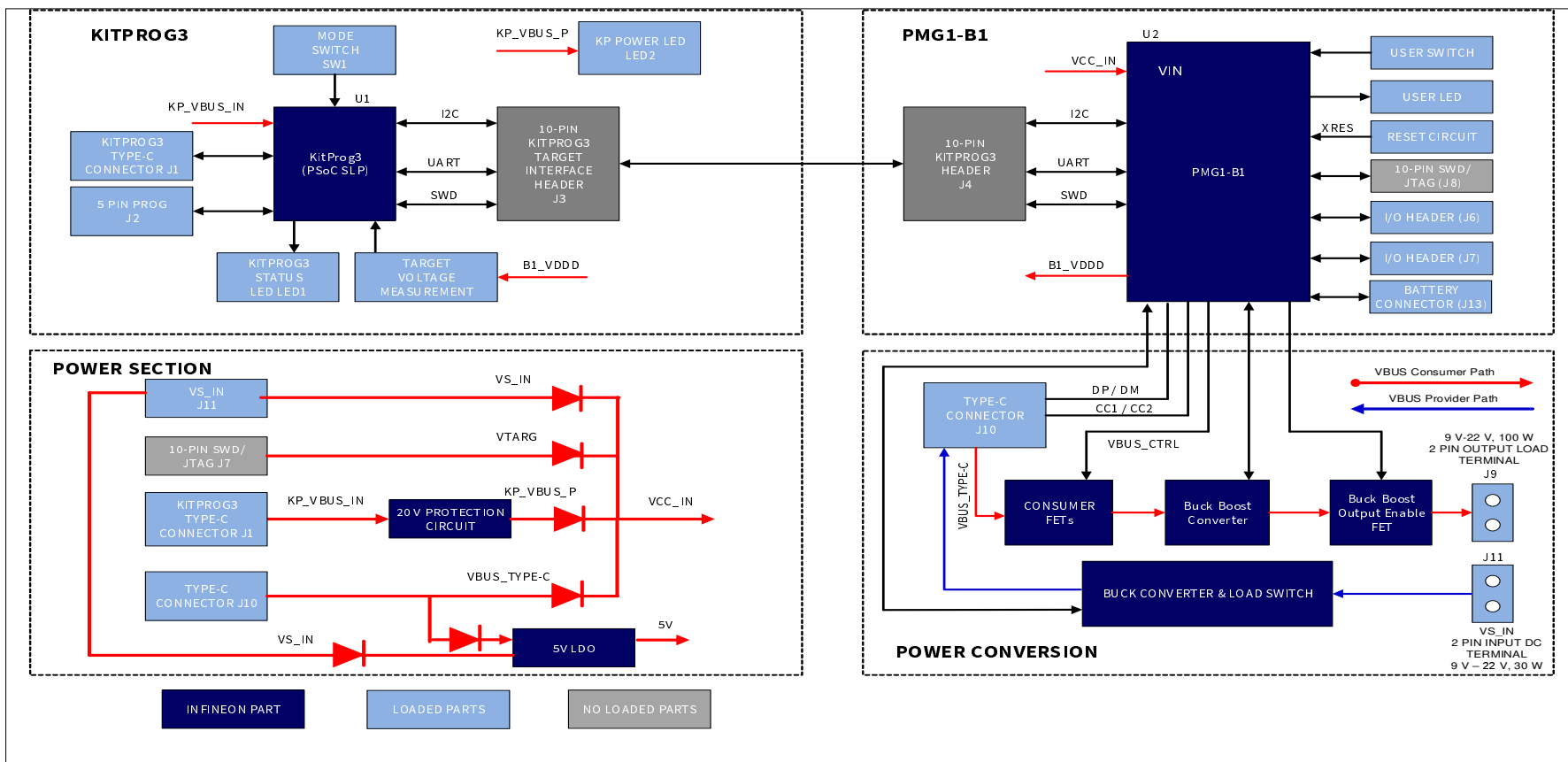
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Page Title : TITLE & TABLE OF CONTENTS

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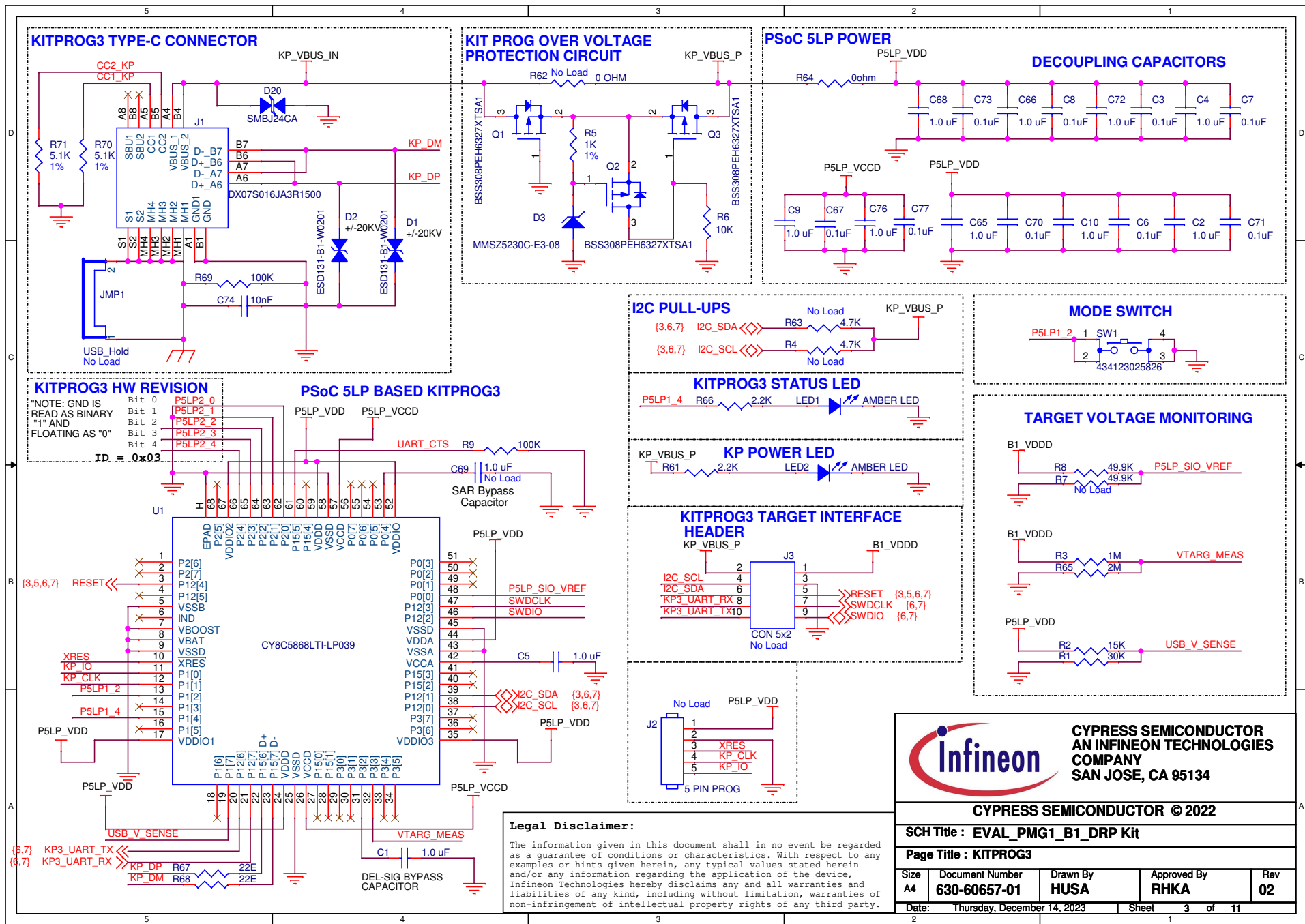
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Page Title : BLOCK DIAGRAM

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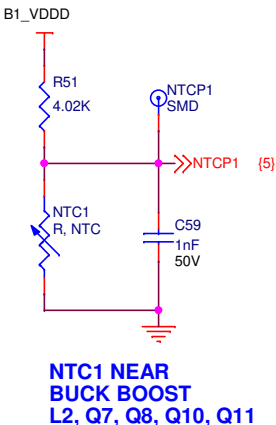
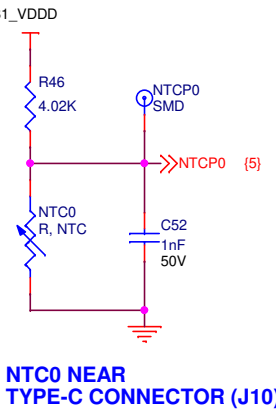
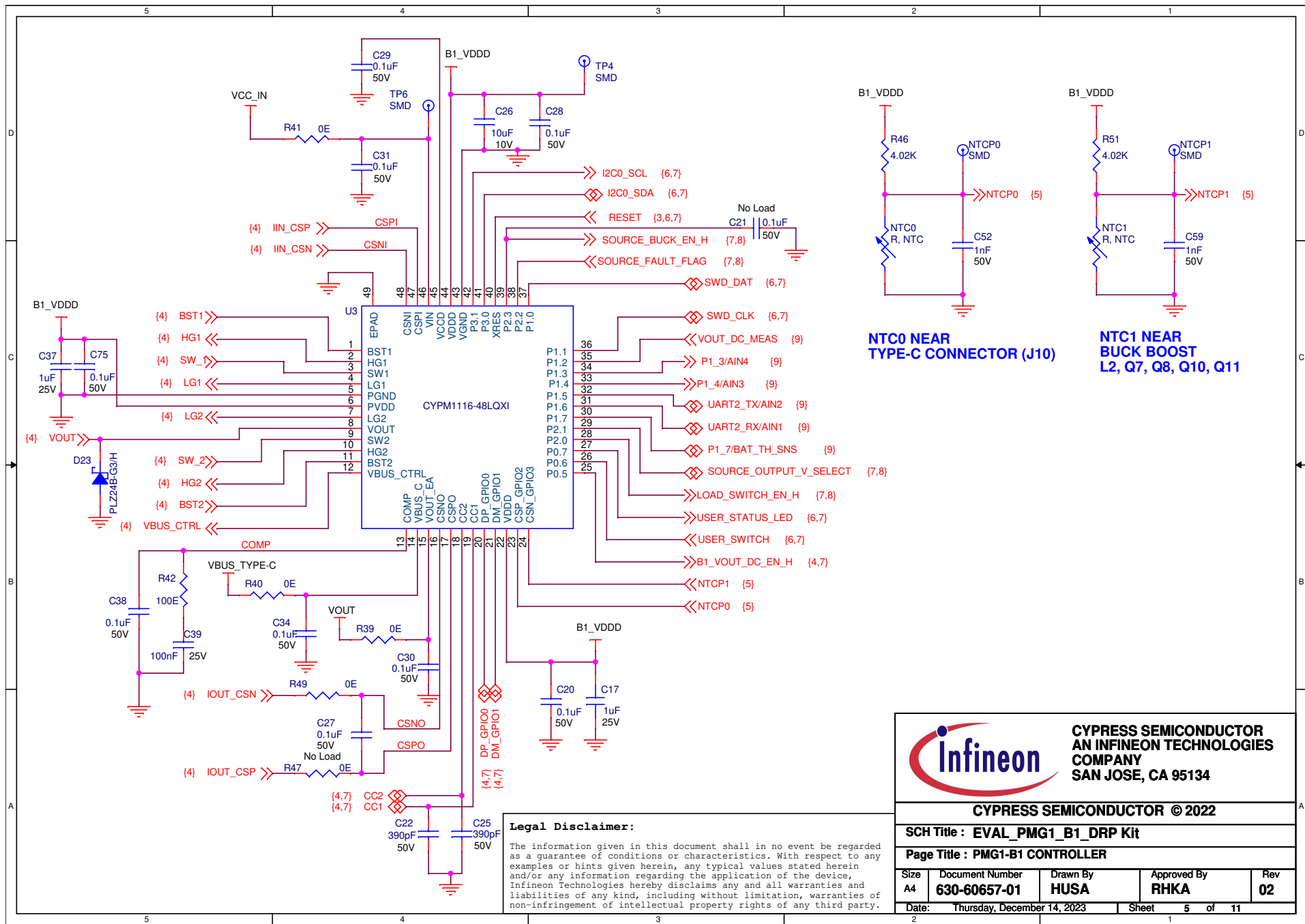
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Page Title : KITPROG3

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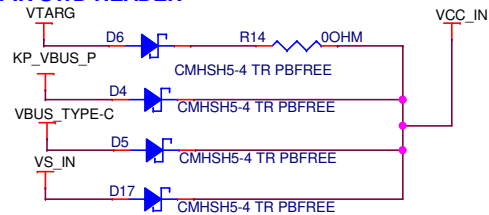
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Page Title : PMG1-B1 CONTROLLER

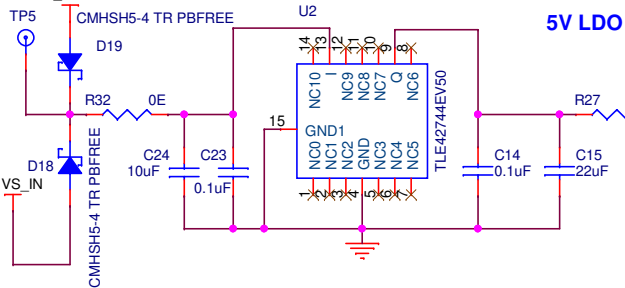
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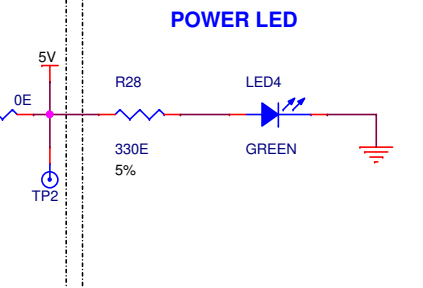
INPUT SUPPLY FROM KITPROG3 & 10 PIN SWD HEADER



VBUS_TYPE-C



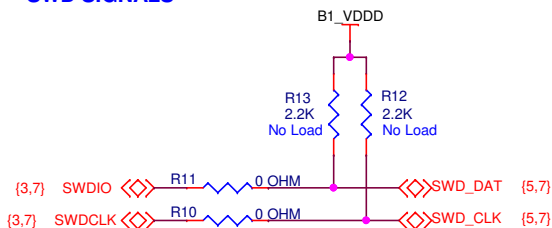
5V LDO



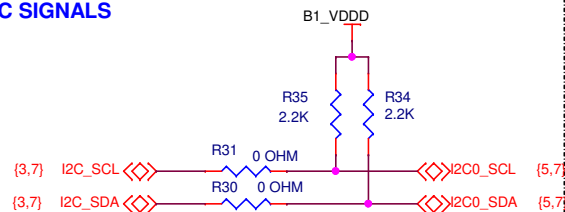
POWER LED



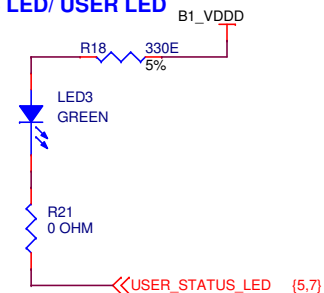
SWD SIGNALS



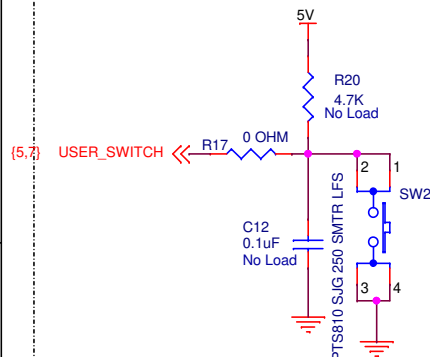
I2C SIGNALS



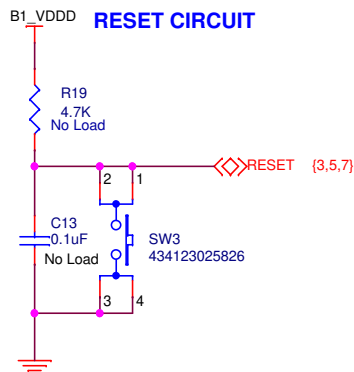
STATUS LED/ USER LED



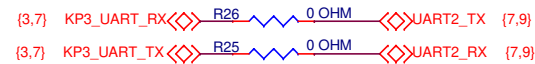
USER SWITCH



RESET CIRCUIT



UART SIGNAL



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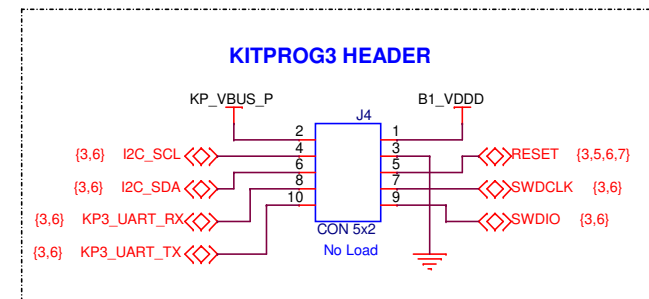
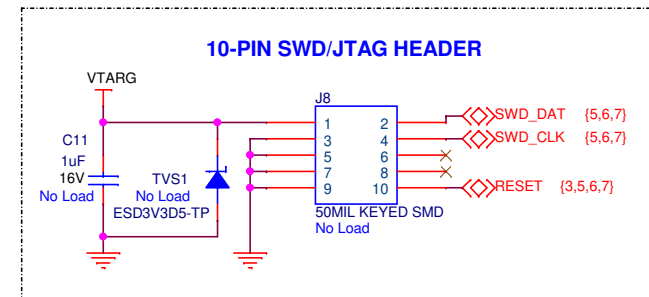
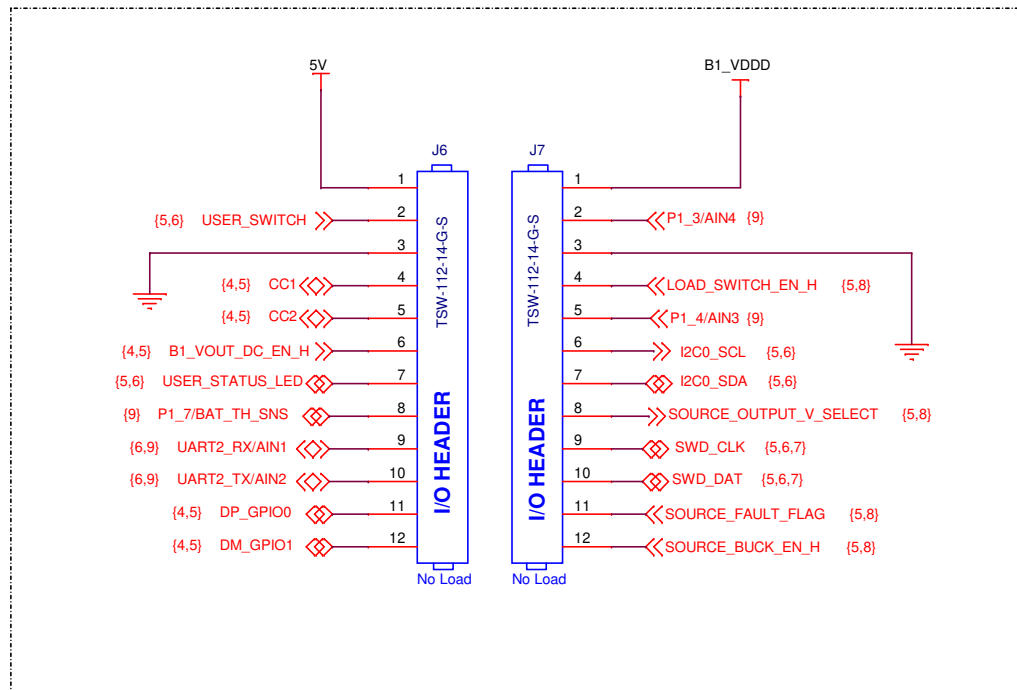
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Page Title : INTERFACE SIGNALS

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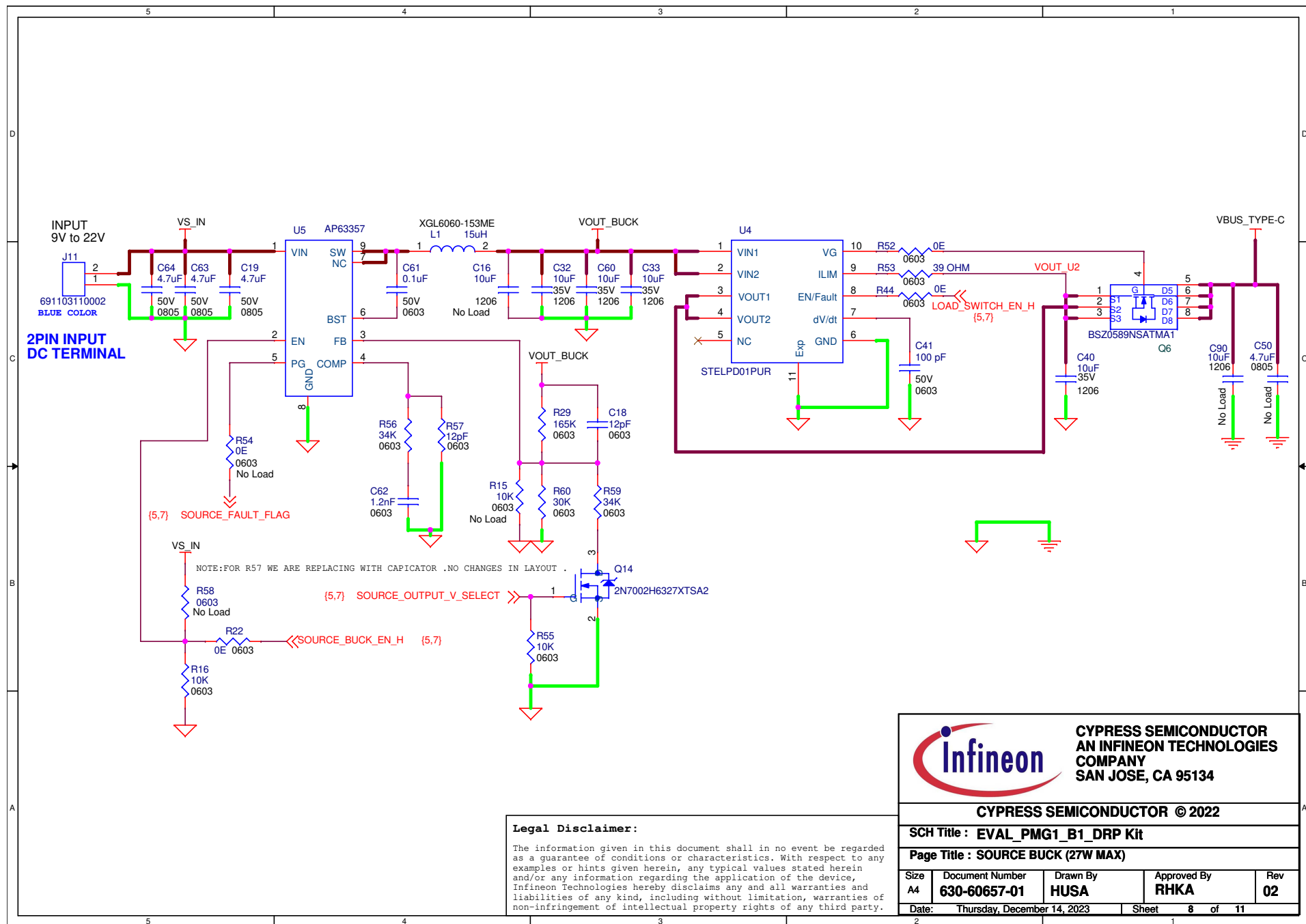
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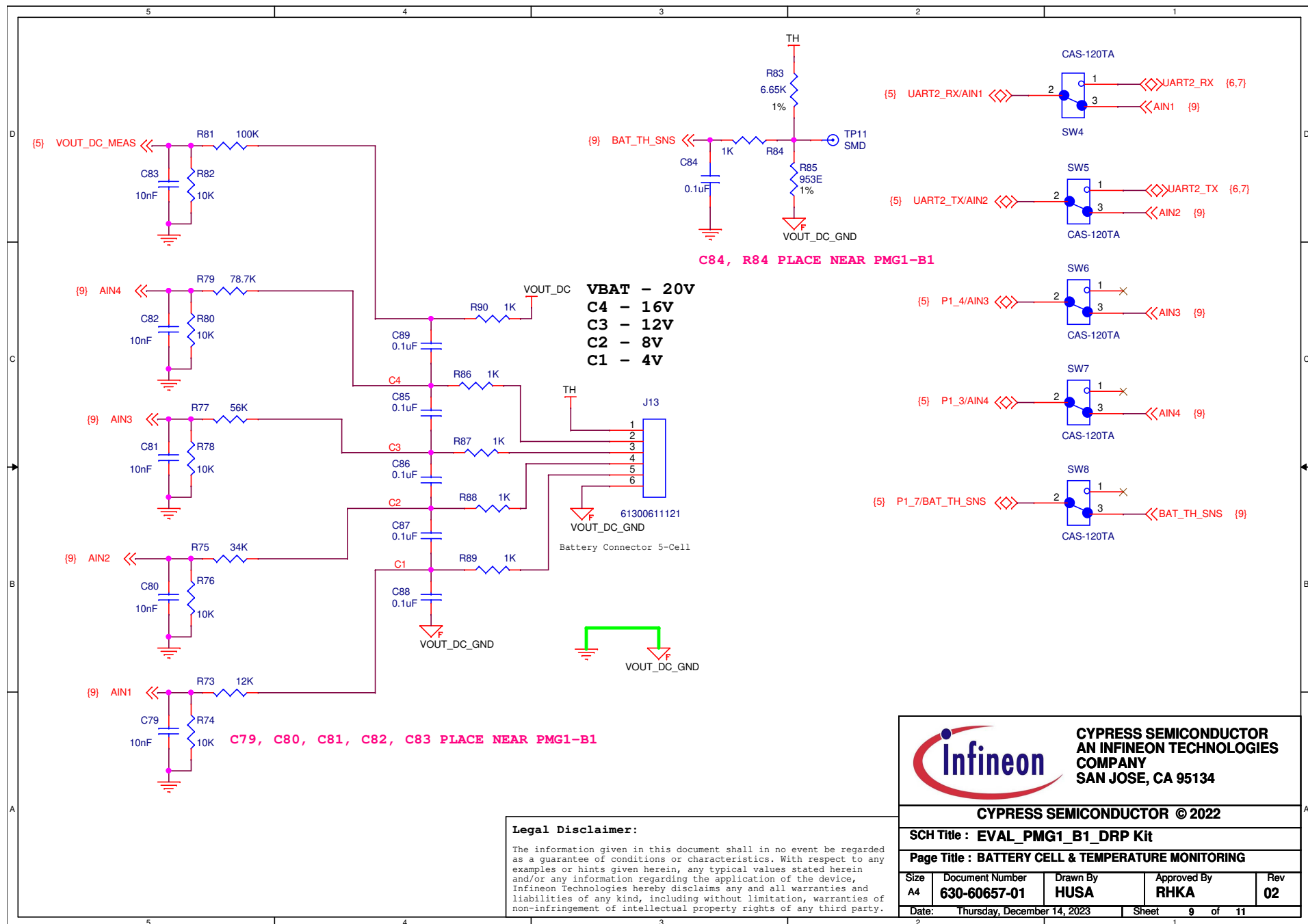
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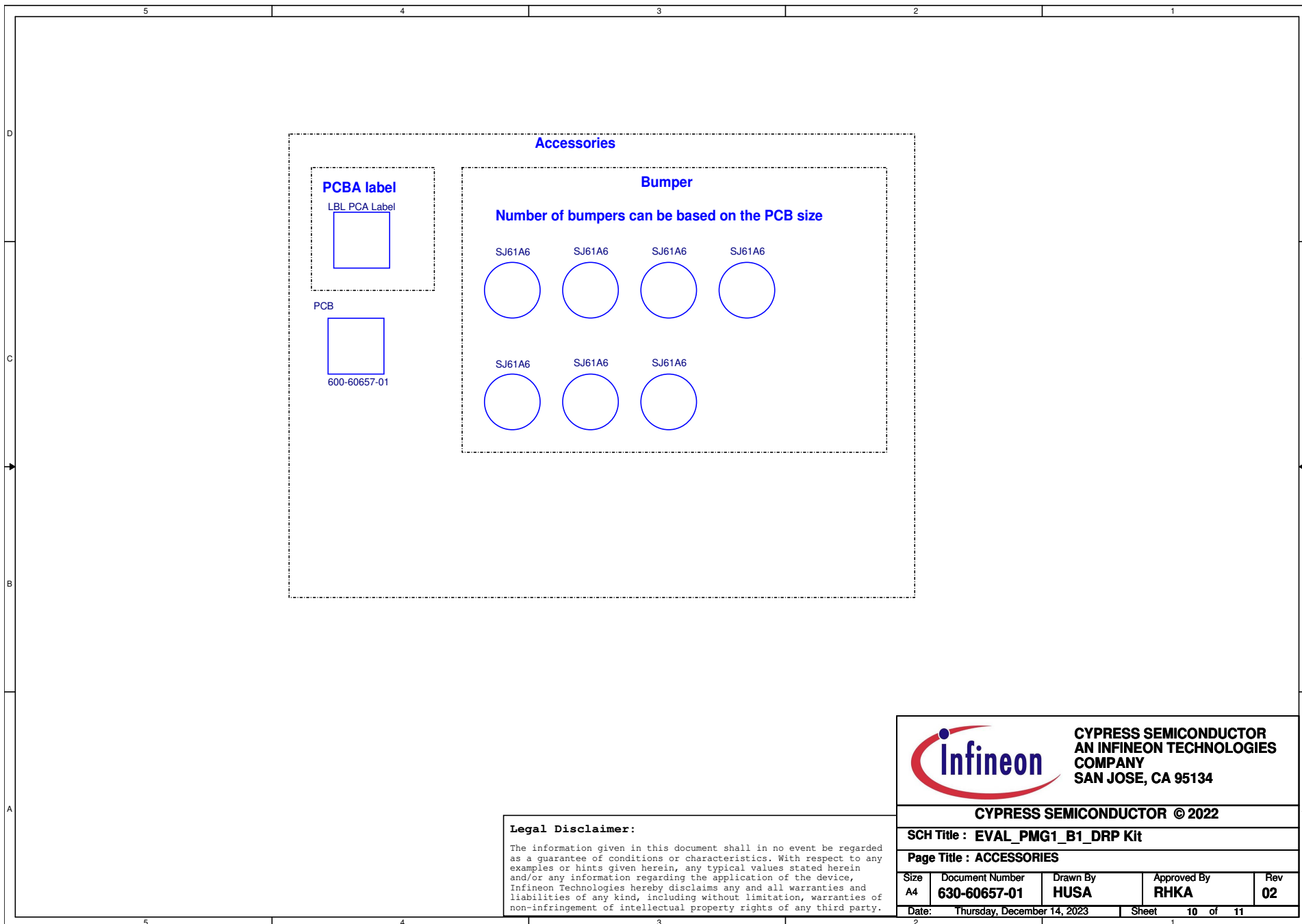
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Page Title : CONNECTOR INTERFACES

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REVISION HISTORY

REV	DESCRIPTION OF CHANGE	Orig. of Change	DATE
01	Initial Release		2022/04/05
02	Q5 MOSFET short between Drain to Source removed.		2022/07/25

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