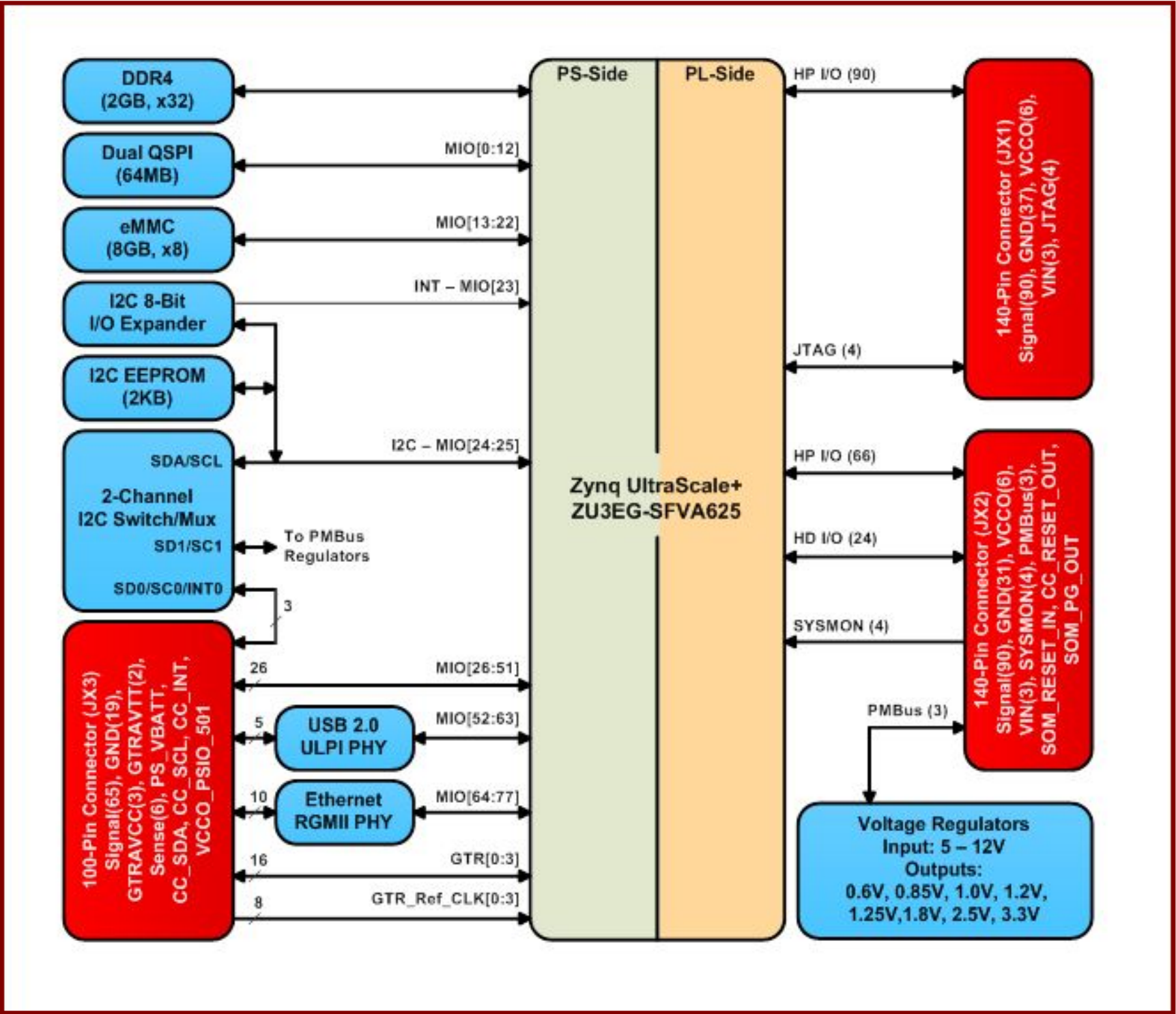
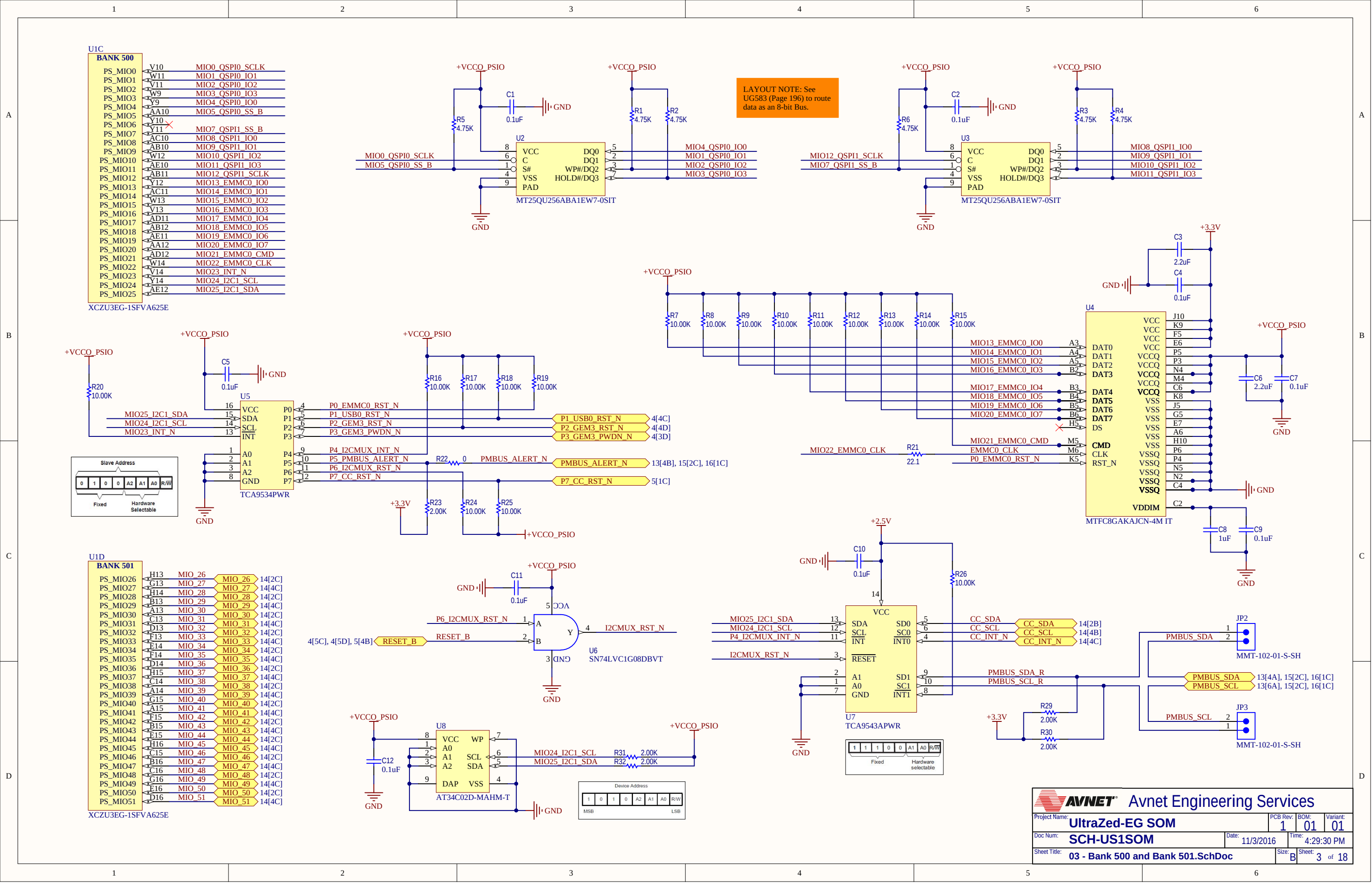
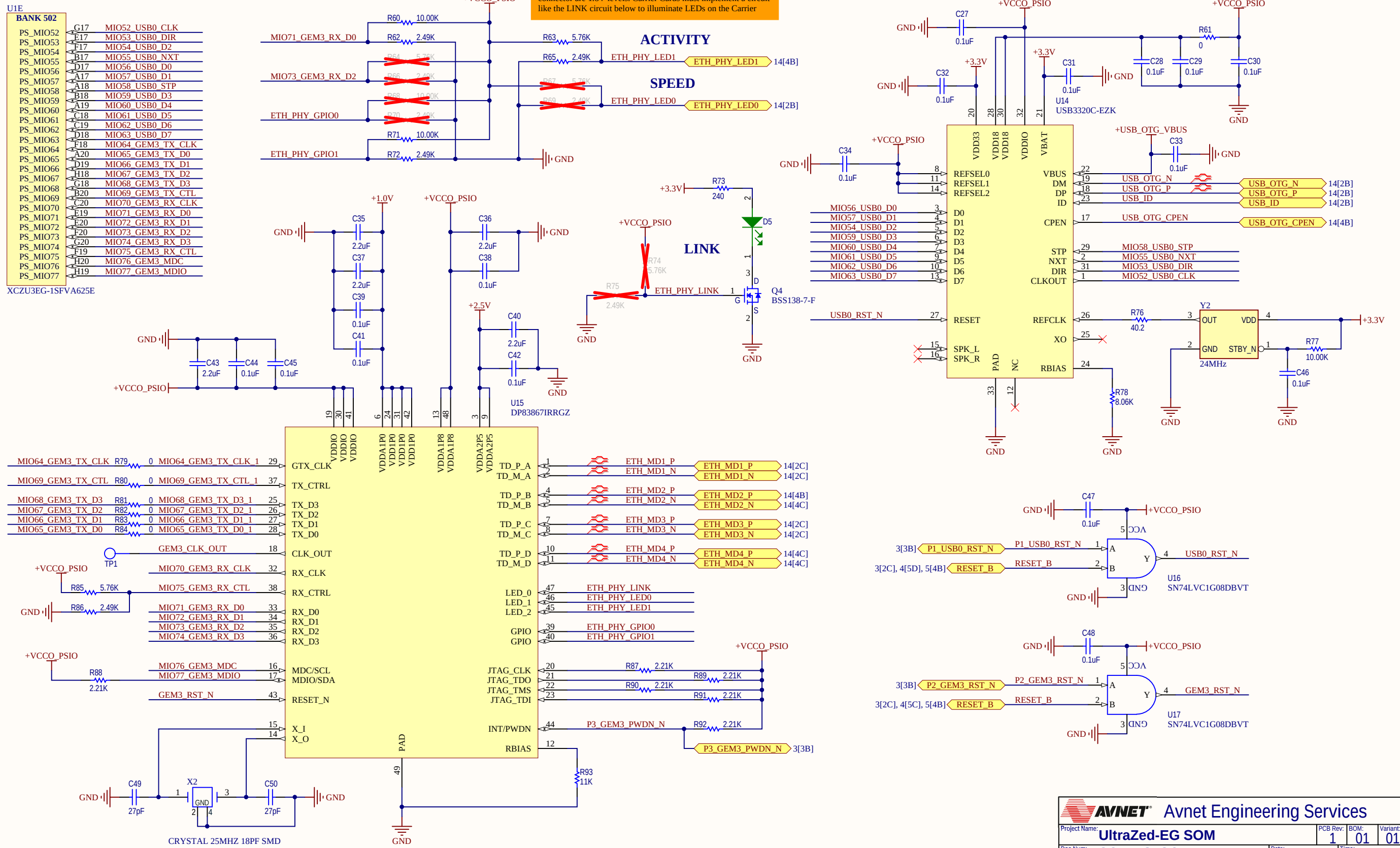


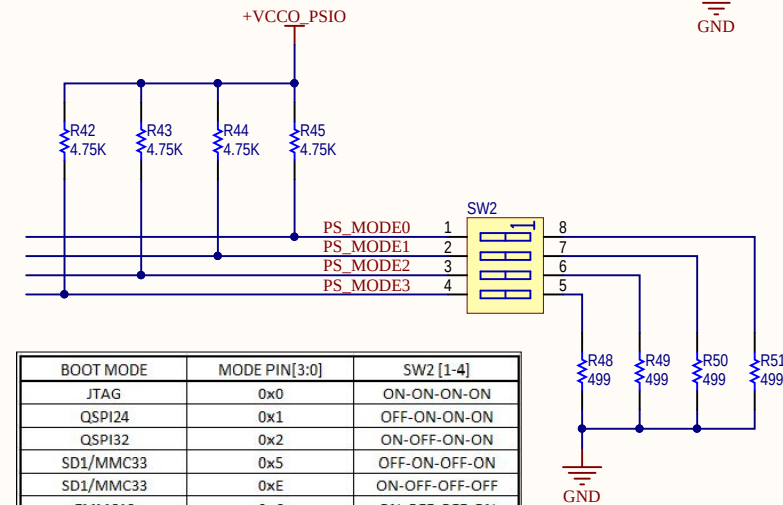
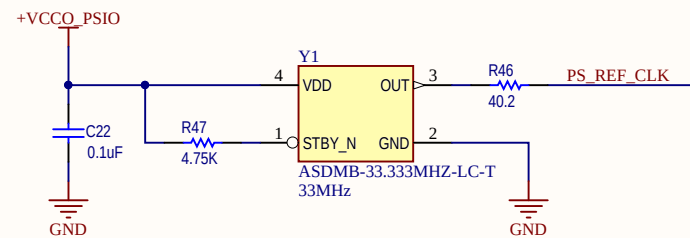
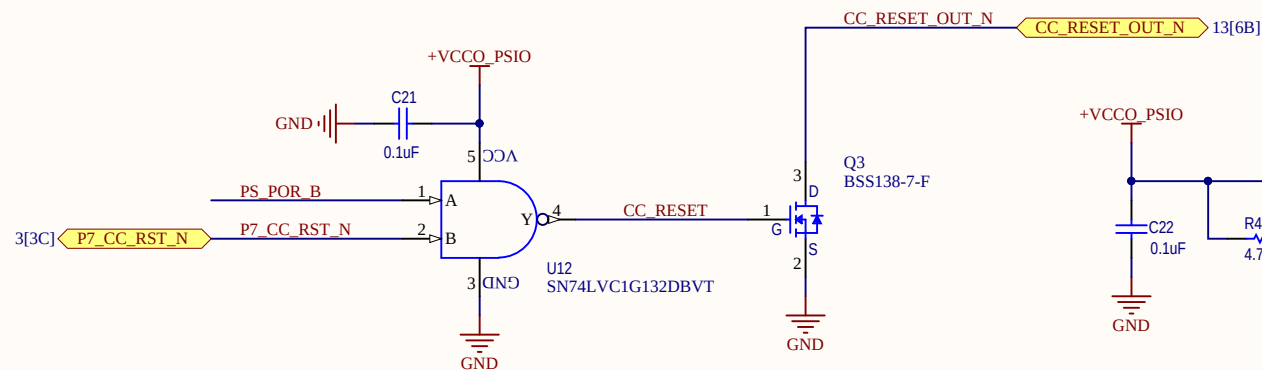
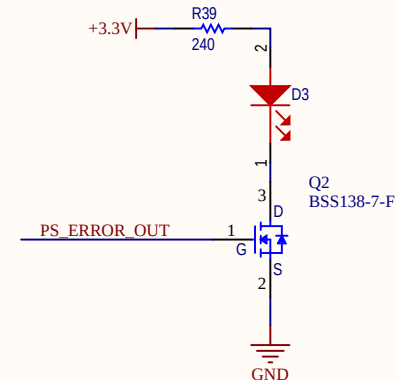
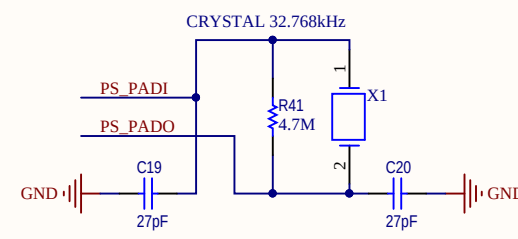
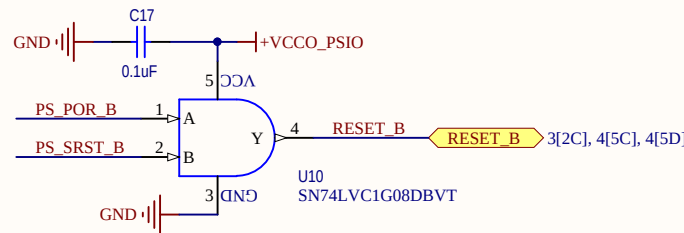
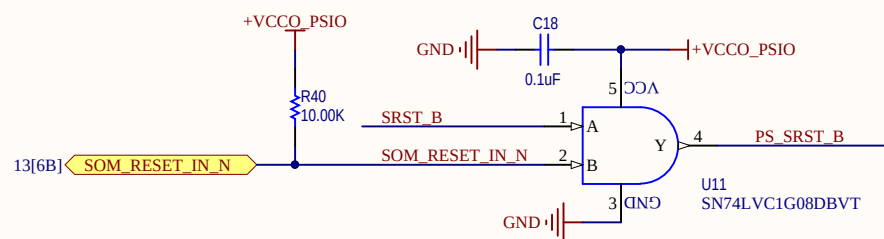
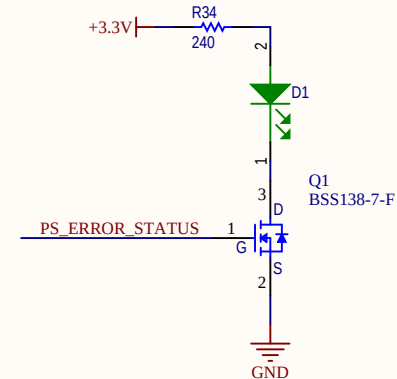
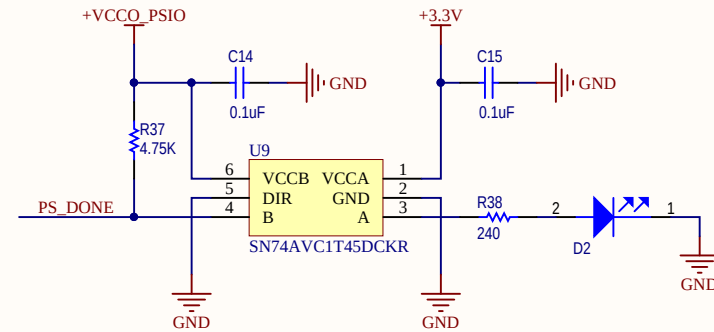
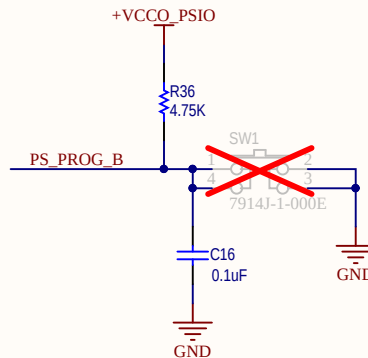
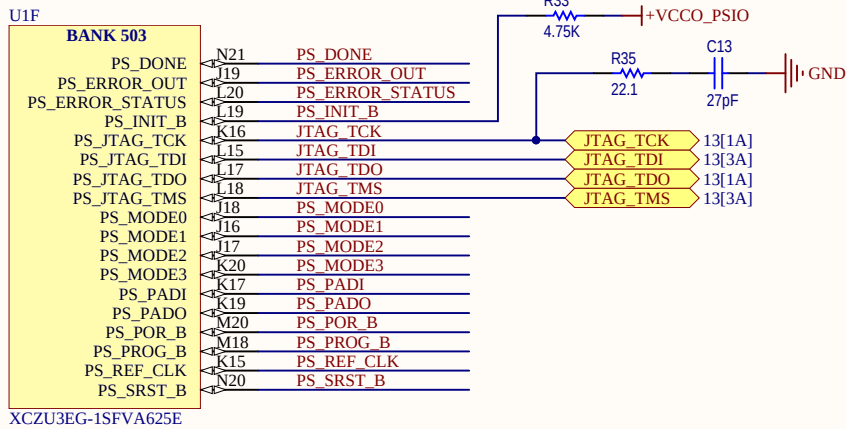
1		2	3	4	5	6
A	UltraZed-EG SOM Avnet Engineering Services www.avnetkits.com Sheet Name 01 - Avnet Lead Sheet 02 - Block Diagram 03 - Bank 500 and Bank 501 04 - Bank 502 05 - Bank 503 06 - Bank 504 07 - Bank 505 08 - Bank 0 and Bank 26 09 - Bank 64, Bank 65, and Bank 66 10 - Power and Decoupling 11 - DDR4 Device #1 12 - DDR4 Device #2 13 - JX1 and JX2 Micro Headers 14 - JX3 Micro Header 15 - Rocky PMIC #1 16 - Rocky PMIC #2 17 - Power Supplies 18 - Back Page					
	AVNET®					
	UltraZed-EG SOM					
	Revision 1					
B						
C						
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	AVNET® Avnet Engineering Services Project Name: UltraZed-EG SOM Doc Num: SCH-US1SOM Sheet Title: 01 - Avnet Lead Sheet.SchDoc PCB Rev: 1 Date: 11/3/2016 Size: B BOM: 01 Time: 4:29:30 PM Variant: 01 Sheet: 1 of 18					
	1	2	3	4	5	6



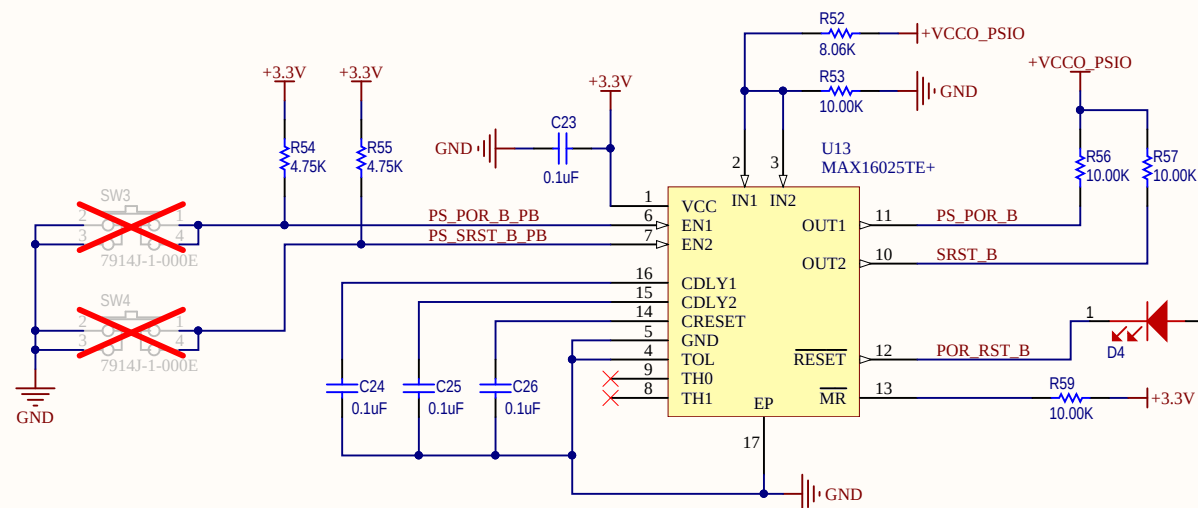


SCHEMATIC NOTE: ACTIVITY and SPEED signals to the JX connector are 1.8V levels. Carrier Cards must implement a circuit like the LINK circuit below to illuminate LEDs on the Carrier

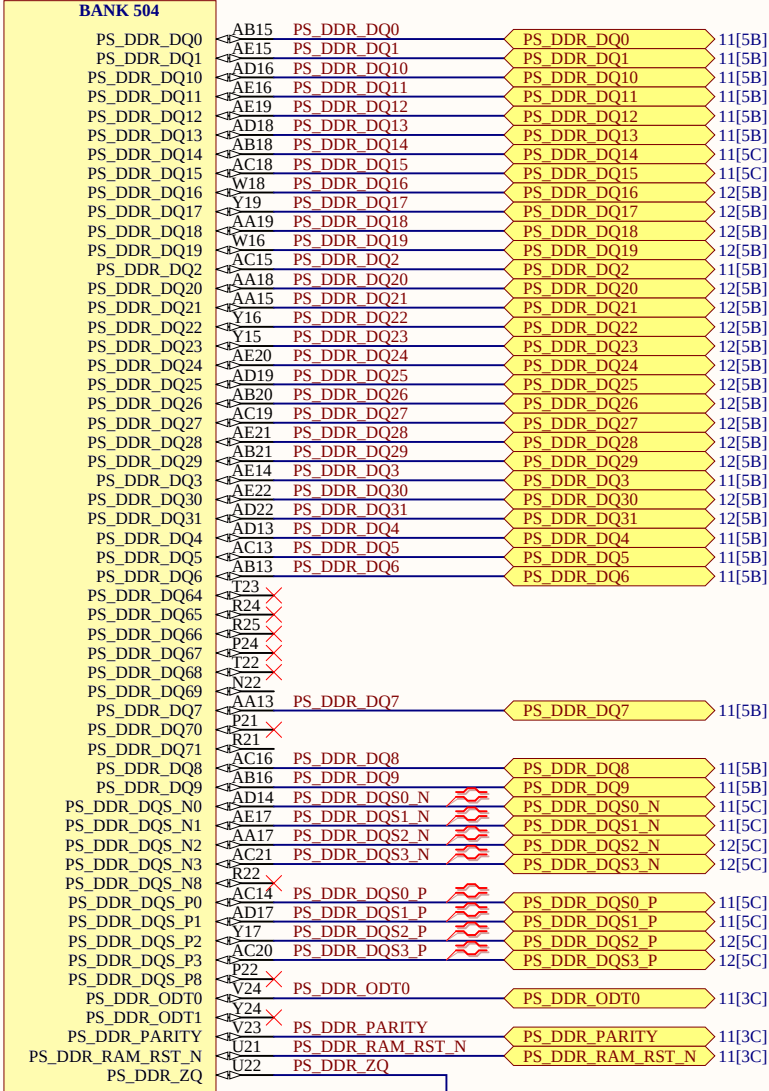




BOOT MODE	MODE PIN[3:0]	SW2 [1-4]
JTAG	0x0	ON-ON-ON-ON
QSPI24	0x1	OFF-ON-ON-ON
QSPI32	0x2	ON-OFF-ON-ON
SD1/MMC33	0x5	OFF-ON-OFF-ON
SD1/MMC33	0xE	ON-OFF-OFF-OFF
EMMC18	0x6	ON-OFF-OFF-ON



U1P

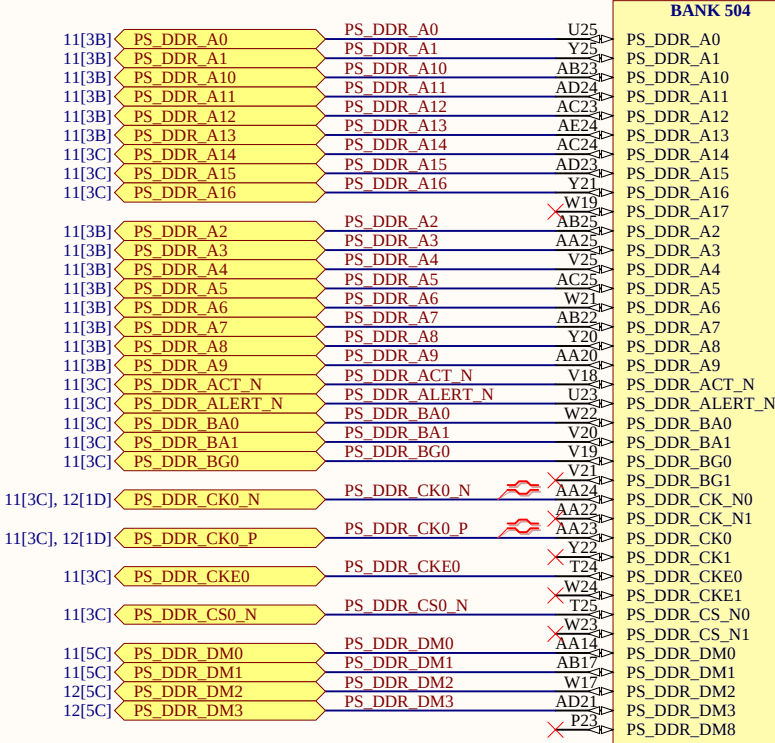


XCZU3EG-1SFVA625E

LAYOUT NOTE:
Place ZQ resistor
close to U1.



U1O

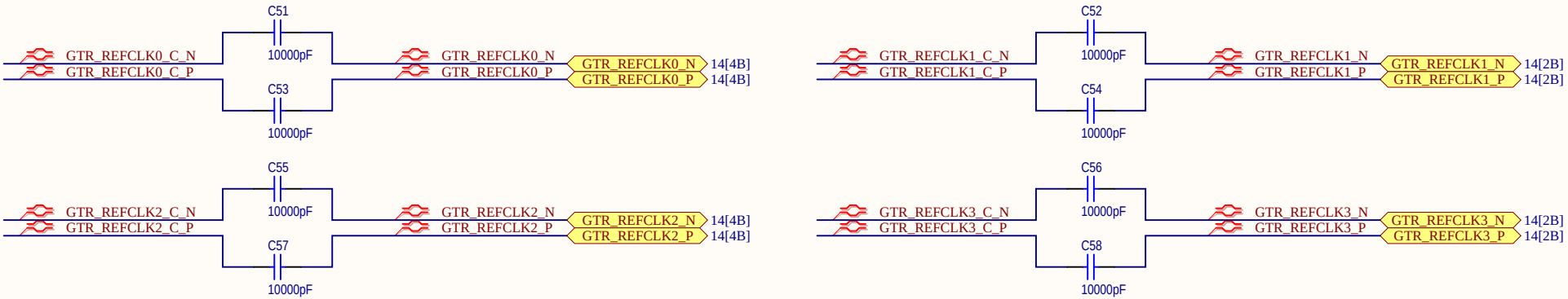
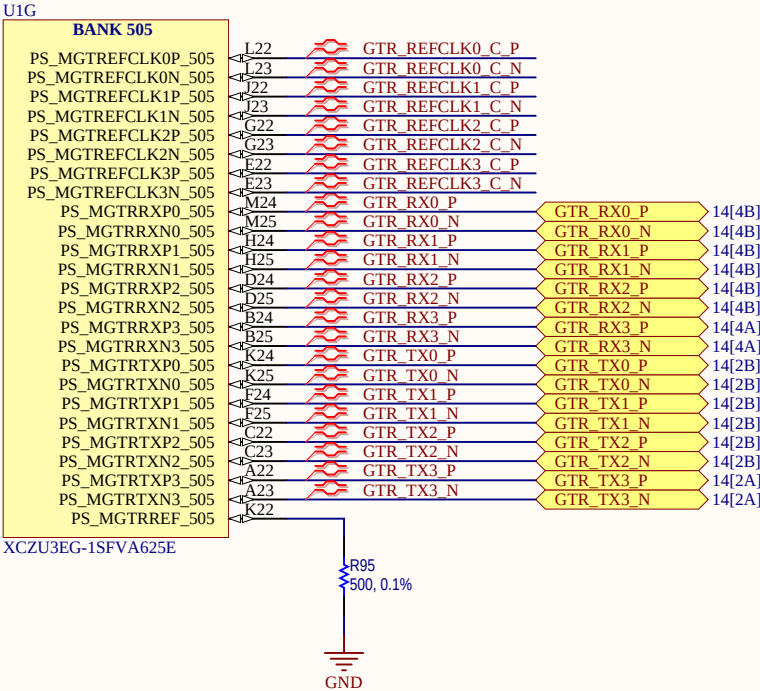


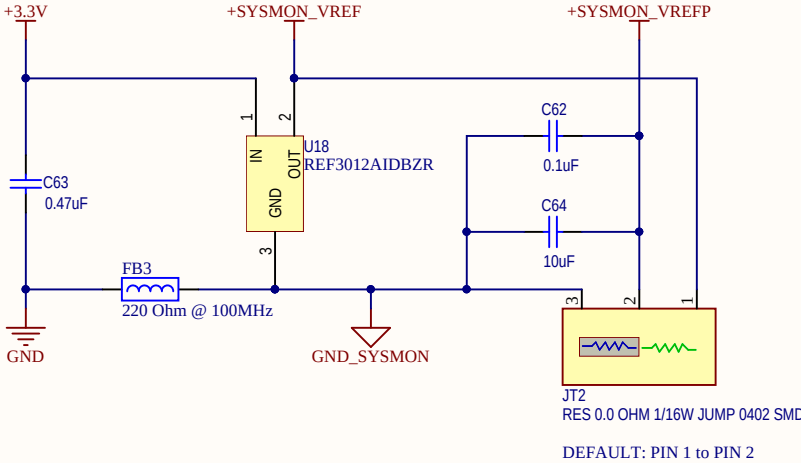
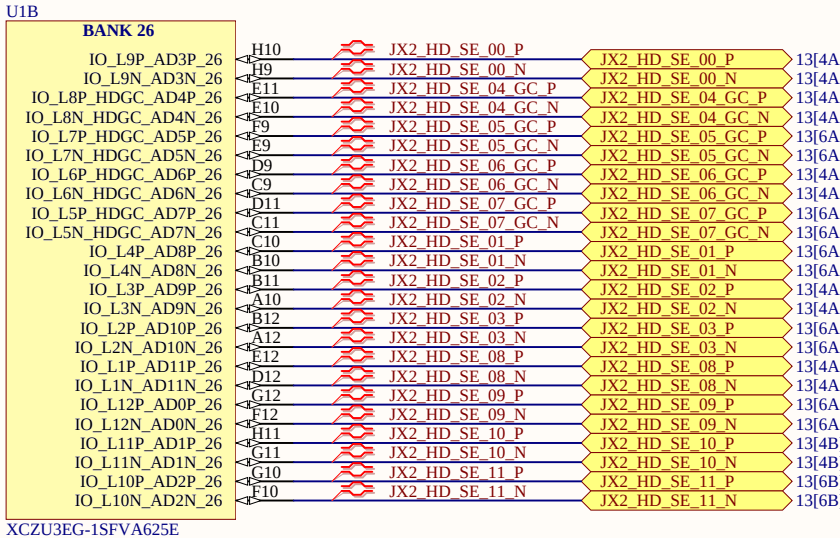
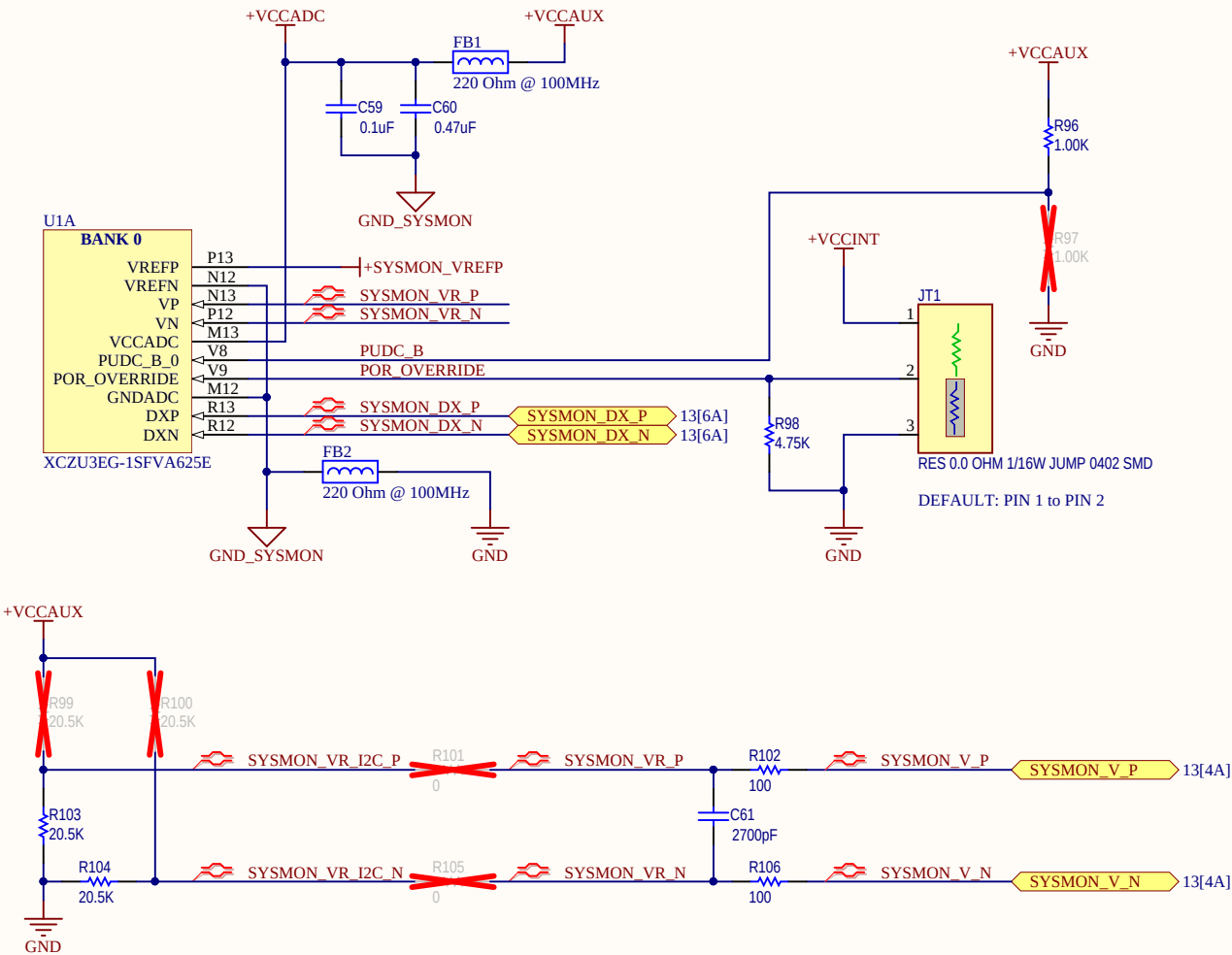
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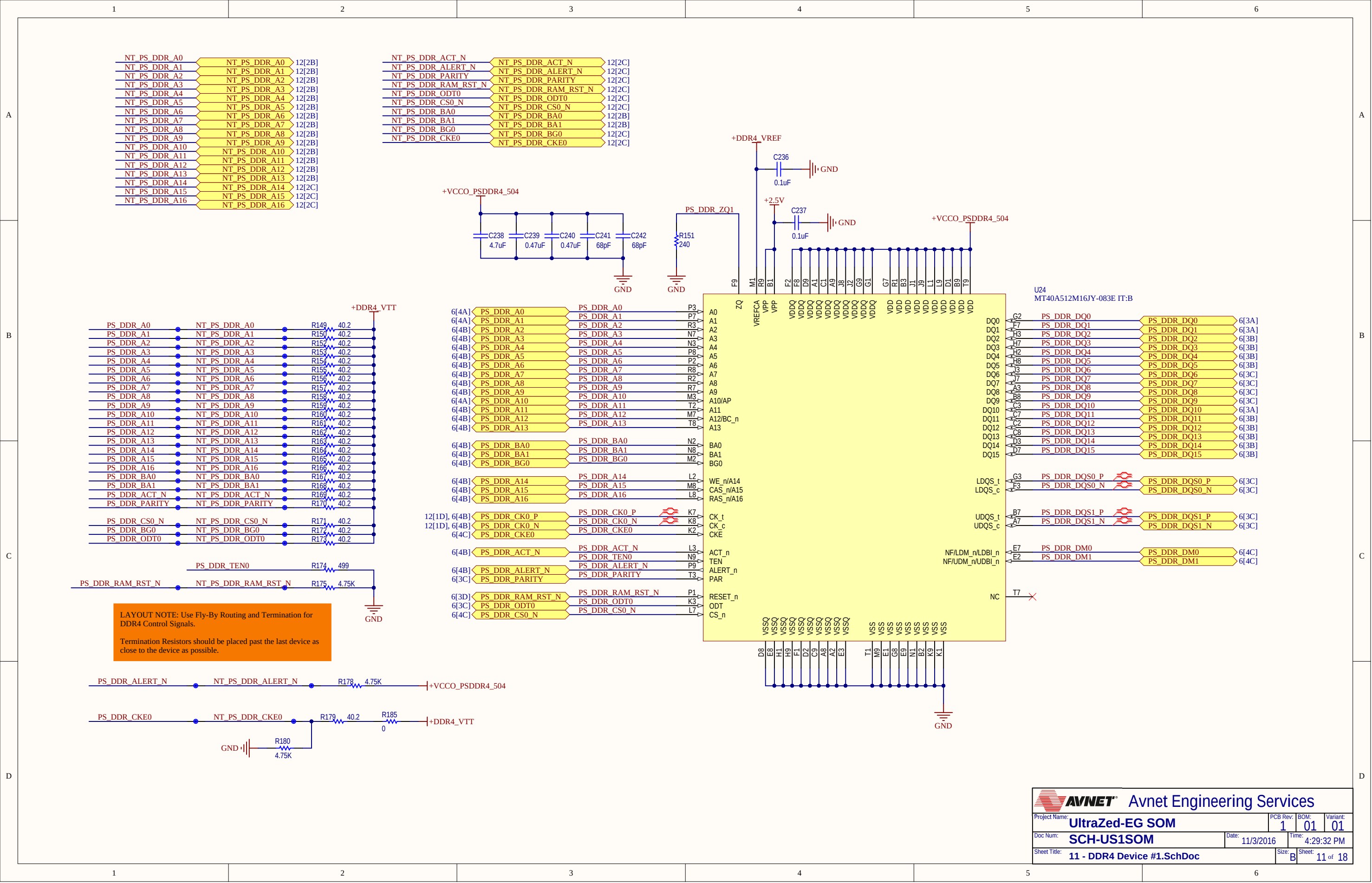
Avnet Engineering Services

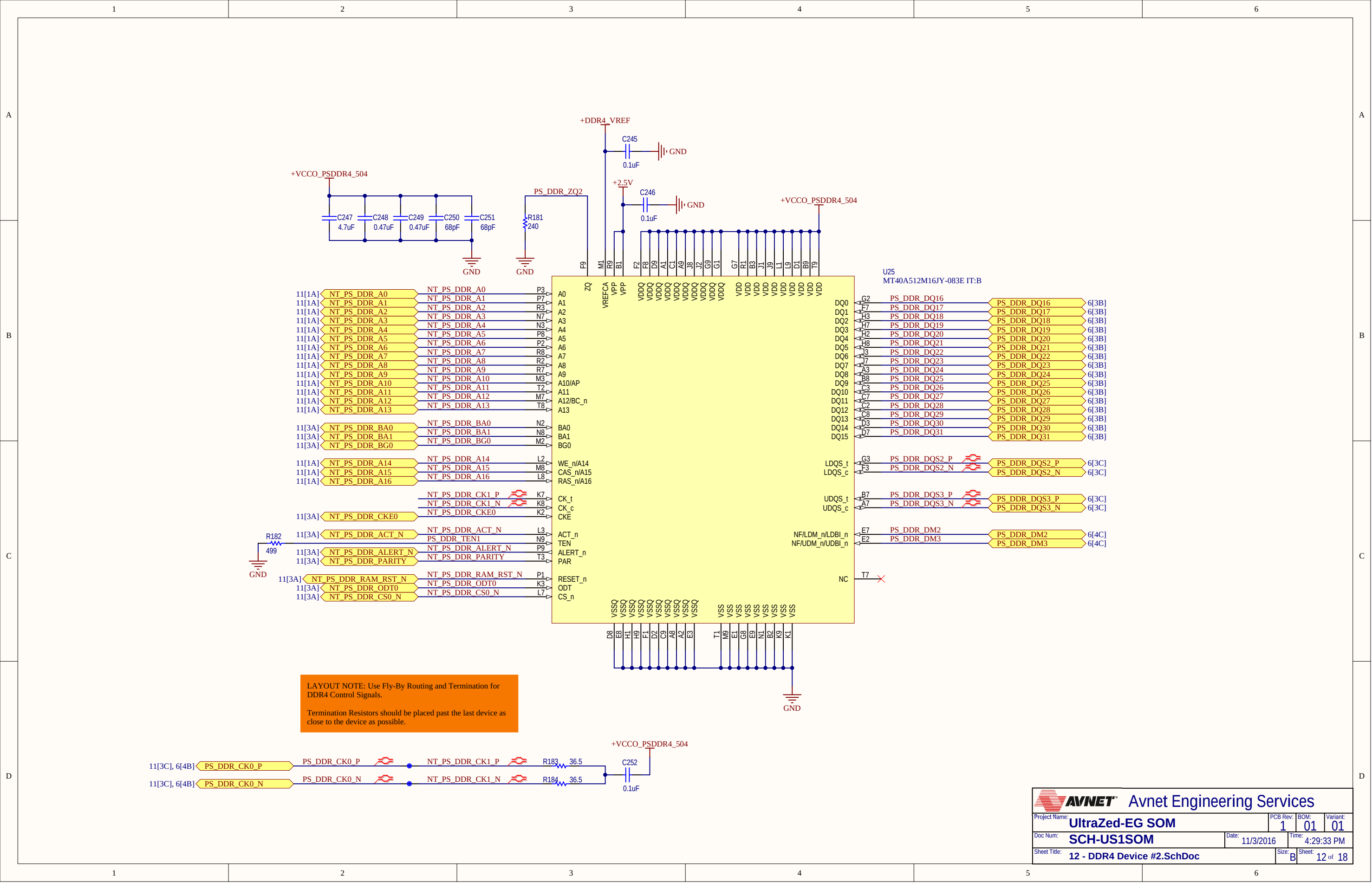
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Doc Num:	SCH-US1SOM	Date:	11/3/2016	Time: 4:29:31 PM
Sheet Title:	06 - Bank 504.SchDoc	Size:	B	Sheet: 6 of 18

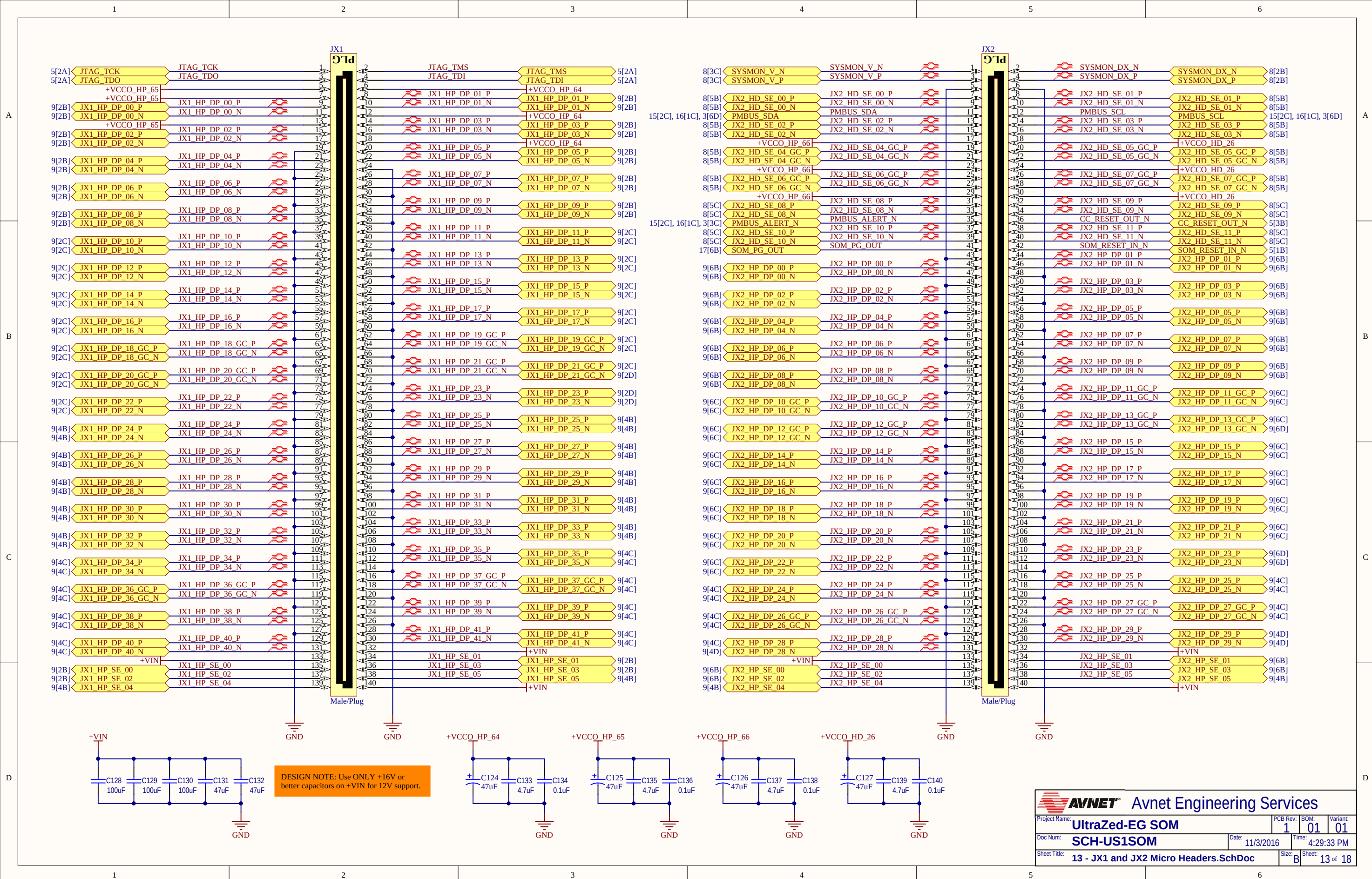


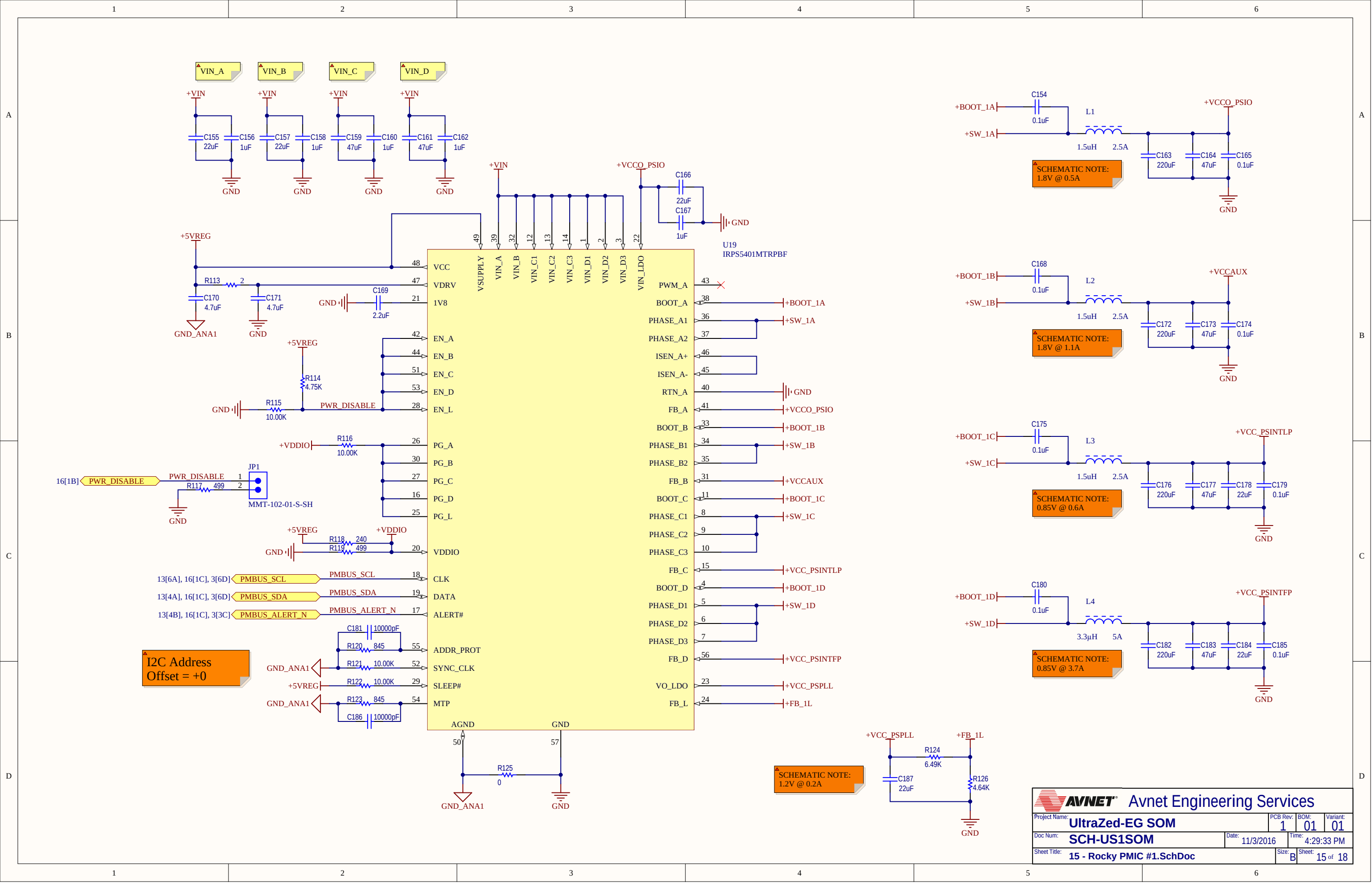


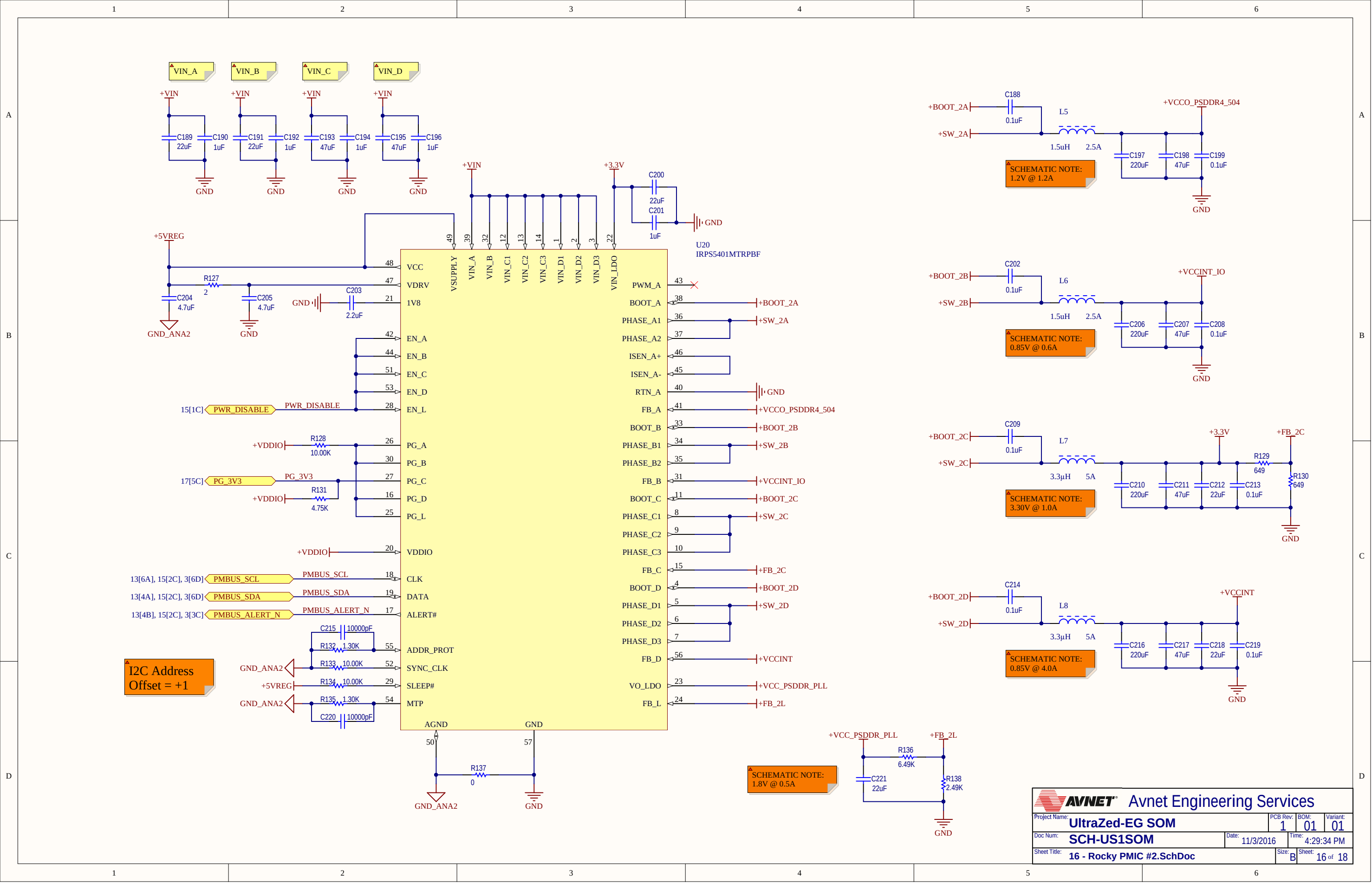


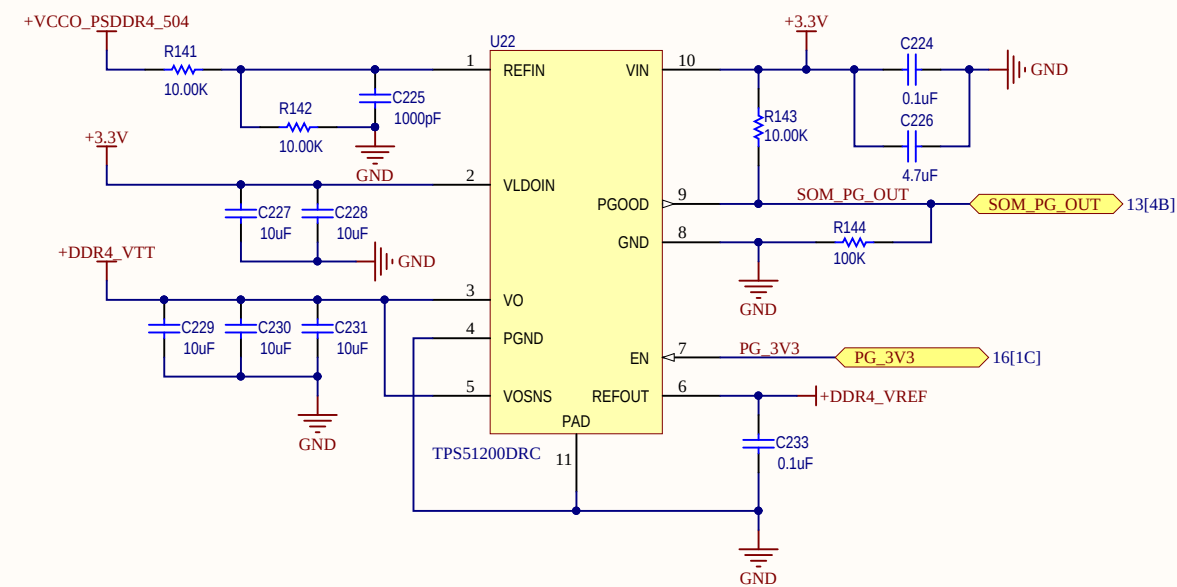
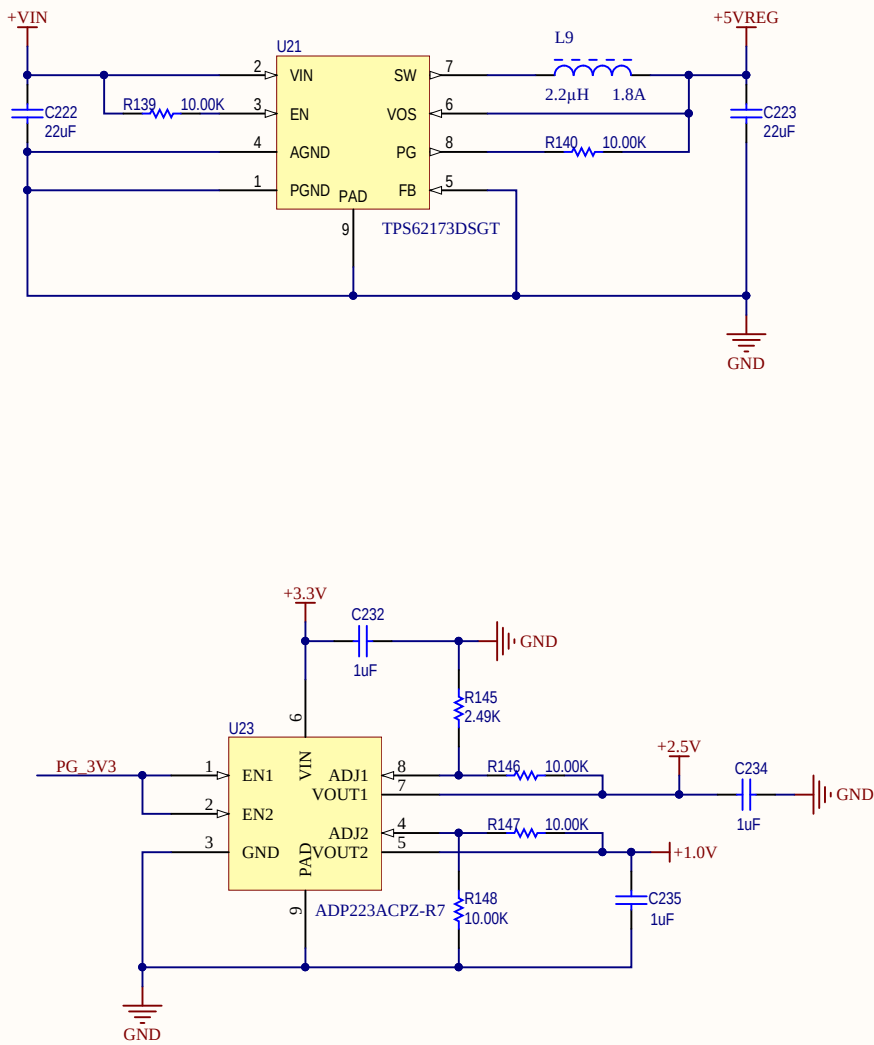












Assembly:

Label1
BD-XXXX-XXXXX-G
Label, Product

Label2
XXXXXXXX
Label, Serial Number

ESD1

ESD Bag

Label_ESD1

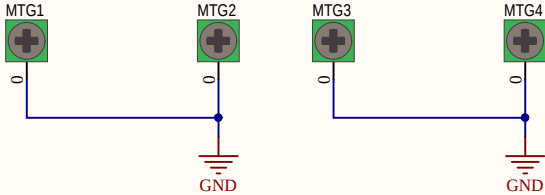
Label, ESD

Mechanicals:

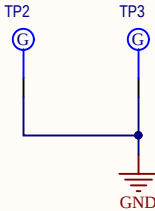
PCB


XXX-XXX-PCB-X
PCB PN (In Copper)

Mounting Holes:



GND Test Points:



Jumper Shunts:

