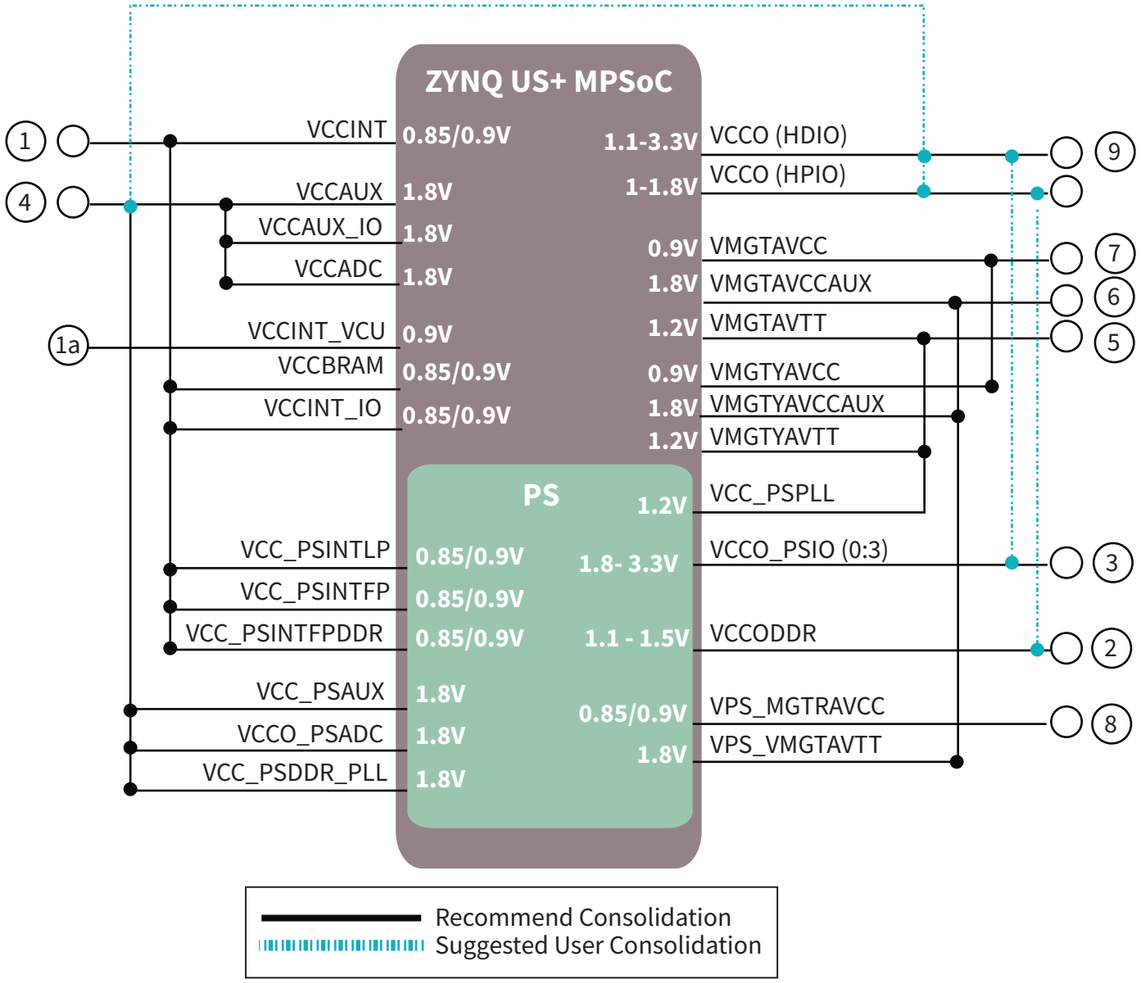
ZYNQ UltraScale+
Zu07
Xilinx ZCU-104 Embedded Vision Reference Design



VIN = 12V

IRPS5401 (x10, x40)
- Ch A → TDA21240 → (1) 0.85V / 15A — VCCINT
- Ch B → (4) 1.8V / 2.0A — VCC1V8
- Ch C → (2) 1.2V / 5.0A — VCC1V2 (DDR)
- Ch D
- LDO → (7) 0.9V / 0.5A — MGTAVCC

IRPS5401 (x11, x41)
- Ch A → TDA21240 — 3.3V /10A — UTIL_3V3 (various)
- Ch B — 1.13V / 1.0A — UTIL_1V13 (ENET/HDMI)
- Ch C — 5.0V / 2.1A — UTIL_5V0 (various)
- Ch D — (1.8V / 3.0A) — FMC_VADJ
- LDO → (8) 0.85V / 0.5A — MGTRAVCC

IR3897 (VCC1V2 → VIN)
- Vout → (1a) 0.9V / 3.0A — VCCINT_VCU

IR3897 (VCC1V2 → VP)
- Vout — 0.6V, +/-3A — PL_DDR4_VTT — DDR

IR3897 (VCC1V2 → VP)
- Vout — 0.6V, +/-3A — PL_DDR4_VTT — DDR

IFX54441 (2.5V → VIN)
- Vout → (6) 1.8V / 0.1A — MGT1V8

IR3883
- Vout → (3) 3.3V, 0.5A — VCC3V3

IR3883
- Vout — 2.5V, 1.0A — UTIL_2V5

IR3823
- Vout → (5) 1.2V, 0.5A — MGT1V2