

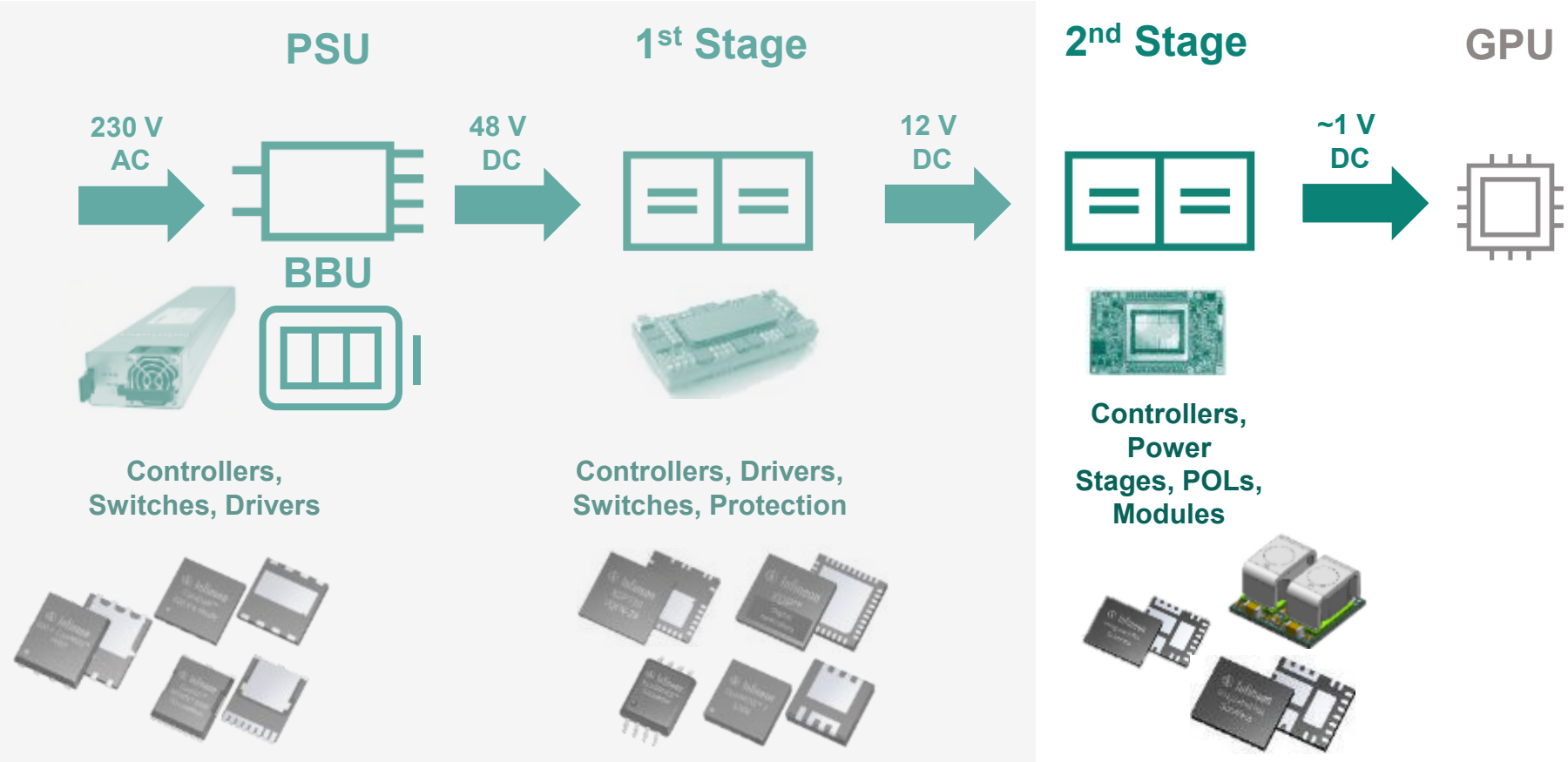
Product family overview
XDP™ Digital multiphase PWM buck controllers

June 2026

Public

Infineon is powering AI from grid to core

Server power flow today

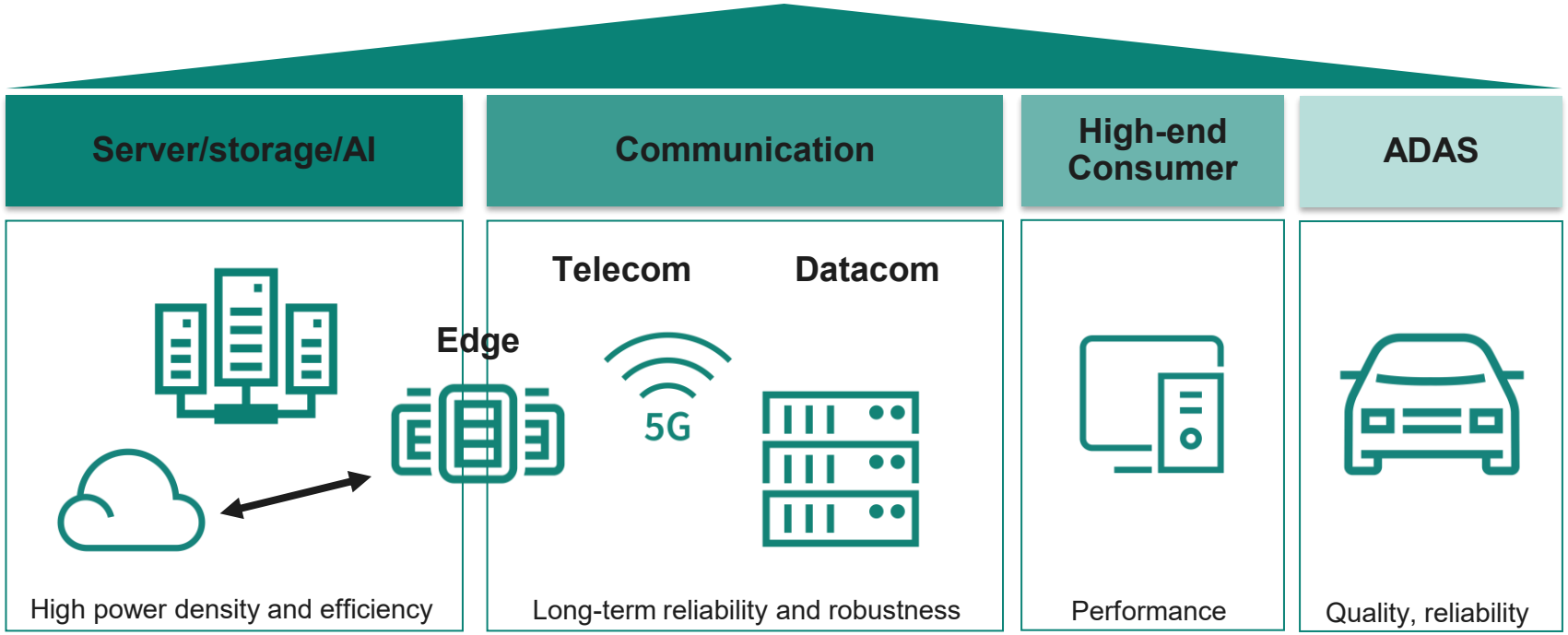


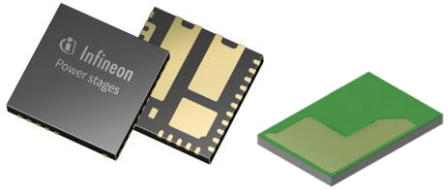
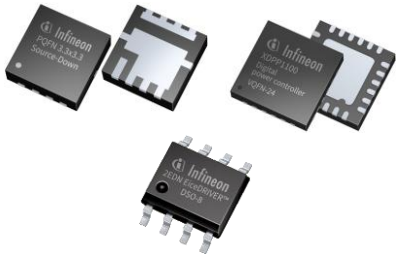


Infineon has full system offering across all AI server functional blocks

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Infineon computing solutions

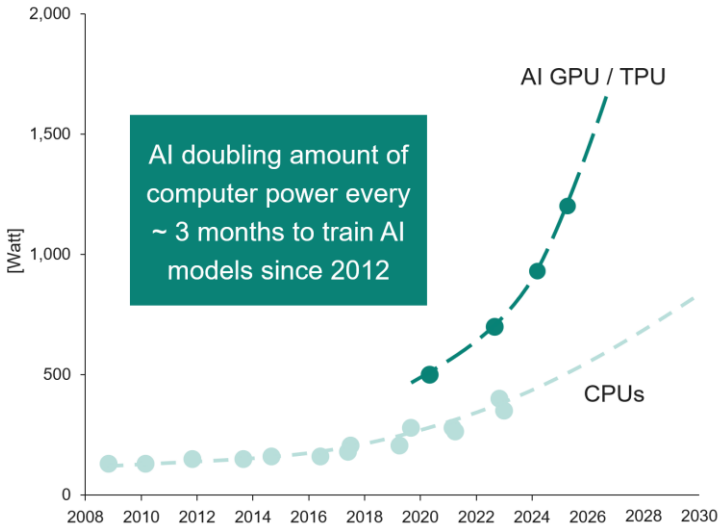


XDP™ Controllers	OptiMOS™ Smart power stages	High density power modules	OptiMOS™ Integrated POL	Protection ICs	48/54 V systems
					
Up to 16 phases, up to 4 loops	20 – 90 A		2 – 40 A		4:1, …. 10:1

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Computing is all about power density

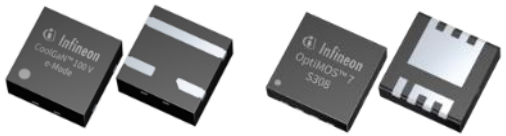
> 1 MW per single compute rack



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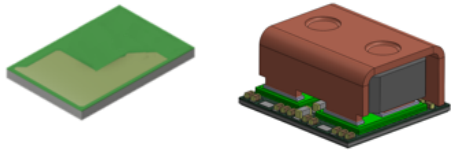


Power semiconductor



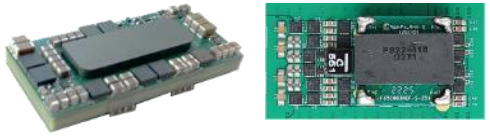
- Silicon, SiC and GaN technologies
- Analog/mixed signal

Advanced packaging



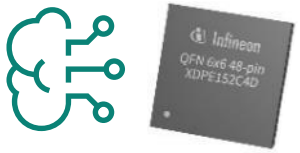
- Chip embedding
- Modules
- Integrated magnetics

System architecture



- Vertical power delivery
- Scalable architecture
- Magnetics

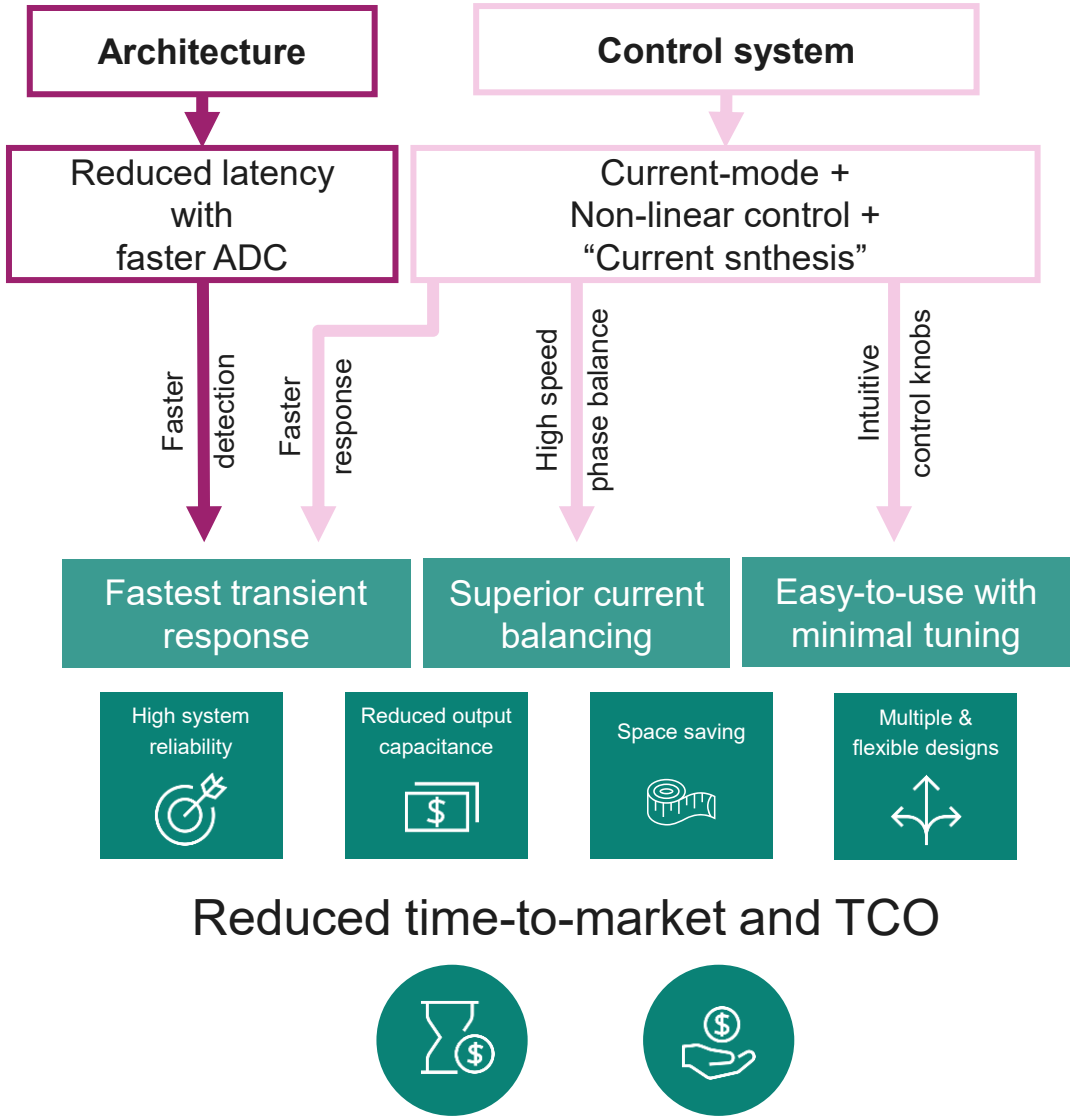
Smart control and software

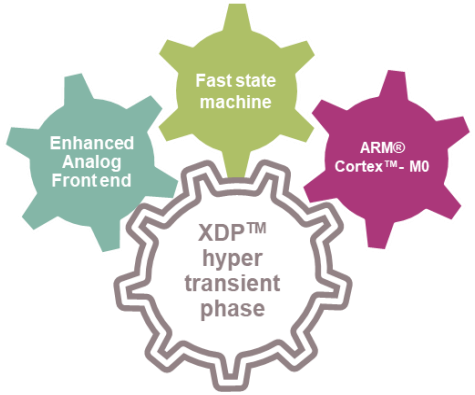


- Hyper-transient digital controller
- Hot-swap/e-fuse controller
- Smart digital controller
- Point-of-load

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Infineon's multiphase controllers
Geared for flexibility to enable performance differentiation





Firmware upgradable features

- Multi-function pin definition
- PMBus commands
- Fault protection and response
- Telemetry filters
- Limited SVID changes
- Register protection
- Phase current and output current calibration
- Thermal balancing
- Pin-strapped programming (multiple config. files)

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Portfolio overview of products in production

Part number	Package	Maximum phases	Interfaces	Voltage outputs	Configuration
PXM1310CDM-G003	QFN 5 x 5 40-pin	4	I2C, VR13	2	3+1
PXE1110CDM-G003	QFN 5 x 5 40-pin	2	PMBus	2	1+1
PXE1610CDN-G003	QFN 6 x 6 48-pin	7	PMBus	2	6+1
XDPE12254C-0000	QFN 5 x 5 40-pin	5	PMBus, VR13, VR13HC, IMVP8, IMVP9	2	4+1
XDPE12284C-0000	QFN 5 x 5 40-pin	8	PMBus, VR13, VR13HC, IMVP8, IMVP9	2	8+0 to 4+4
XDPE132G5H-G000	QFN 7 x 7 56-pin	16	AVSBus, PMBus, SVI2, PWMVID	2	16+0 to 8+8
XDPE15254D-0000	QFN 5 x 5 40-pin	5	PMBus, VR14	2	5+0 to 3+2
XDPE15284D-0000	QFN 5 x 5 40-pin	8	PMBus, VR14	2	8+0 to 4+4
XDPE152C4D-0000	QFN 5 x 5 40-pin	12	PMBus, VR14	2	12+0 to 6+6
XDPE19283B-0000	QFN 5x5 40-pin	8	PMBus, SVI3	2	8+0 to 4+4
XDPE192C3B-0000	QFN 6x6 48-pin	12	PMBus, SVI3	2	12+0 to 6+6
XDPE19284C-0000	QFN 5x5 40-pin	8	PMBus, VR14	2	8+0 to 4+4
XDPE192C4C-0000	QFN 5x5 40-pin	12	PMBus, VR14	2	12+0 to 6+6
XDPE19283D-0000	QFN 5x5 40-pin	8	AMD SVI3, PMbus	2	8+0 to 4+4
XDPE192C3D-0000	QFN 6x6 48-pin	12	AMD SVI3, PMbus	2	12+0 to 6+6
XDPE19283E-0000	QFN 5x5 40-pin	8	AMD SVI3, PMbus	2	8+0 to 4+4
XDPE192C3E-0000	QFN 6x6 48-pin	12	AMD SVI3, PMbus	2	12+0 to 6+6
XDPE1A2G7B-0000	QFN 7x7 56-pin	16	PMBus, nVIDIA PWM-VID	2	16+0 to 8+8
XDPE1A2G5B-0000	QFN 7x7 56-pin	16	PMBus, AVSBus	2	16+0 to 8+8
XDPE1B284B-0000	QFN 5x5 40-pin	8	VR13, VR13HC, VR14 SVID, AVS, PMBus	2	8+0 to 0+8
XDP1B2C4B-0000	QFN 6x6 48-pin	12	VR13, VR13HC, VR14 SVID, AVS, PMBus	2	12+0 to 0+12

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Portfolio overview of products in production

Part number	Package	Maximum phases	Interfaces	Voltage outputs	Configuration
XDPE1A2G7C-0000	QFN 7x7 56-pin	16	PMBus rev 1.3.1, nVIDIA PWM-VID	2	16+0 to 8+8
XDPE1A2G5C-0000	QFN 7x7 56-pin	16	PMBus rev 1.4, AVSBus	2	16+0 to 8+8
XDPE1C284A-0000	QFN 5x5 40-pin	8	VR14, VR14.Cloud, AVS, PMBus	2	8+0 to 0+8
XDPE1C2C4A-0000	QFN 6x6 48-pin	12	VR14, VR14.Cloud, AVS, PMBus	2	12+0 to 0+12
XDPE1D2G3C-0000	QFN 7x7 56-pin	16	AMD SVI3.0, PMBus rev 1.3.1	2	16+0 to 0+16
XDPE1E3G6A-0000	QFN 7x7 60-pin	16	VR14, VR14.Cloud, AMD SVI3, AVS, PMBus	3	16+0 to 0+16
XDPE1E496A-0000	QFN 6x6 52-pin	9	VR14, VR14.Cloud, AMD SVI3, AVS, PMBus	4	9+0 to 0+9
XDPE1A2G7C-0000	QFN 7x7 56-pin	16	PMBus rev 1.3.1, nVIDIA PWM-VID	2	16+0 to 8+8
XDPE1A2G7C-0020	QFN 7x7 56-pin	16	PMBus, nVidia PWM-VID	2	16+0 to 0+16

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XDPE™ Product naming nomenclature



2-digit family
- 12, 15, 19, 1A, 1B, 1C, 1D,1E

Number of outputs
- 1,2,3,4

Number of phases
- 5, 8, 9, C(=12), G (=16)

Interface/Protocol

SVID, SVI2/3, AVSBus, PMBus, PWM-VID

Configuration File Version

Firmware Derivative

Customer Identifier

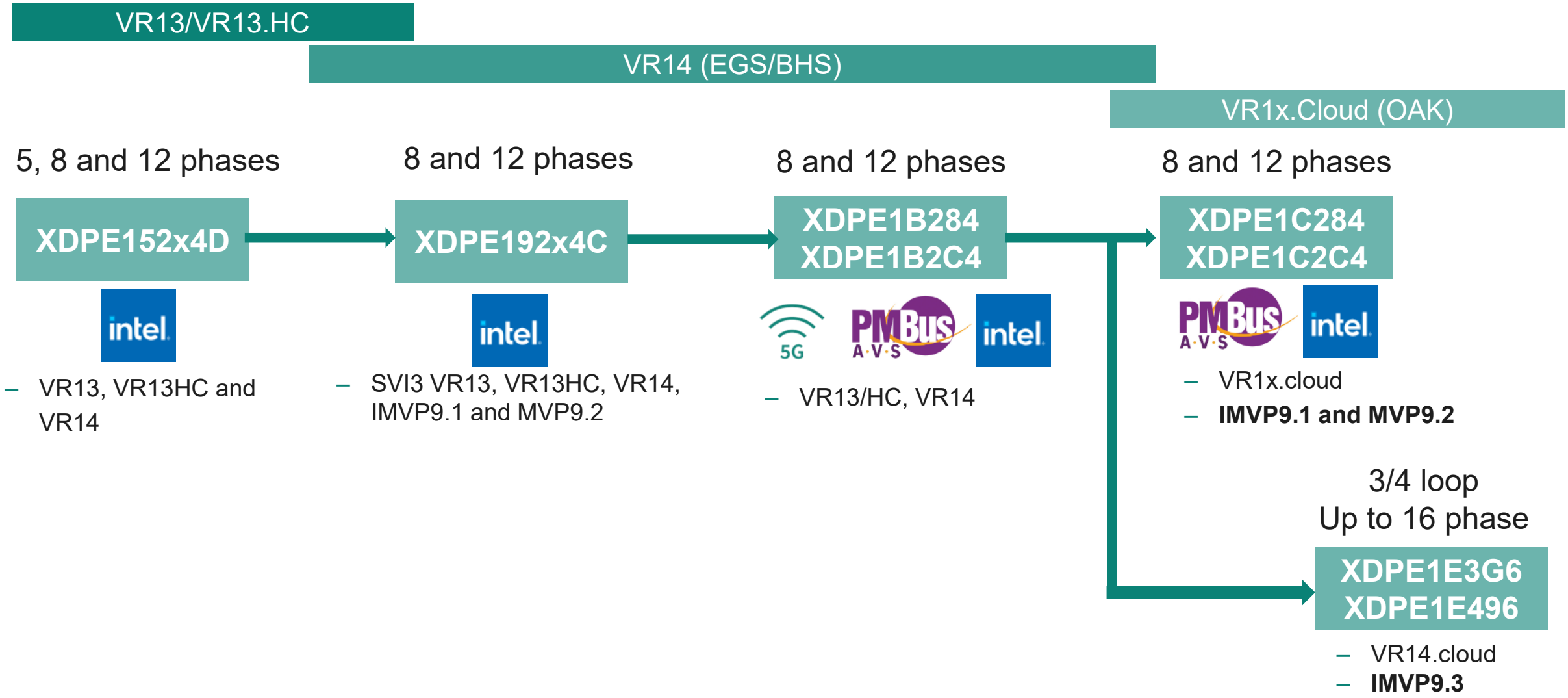
xPU vendor Identifier

Silicon verson
- A, B, C, D

Example: XDPE192C3D-0000

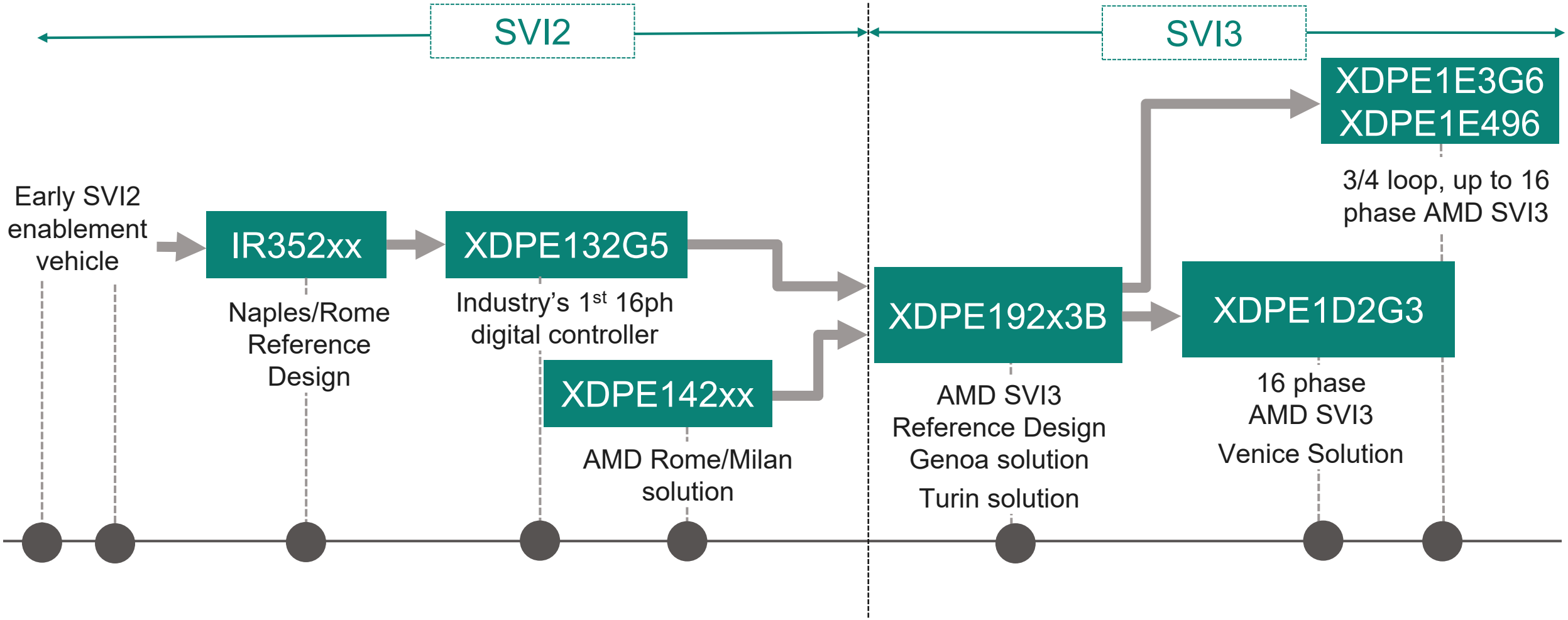
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Infineon solutions for Intel platforms



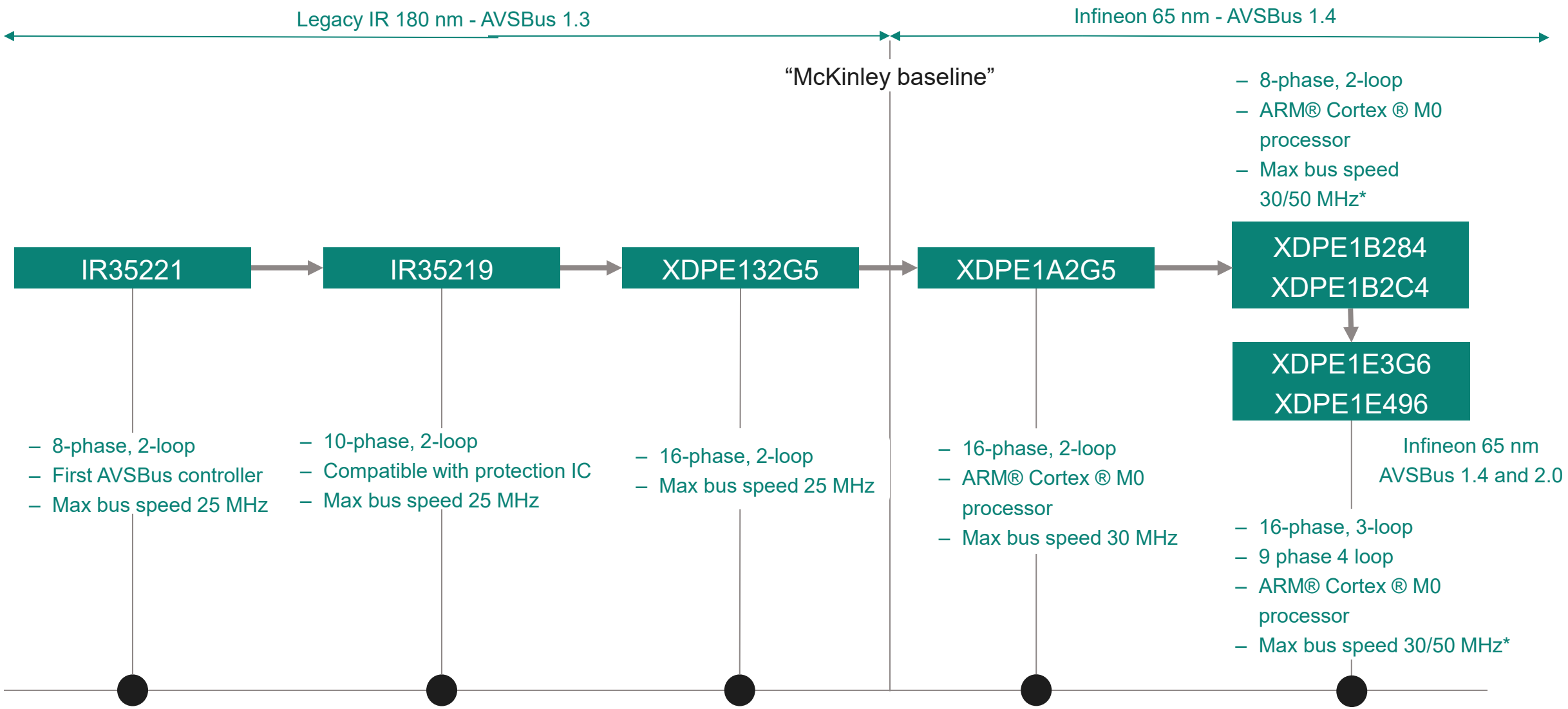
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Infineon solutions for AMD platforms



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Infineon solutions for AVSBus platforms



* With attention to bus length and data capture on falling edge

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