

# SOT-23 High-Side Gate Driver IC

#### **Features**

- Floating gate driver designed for bootstrap operation
- Fully operational to +200 V
- Excellent dv/dt immunity
- Excellent negative V<sub>S</sub> transient immunity
- Wide V<sub>CC</sub> range
- UVLO on low-side and high-side
- Schmitt-trigger input with internal pull-down
- Output in phase with input
- Excellent latch immunity on all inputs & outputs
- · RoHS compliant
- 6-pin SOT-23 package

#### **Applications**

- High-side gate driver control
- Pulse transformer replacement
- General purpose switched mode power electronics

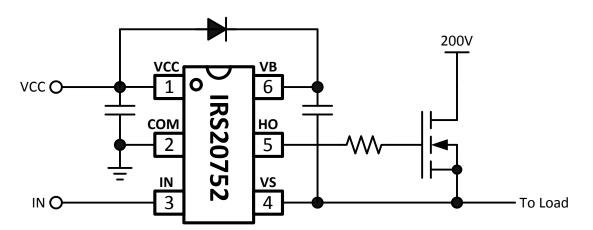
#### **Description**

The IRS20752 is a high-side, single-channel gate driver IC with 200V blocking and level-shifting capability. This allows for the gate driver to be connected directly to the gate of a high-side power MOSFET, while being controlled by the low-side, ground potential circuitry. The IRS20752 includes a wide  $V_{\rm CC}$  supply range, UVLO protection, and excellent immunity to harsh dv/dt or  $-V_{\rm S}$  switching environments. IR's HVIC technology allows for these functions and features to be realized in a 6-pin SOT-23 package.

### **Package Options**



### **Typical Connection Diagram**



### **Ordering Information**

		Standar	d Pack		
Base Part Number	Package Type	Form	Quantity	Orderable Part Number	
IRS20752LPBF	SOT-23-6L	Tape and Reel	3000	IRS20752LTRPBF	

# IRS20752LPBF



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#### **Absolute Maximum Ratings**

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. All voltage parameters are absolute voltages referenced to COM, all currents are defined positive into any pin. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions.

Symbol	Definition		Min	Max	Units
$V_{B}$	High side floating absolute voltage		-0.3	220	
Vs	High side floating supply offset voltage	je	V <sub>B</sub> - 20	$V_B + 0.3$	
$V_{HO}$	High side floating gate drive output vo	oltage	V <sub>S</sub> - 0.3	$V_B + 0.3$	.,
V <sub>cc</sub>	Low side and logic fixed supply voltage		-0.3	20	- V
V <sub>IN</sub>	Logic input voltage		COM - 0.3	V <sub>CC</sub> + 0.3	
СОМ	Logic ground		V <sub>CC</sub> - 20	$V_{CC} + 0.3$	
dVS/dt	High side floating supply offset voltage slew rate			50	V/ns
R⊝ <sub>JA</sub>	Thermal resistance, junction to ambient 6L-SOT-23			151	°C/W
TJ	Junction temperature		-55	150	
Ts	Storage temperature		-55	150	٥C
T <sub>L</sub>	IC Pin temperature (soldering, 10 seconds)			300	

#### **Recommended Operating Conditions**

For proper operation the device should be used within the recommended conditions.

Symbol	Definition	Min	Max	Units
V <sub>B</sub>	High side floating absolute voltage	V <sub>S</sub> + 10	V <sub>S</sub> + 18	
Vs	High side floating supply offset voltage	COM - 8 <sup>†</sup>	200	
$V_{HO}$	High side floating gate drive output voltage	V <sub>S</sub>	$V_{B}$	V
V <sub>cc</sub>	Low side and logic fixed supply voltage	10	18	
V <sub>IN</sub>	Logic input voltage	СОМ	V <sub>CC</sub>	
T <sub>J</sub>	Junction temperature	-40	125	°C

<sup>&</sup>lt;sup>†</sup> Logic operational for  $V_S$  of -8V to +200V. Logic state held for  $V_S$  of -8V to  $-V_{BS}$ .



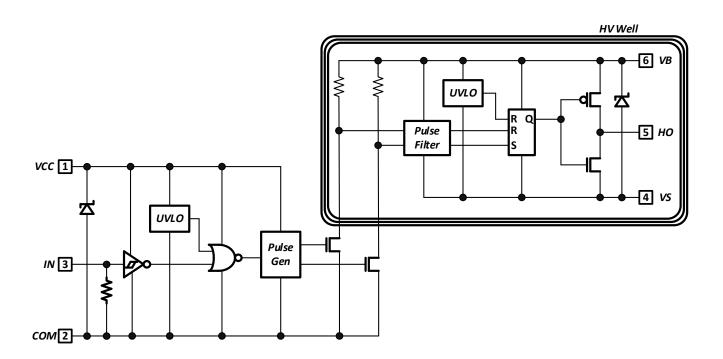
### **Electrical Characteristics**

 $V_{\text{CC}}\!\!=\!\!15\text{V},\,V_{\text{BS}}\!\!=\!\!15\text{V},\,C_{\text{L}}\!\!=\!\!1000\text{pF},$  and  $T_{\text{A}}$  = 25 °C unless otherwise specified.

Symbol	Definition	Min	Тур	Max	Units	Test Conditions	
Low Side Ch	naracteristics			•			
V <sub>CCUV+</sub>	V <sub>CC</sub> supply UVLO positive-going	8.0	9.0	10.0	V		
V <sub>CCUV</sub> -	V <sub>CC</sub> supply UVLO negative-going	7.0	8.0	9.0	V		
I <sub>QCC</sub>	Quiescent V <sub>CC</sub> supply current		100		μA		
V <sub>CC_CLAMP</sub>	V <sub>CC</sub> internal Zener clamp voltage		20.4			$I_{CC} = 5mA$	
$V_{IH}$	Logic "1" input voltage			2.2	V		
V <sub>IL</sub>	Logic "0" input voltage	0.8					
I <sub>IN+</sub>	Logic "1" input bias current		20	40		$V_{IN} = V_{CC}$	
I <sub>IN-</sub>	Logic "0" input bias current			5	μA	V <sub>IN</sub> = COM	
High Side C	haracteristics						
V <sub>BSUV+</sub>	V <sub>BS</sub> supply UVLO positive-going	8.0	9.0	10.0			
$V_{BSUV}$	V <sub>BS</sub> supply UVLO negative-going	7.0	8.0	9.0			
V <sub>BS_CLAMP</sub>	V <sub>BS</sub> internal Zener clamp voltage		20.4		V	$I_{BS} = 5mA$	
V <sub>OH</sub>	High level output voltage (V <sub>B</sub> – HO)		0.8	1.4		I <sub>O</sub> = 2mA	
V <sub>OL</sub>	Low level output voltage (HO – V <sub>S</sub> )		0.3	0.6			
I <sub>LK</sub>	Offset supply leakage current			50		$V_{B} = V_{S} = 200V$	
$I_{QBS}$	Quiescent V <sub>BS</sub> supply current		80		μA	$V_{IN} = V_{CC}$ or COM	
Gate Drive C	Characteristics						
t <sub>ON</sub>	Turn-on propagation delay		140			V <sub>S</sub> = 0V	
t <sub>OFF</sub>	Turn-off propagation delay		215		20	V <sub>S</sub> = 200V	
t <sub>RISE</sub>	Turn-on rise time		85		ns	\/ O\/	
t <sub>FALL</sub>	Turn-off fall time		40			V <sub>S</sub> = 0V	
I <sub>O+</sub>	HO gate drive output source current		160		т Л		
I <sub>O-</sub>	HO gate drive output sink current		240		mA		

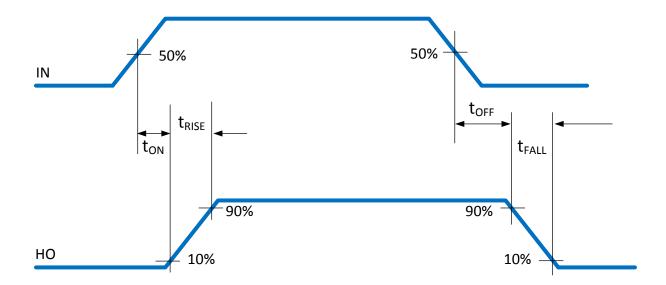


# **Functional Block Diagram**





# **Timing Diagram**

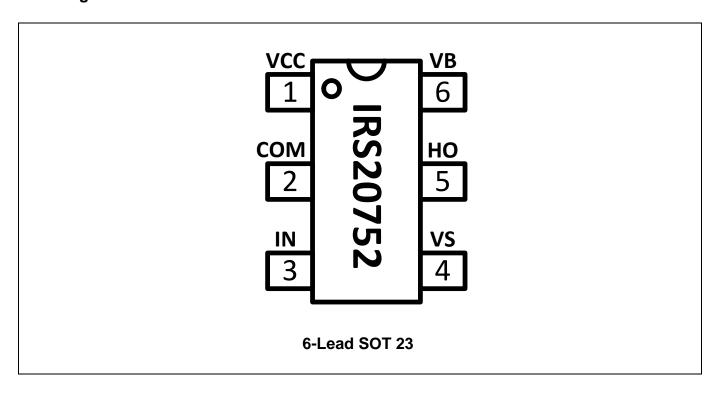




### **Pin Definitions**

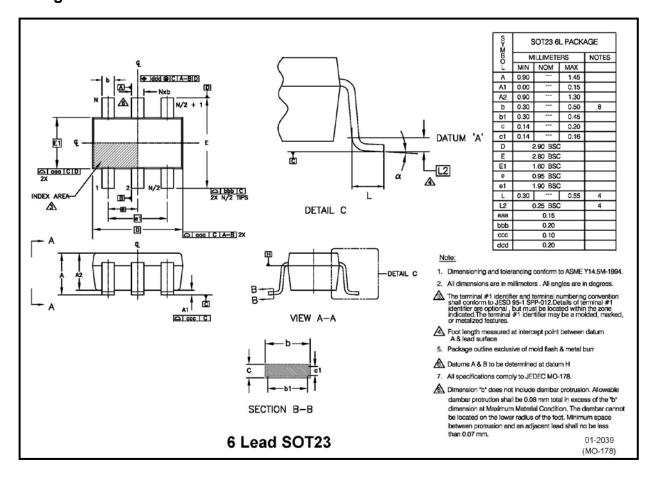
Pin	Symbol	Description
1	vcc	IC supply voltage
2	СОМ	IC power and signal ground
3	IN	Logic input
4	VS	High side floating supply offset voltage
5	НО	High side gate driver output
6	VB	High side floating supply voltage

## **Pin Assignments**



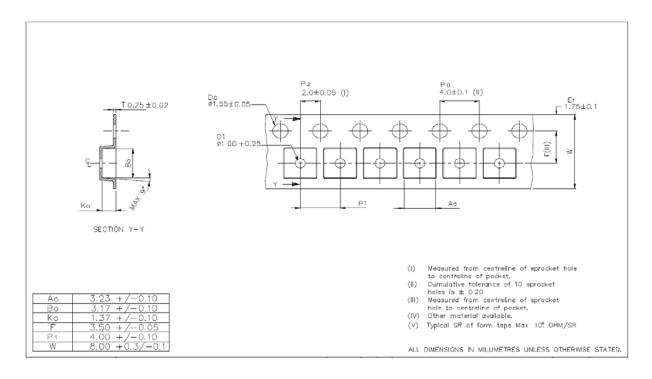


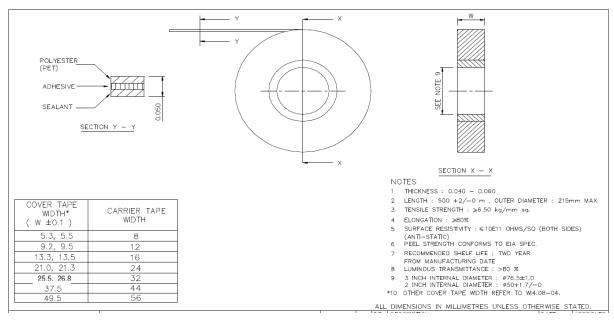
Package Details: 6L-SOT23





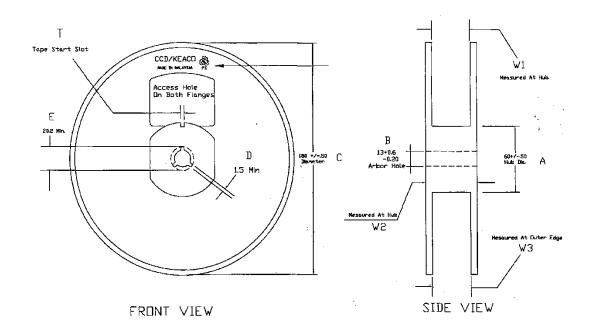
### Tape and Reel Details: 6L-SOT23

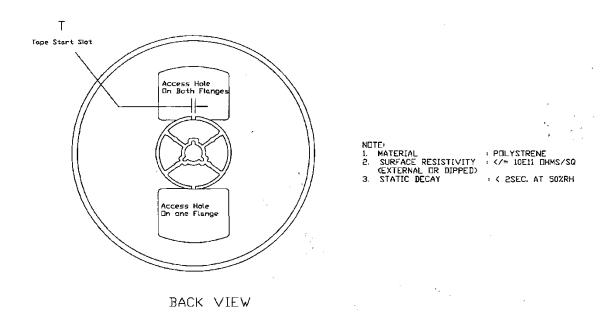






## Tape and Reel Details: 6L-SOT23

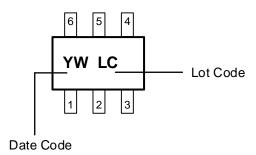




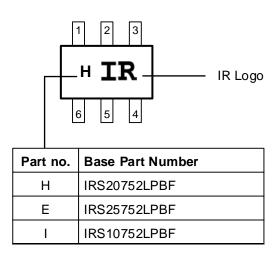


## Part Marking Information: 6 Lead SOT23

## **Top Marking**



### **Bottom Marking**





#### Qualification Information<sup>†</sup>

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		Industrial <sup>††</sup> (per JEDEC JESD 47E)				
Qualification Level			Comments: This family of ICs has passed JEDEC's			
addinication Ecver			Industrial qualification. IR's Consumer qualification level is			
			granted by extension of the higher Industrial level.			
Moisture Sensitivity Level		SOT-23	MSL1 <sup>†††</sup>			
		301-23	(per IPC/JEDEC J-STD-020C)			
	Machine Model	Class B				
ESD	Machine Model	(per JEDEC s	(per JEDEC standard EIA/JESD22-A115-A)			
ESD	Human Body Model	Class 1B				
		(per EIA/JEDEC standard JESD22-A114-B)				
IC Latch-Up Test		Class I, Level A				
		(per JESD78A)				
RoHS Compliant			Yes			

- † Qualification standards can be found at International Rectifier's web site <a href="http://www.infineon.com/">http://www.infineon.com/</a>
- †† Higher qualification ratings may be available should the user have such requirements. Please contact your International Rectifier sales representative for further information.
- ††† Higher MSL ratings may be available for the specific package types listed here. Please contact your International Rectifier sales representative for further information.

**Revision History** 

Date	Comment		
01/04/2017	<ul> <li>Updated "Infineon" logo –all pages.</li> <li>Added disclaimer on last page.</li> <li>Updated part marking information on page 11.</li> </ul>		



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