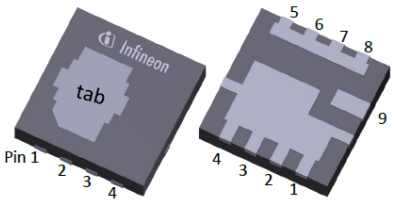
英飞凌MOSFET

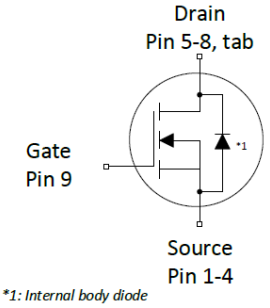
PG-WHTFN-9 (3x3)

OptiMOS™ 7 功率晶体管，25 V

特性



- N沟道，逻辑电平
- 极低的导通电阻 $R_{DS(on)}$
- 针对硬开关拓扑进行了优化
- 针对最佳 FOM_{oss} 进行了优化
- dv/dt耐受性方面具有出色的米勒比
- 卓越的热阻
- 100% 雪崩测试
- 无铅镀层；符合 RoHS 标准
- 符合 IEC61249-2-21 标准的无卤素

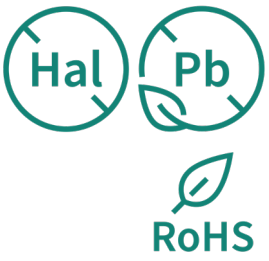


产品验证

符合 JEDEC JESD47, JESD22 和 J-STD-020 相关测试的工业应用要求。

表 1　　主要性能参数

Parameter	Value	Unit
V_{DS}	25	V
$R_{DS(on),max}$	0.80	mΩ
I_D	282	A
Q_{oss}	24	nC
Q_G (0V...10V)	37	nC



Part number	Package	Marking	Related links
IQEH80NE2LM7UCGSC	PG-WHTFN-9	F	-

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1 最大额定值

除非另有规定，T_A =25 °C

表 2 最大额定值

Parameter	Symbol	Values			Unit	Note / Test condition
		Min.	Typ.	Max.		
Continuous drain current [1]	I_D	-	-	282	A	V_{GS}=10 V, T_C=25 °C
				199		V_{GS}=10 V, T_C=100 °C
				159		V_{GS}=4.5 V, T_C=100 °C
				43		V_{GS}=10 V,T_A=25 °C,R_{thJA}=60 °C/W [2]
Pulsed drain current [3]	$I_{D,pulse}$	-	-	1128	A	T_C=25 °C
Avalanche energy, single pulse [4]	E_{AS}	-	-	140	mJ	I_D=20 A, R_{GS}=25 Ω
Gate source voltage	V_{GS}	-16	-	16	V	-
Power dissipation	P_{tot}	-	-	107	W	T_C=25 °C
				2.5		T_A=25 °C, R_{thJA}=60 °C/W [2]
Operating and storage temperature	T_j, T_{stg}	-55	-	175	°C	-

[1] 额定值指产品仅具有数据手册指定的绝对最大值，保持外壳温度符合规定要求。其他外壳温度请参见图 2。需要根据实际环境条件降低额定值。

[2] 器件置于40 mm × 40 mm × 1.5 mm 环氧树脂 PCB FR4 上，配有6 cm^2（单层，70 µm厚）铜层面积用于漏极连接。PCB在静止空气中垂直放置。

[3] 详细信息请参见图 3

[4] 详细信息请参见图 13

2 热特性

表 3 热特性

Parameter	Symbol	Values			Unit	Note / Test condition
		Min.	Typ.	Max.		
Thermal resistance, junction - case, bottom	R_{thJC}	-	-	1.4	°C/W	-
Thermal resistance, junction - case, top	R_{thJC}		2.1	-		
Thermal resistance, junction - ambient, 6 cm^2 cooling area [5]	R_{thJA}		-	60		

[5] 器件置于40 mm × 40 mm × 1.5 mm 环氧树脂 PCB FR4基板上，配有6 cm^2（单层，70微米厚）铜层，用于漏极连接。PCB在静止空气中垂直放置。

3 电气特性

除非另有规定，T_j = 25°C

表 4 静态特性

Parameter	Symbol	Values			Unit	Note / Test condition
		Min.	Typ.	Max.		
Drain-source breakdown voltage	$V_{(BR)DSS}$	25	-	-	V	V_{GS}=0 V, I_D=1 mA
Gate threshold voltage	$V_{GS(th)}$	1.4	1.7	2.0	V	V_{DS}=V_{GS}, I_D=279 µA
Zero gate voltage drain current	I_{DSS}	-	0.1	1	µA	V_{DS}=22 V, V_{GS}=0 V, T_j=25 °C
			10	100		V_{DS}=22 V, V_{GS}=0 V, T_j=125 °C
Gate-source leakage current	I_{GSS}	-	10	100	nA	V_{GS}=16 V, V_{DS}=0 V
Drain-source on-state resistance	$R_{DS(on)}$	-	0.71	0.8	mΩ	V_{GS}=10 V, I_D=20 A
			1.0	1.26		V_{GS}=4.5 V, I_D=20 A
Gate resistance	R_G	-	0.85	-	Ω	-
Transconductance	g_{fs}	50	100	-	S	$\|V_{DS}\|\geq 2\|I_D\|R_{DS(on)max}$, I_D=20 A

表 5 动态特性

Parameter	Symbol	Values			Unit	Note / Test condition
		Min.	Typ.	Max.		
Input capacitance	C_{iss}	-	3000	3900	pF	V_{GS}=0 V, V_{DS}=12 V, f=1 MHz
Output capacitance	C_{oss}		1100	-		
Reverse transfer capacitance	C_{rss}		52	-		
Turn-on delay time	$t_{d(on)}$	-	5.4	-	ns	V_{DD}=12 V, V_{GS}=10 V, I_D=20 A, $R_{G,ext}$=3 Ω
Rise time	t_r		1.4			
Turn-off delay time	$t_{d(off)}$		20.1			
Fall time	t_f		2.7			

表 6　　栅极电荷特性[6)]

Parameter	Symbol	Values			Unit	Note / Test condition
		Min.	Typ.	Max.		
Gate to source charge	Q_{gs}	-	8.8	-	nC	V_{DD}=12 V, I_D=20 A, V_{GS}=0 to 4.5 V
Gate charge at threshold	$Q_{g(th)}$		5.2		nC	
Gate to drain charge	Q_{gd}		2.7		nC	
Switching charge	Q_{sw}		6.4		nC	
Gate charge total	Q_g		17.2		nC	
Gate plateau voltage	$V_{plateau}$		2.9		V	
Gate charge total	Q_g	-	37	-	nC	V_{DD}=12 V, I_D=20 A, V_{GS}=0 to 10 V
Output charge	Q_{oss}	-	24	-	nC	V_{DS}=12 V, V_{GS}=0 V

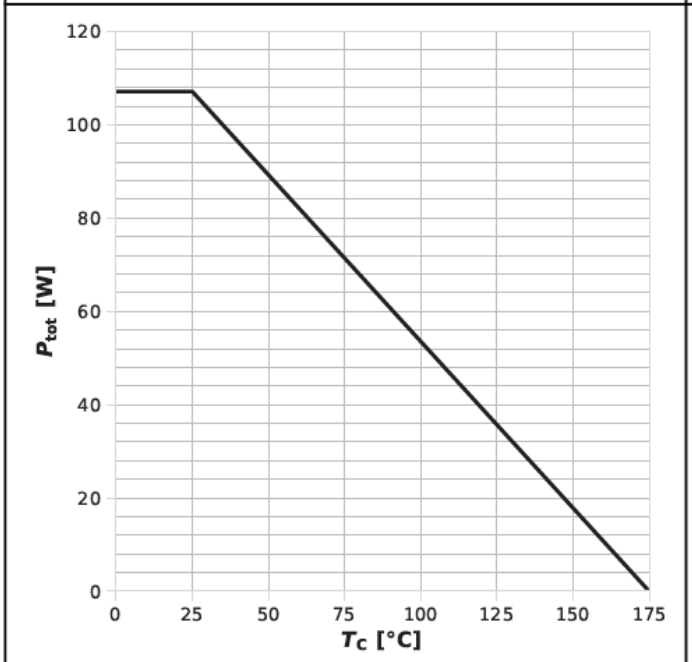
6)　参数定义请参数见“栅极电荷波形”。

表 7　　反向二极管

Parameter	Symbol	Values			Unit	Note / Test condition
		Min.	Typ.	Max.		
Diode continuous forward current	I_S	-	-	98	A	T_C=25 °C
Diode pulse current	$I_{S,pulse}$			1128		
Diode forward voltage	V_{SD}	-	0.77	1.0	V	V_{GS}=0 V, I_F=20 A, T_j=25 °C
Reverse recovery time	t_{rr}	-	30	-	ns	V_R=12 V, I_F=20 A, di_F/dt=100 A/µs
Reverse recovery charge	Q_{rr}		26		nC	
Reverse recovery time	t_{rr}	-	21	-	ns	V_R=12 V, I_F=20 A, di_F/dt=500 A/µs
Reverse recovery charge	Q_{rr}		75		nC	

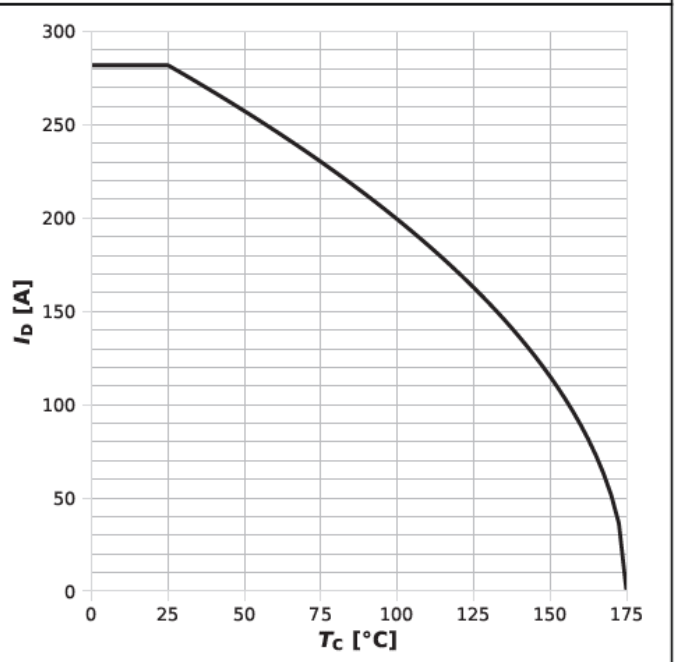
4 电气特性图

Diagram 1: Power dissipation



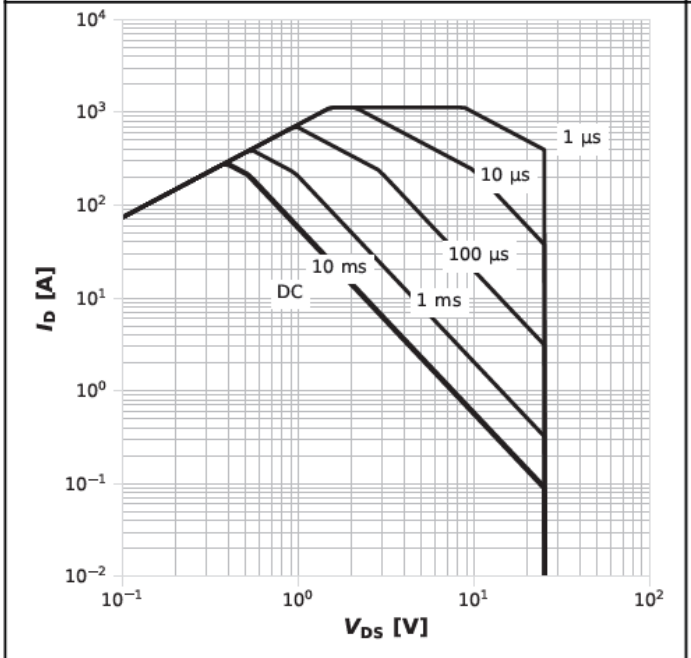
$P_{tot}=f(T_C)$

Diagram 2: Drain current



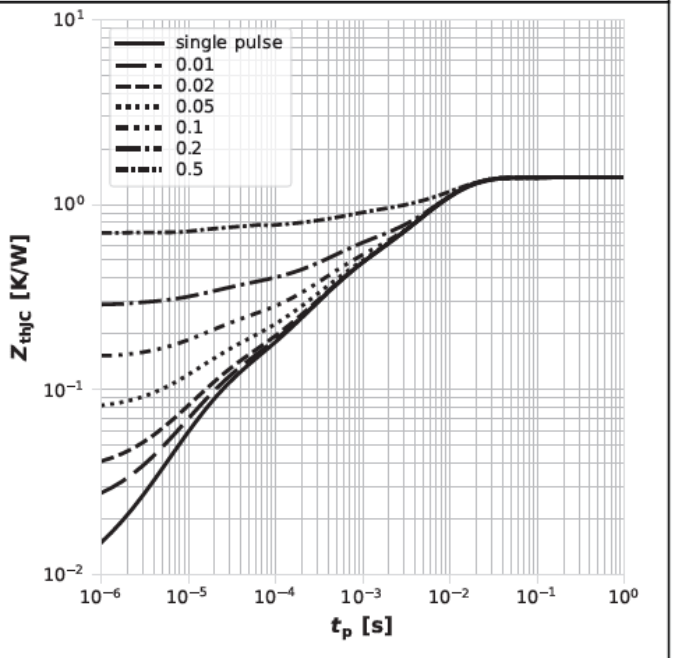
$I_D=f(T_C)$; $V_{GS}\geq 10$ V

Diagram 3: Safe operating area



$I_D=f(V_{DS})$; T_C=25 °C; D=0; parameter: t_p

Diagram 4: Max. transient thermal impedance



$Z_{thJC}=f(t_p)$; parameter: $D=t_p/T$

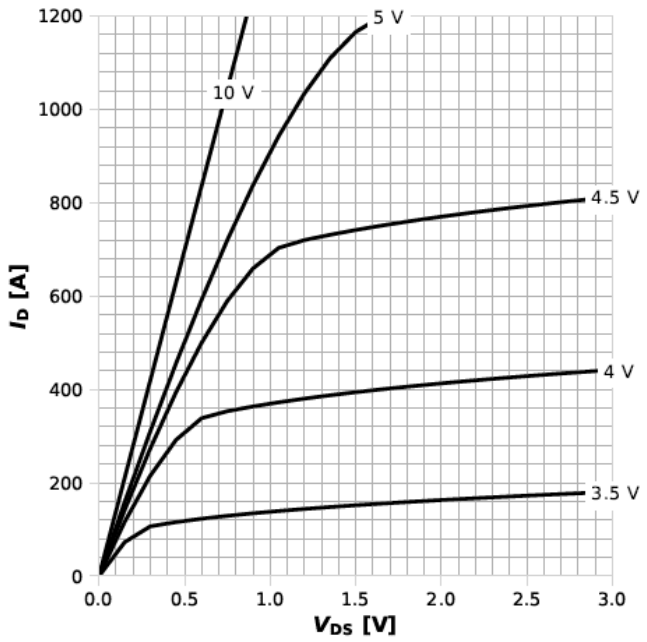
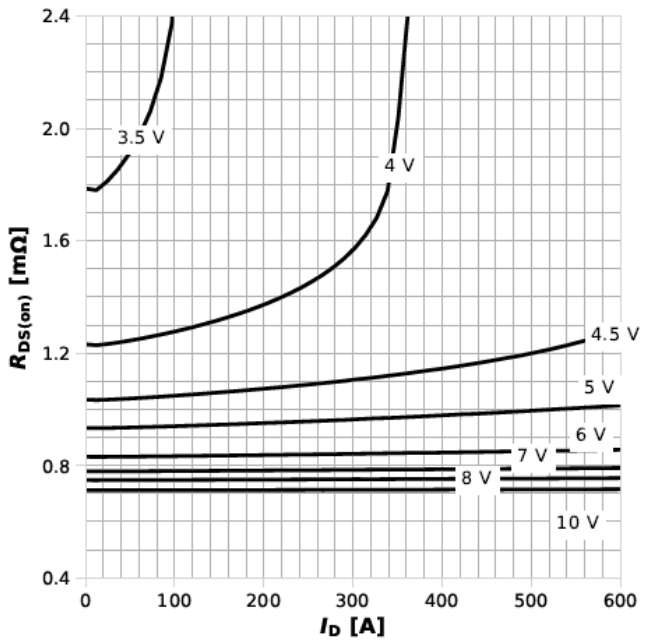
Diagram 5: Typ. output characteristics	Diagram 6: Typ. drain-source on resistance
	
I_D=f(V_{DS}), T_j=25 °C; parameter: V_{GS}	$R_{DS(on)}$=f(I_D), T_j=25 °C; parameter: V_{GS}

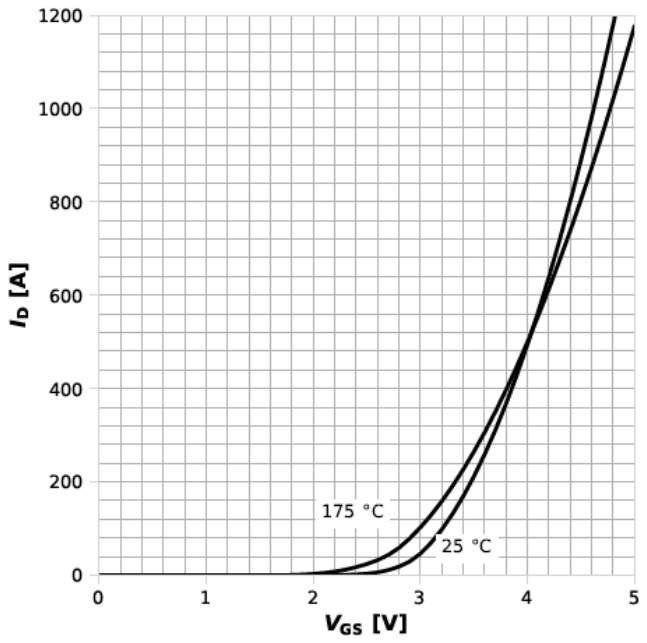
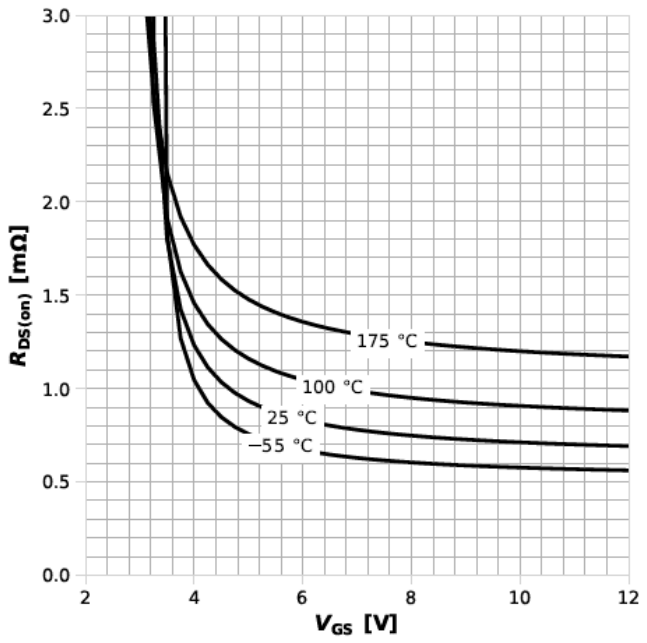
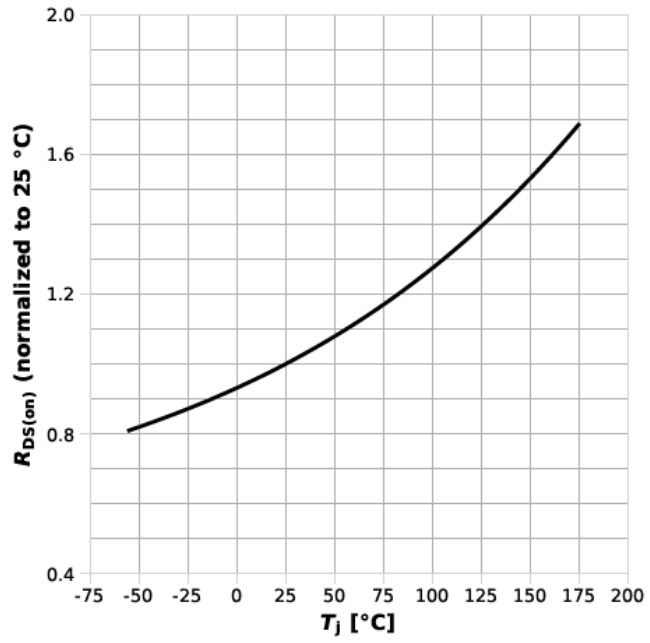
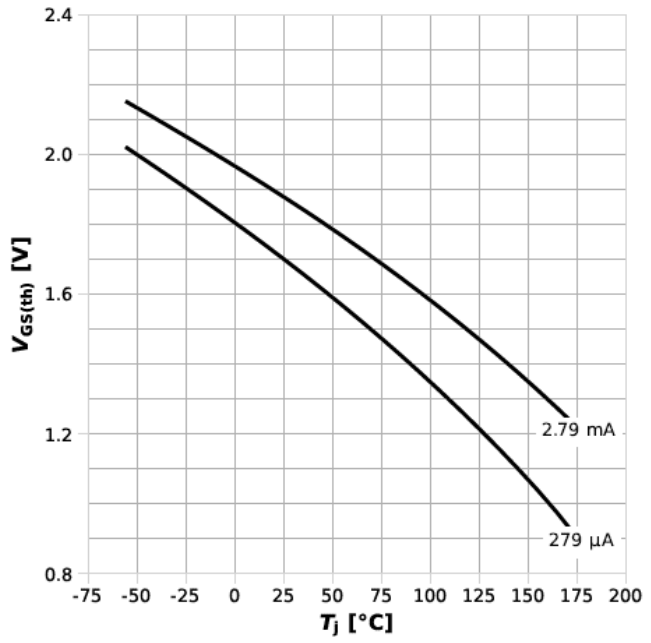
Diagram 7: Typ. transfer characteristics	Diagram 8: Typ. drain-source on resistance
	
I_D=f(V_{GS}), |V_{DS}|>2|I_D|$R_{DS(on)max}$; parameter: T_j	$R_{DS(on)}$=f(V_{GS}), I_D=20 A; parameter: T_j

Diagram 9: Normalized drain-source on resistance



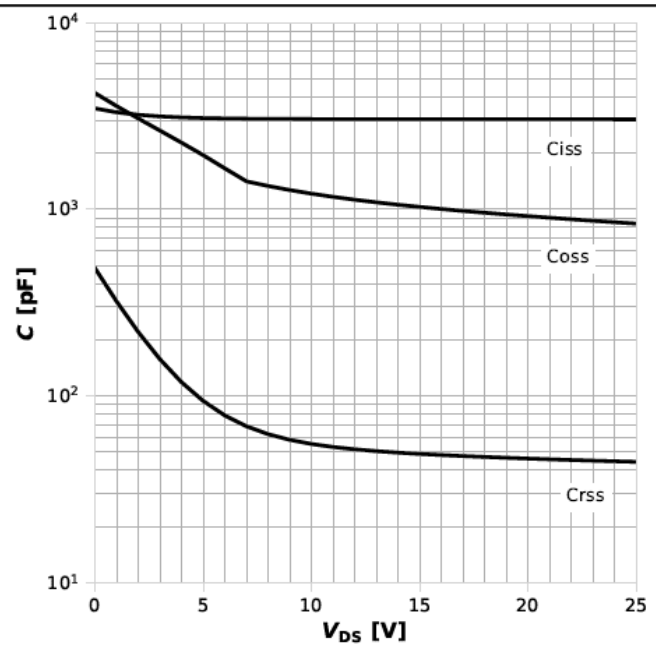
$R_{DS(on)}$=f(T_j), I_D=20 A, V_{GS}=10 V

Diagram 10: Typ. gate threshold voltage



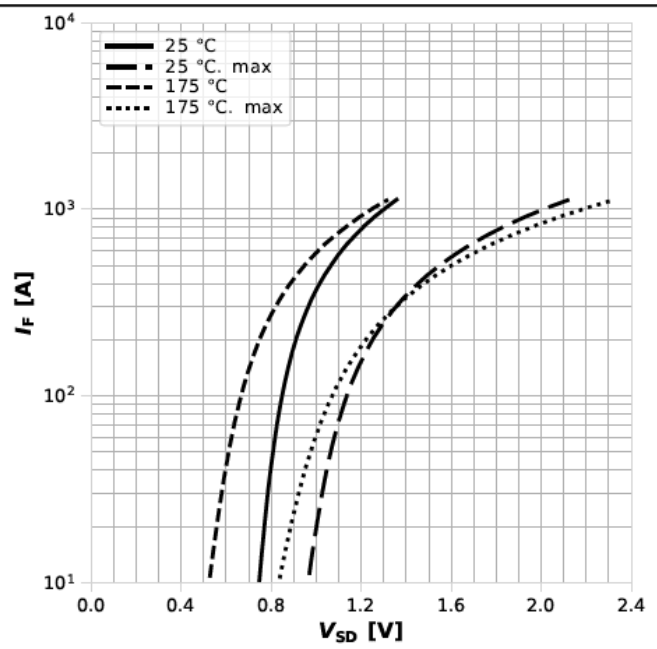
$V_{GS(th}$=f(T_j), V_{GS}=V_{DS}; parameter: I_D

Diagram 11: Typ. capacitances



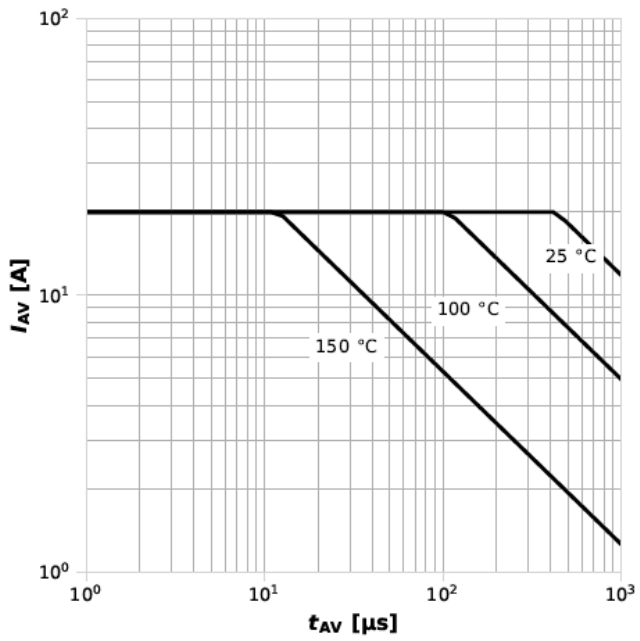
C=f(V_{DS}); V_{GS}=0 V; f=1 MHz

Diagram 12: Forward characteristics of reverse diode



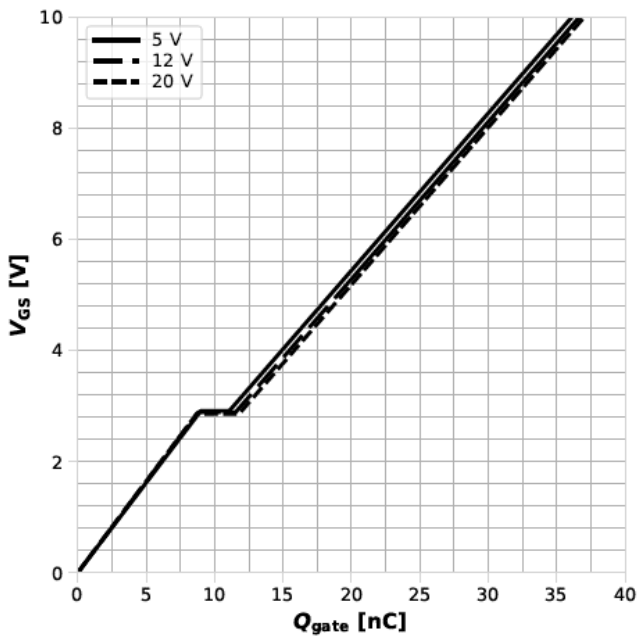
I_F=f(V_{SD}); parameter: T_j

Diagram 13: Avalanche characteristics



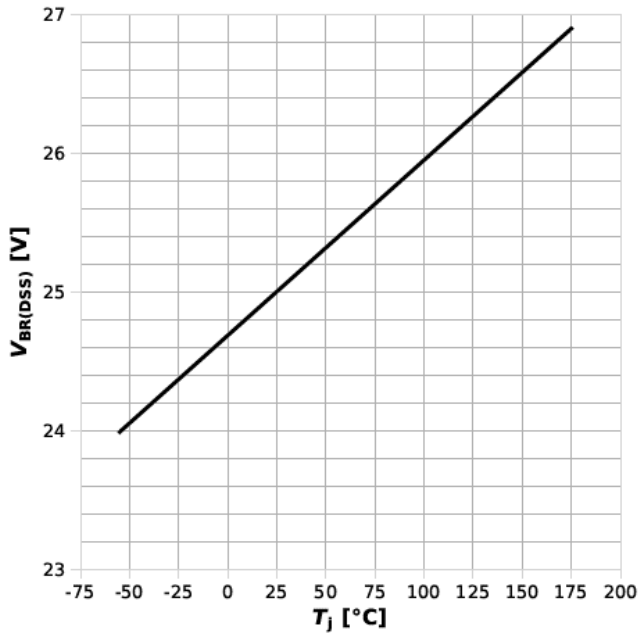
$I_{AS}=f(t_{AV})$; $R_{GS}=25\ \Omega$; parameter: $T_{j,start}$

Diagram 14: Typ. gate charge



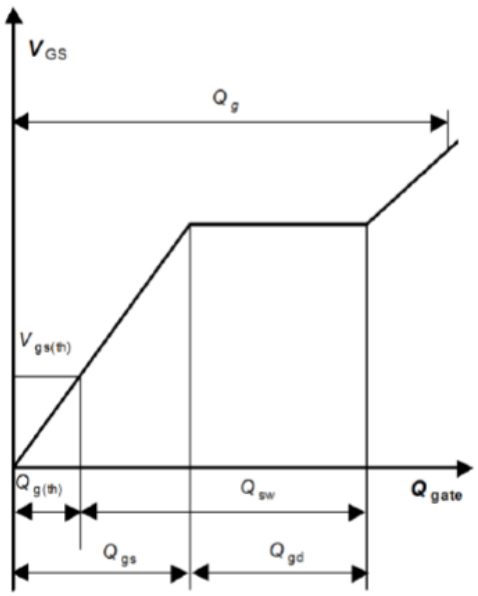
$V_{GS}=f(Q_{gate})$, $I_D=20$ A pulsed, $T_j=25$ °C; parameter: V_{DD}

Diagram 15: Min. drain-source breakdown voltage



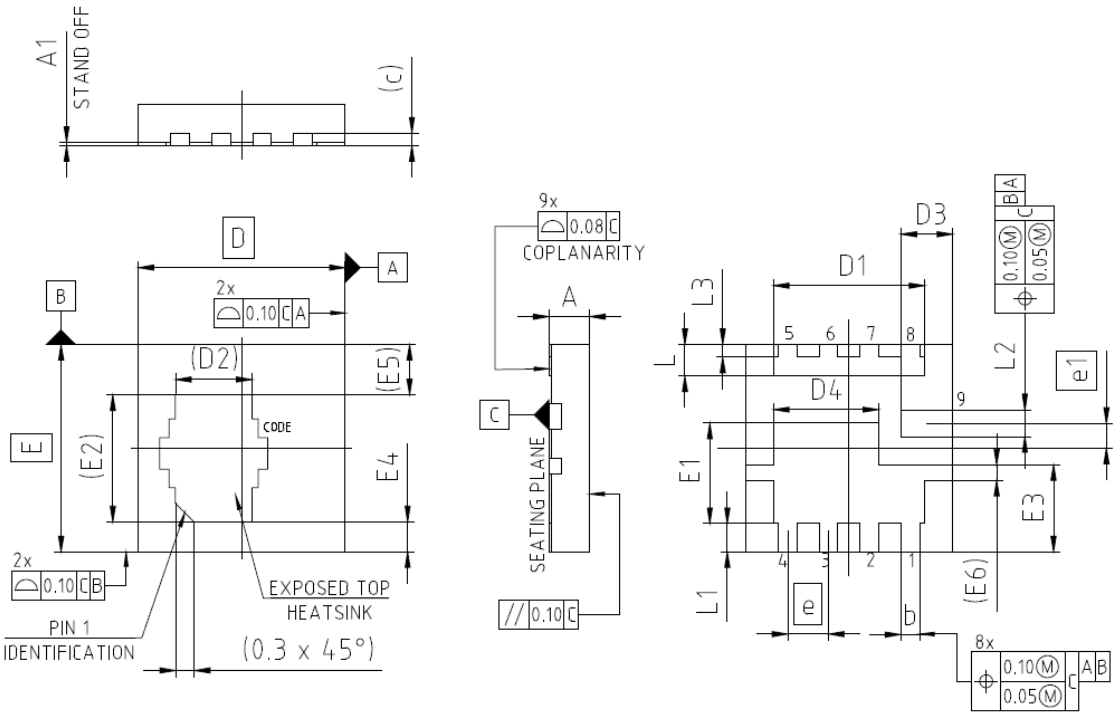
$V_{BR(DSS)}=f(T_j)$; $I_D=1$ mA

Gate charge waveforms



-

5 封装外形



PACKAGE - GROUP NUMBER:	PG-WHTFN-9-U03				
DIMENSIONS	MILLIMETERS		DIMENSIONS	MILLIMETERS	
	MIN.	MAX.		MIN.	MAX.
A	0.55	0.75	E4	0.27	0.67
A1	---	0.05	E5	0.80	
b	0.20	0.40	E6	0.25	
c	0.20		e	0.65	
D	3.30		e1	0.395	
D1	2.31	2.51	L	0.40	0.60
D2	1.22		L1	0.35	0.55
D3	0.73	0.93	L2	0.32	0.52
D4	1.58	1.78	L3	0.10	0.30
E	3.30				
E1	1.50	1.70			
E2	2.03				
E3	1.285	1.485			

NOTE: DIMENSIONS DO NOT INCLUDE MOLD FLASH, PROTRUSION OR GATE BURRS

图 1　　PG- WHTFN- 9 封装外形，尺寸单位为毫米

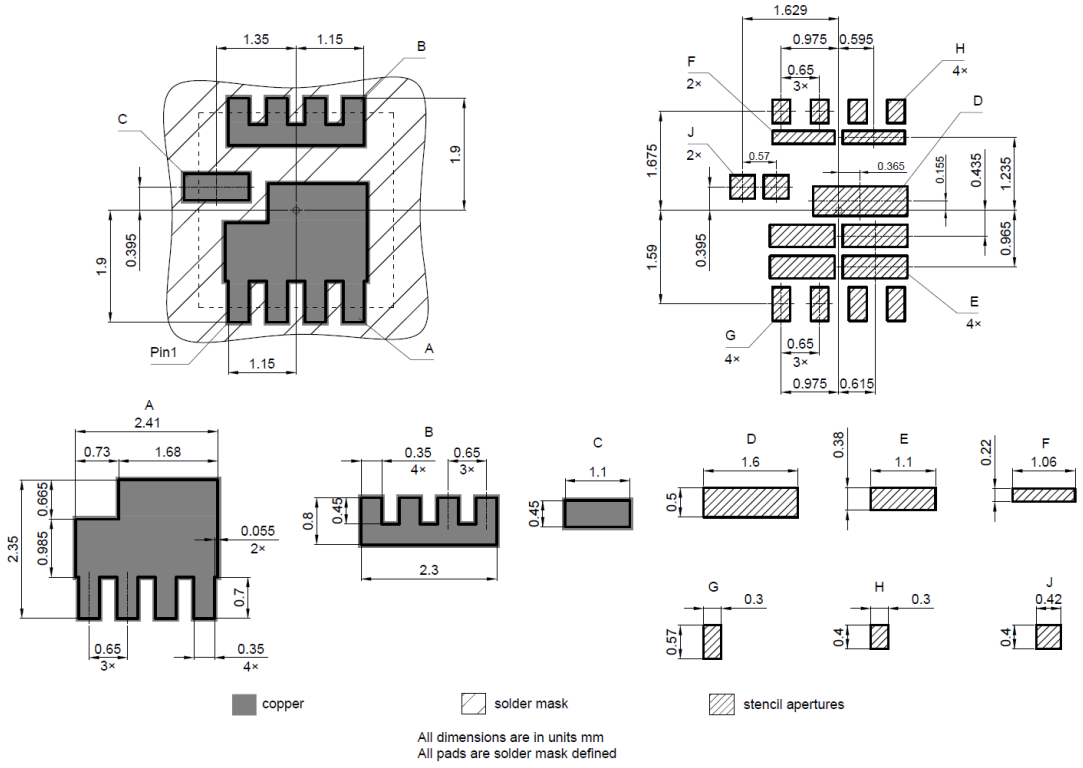


图 2　　PG- WHTFN- 9封装图，尺寸单位为毫米

修订记录

IQEH80NE2LM7UCGSC

Revision 2025-08-07, Rev. 1. 0

Previous revisions

Revision	Date	Subjects (major changes since last revision)
1.0	2025-08-07	Release of final datasheet

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