

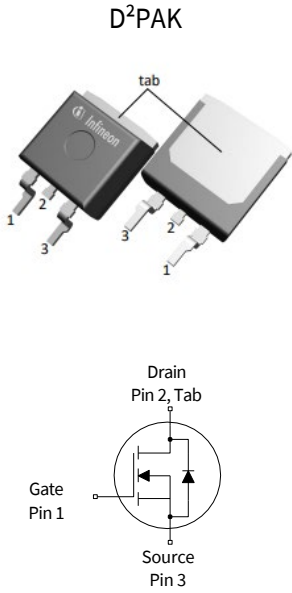


英飞凌MOSFET线性场效应晶体管2

英飞凌IPB021N10NM5LF2 OptiMOS™ 5线性场效应晶体管2, 100 V

特性

- 非常适合热插拔、电池保护和电子保险丝应用
- 当前共享的理想选择
- 极低的导通电阻 $R_{DS(on)}$
- 宽范围安全工作区 (SOA)
- 紧密 $V_{GS(th)}$ 传播
- 低跨导 g_{fs}
- N沟道，标准电平
- 100% 雪崩测试
- 无铅镀层；符合RoHS标准
- 符合 IEC61249-2-21 标准的无卤素

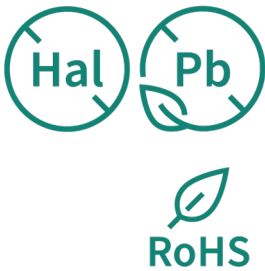


产品验证

通过 JEDEC 相关测试。

表 1 主要性能参数

Parameter	Value	Unit
V_{DS}	100	V
$R_{DS(on),max}$	2.1	mΩ
I_D	176	A
$I_{pulse} (V_{DS}=56\text{ V}, t_p=10\text{ ms})$	10.7	A



Part number	Package	Marking	Related links
IPB021N10NM5LF2	PG-T0263-3	21N10LF2	-

本数据手册的原文使用英文撰写。为方便起见，英飞凌提供了译文；由于翻译过程中可能使用了自动化工具，英飞凌不保证译文的准确性。为确认准确性，请务必访问infineon.com 参考最新的英文版本（控制文档）。

目录

描述.....	1
最大额定值.....	3
热特性	3
电气特性	4
电气特性图.....	6
封装外形	11
修订记录	12
商标.....	13
免责声明.....	13

1 最大额定值

除非另有规定, $T_A=25\text{ }^{\circ}\text{C}$

表 2 最大额定值

Parameter	Symbol	Values			Unit	Note / Test condition
		Min.	Typ.	Max.		
Continuous drain current ¹⁾	I_D	-	-	176	A	$V_{GS}=10\text{ V}, T_C=25\text{ }^{\circ}\text{C}$
				136		$V_{GS}=10\text{ V}, T_C=100\text{ }^{\circ}\text{C}$
				136		$V_{GS}=15\text{ V}, T_C=100\text{ }^{\circ}\text{C}$
				30		$V_{GS}=10\text{ V}, T_A=25\text{ }^{\circ}\text{C}, R_{thJA}=40\text{ }^{\circ}\text{C/W}^{2)}$
Pulsed drain current ³⁾	$I_{D,pulse}$	-	-	704	A	$T_C=25\text{ }^{\circ}\text{C}$
Avalanche energy, single pulse ⁴⁾	E_{AS}	-	-	1166	mJ	$I_D=100\text{ A}, R_{GS}=25\text{ }\Omega$
Gate source voltage	V_{GS}	-20	-	20	V	-
Power dissipation	P_{tot}	-	-	375	W	$T_C=25\text{ }^{\circ}\text{C}$
				3.8		$T_A=25\text{ }^{\circ}\text{C}, R_{thJA}=40\text{ }^{\circ}\text{C/W}^{2)}$
Operating and storage temperature	T_j, T_{stg}	-55	-	175	$^{\circ}\text{C}$	-

- 1) 额定值指产品仅具有数据手册指定的绝对最大值, 保持外壳温度符合规定要求。其他外壳温度请参见图2。需要根据实际环境条件降低额定值。
- 2) 器件置于40 mm × 40 mm × 1.5 mm 环氧树脂 PCB FR4基板上, 配有6 cm² (单层, 70 μm 厚) 铜层, 用于漏极连接。PCB在静止空气中垂直放置。
- 3) 详细信息请参见图 3和图 4
- 4) 详细信息请参见图 14

2 热特性

表 3 热特性

Parameter	Symbol	Values			Unit	Note / Test condition
		Min.	Typ.	Max.		
Thermal resistance, junction - case	R_{thJC}	-	-	0.4	$^{\circ}\text{C/W}$	-
Thermal resistance, junction - ambient, 6 cm ² cooling area ⁵⁾	R_{thJA}			40		
Thermal resistance, junction - ambient, minimal footprint	R_{thJA}			62		

- 5) 器件置于40 mm × 40 mm × 1.5 mm 环氧树脂 PCB FR4基板上, 配有6 cm² (单层, 70 μm 厚) 铜层, 用于漏极连接。PCB在静止空气中垂直放置。

3 电气特性

除非另有规定, $T_j=25\text{ °C}$

表 4 静态特性

Parameter	Symbol	Values			Unit	Note / Test condition
		Min.	Typ.	Max.		
Drain-source breakdown voltage	$V_{(BR)DSS}$	100	-	-	V	$V_{GS}=0\text{ V}$, $I_D=1\text{ mA}$
Gate threshold voltage	$V_{GS(th)}$	2.75	3.1	3.45	V	$V_{DS}=V_{GS}$, $I_D=280\text{ }\mu\text{A}$
Zero gate voltage drain current	I_{DSS}	-	0.1	1	μA	$V_{DS}=100\text{ V}$, $V_{GS}=0\text{ V}$, $T_j=25\text{ °C}$
			10	100		$V_{DS}=100\text{ V}$, $V_{GS}=0\text{ V}$, $T_j=125\text{ °C}$
Gate-source leakage current	I_{GSS}	-	10	100	nA	$V_{GS}=20\text{ V}$, $V_{DS}=0\text{ V}$
Drain-source on-state resistance	$R_{DS(on)}$	-	1.6	2.0	m Ω	$V_{GS}=15\text{ V}$, $I_D=100\text{ A}$
			1.9	2.1		$V_{GS}=10\text{ V}$, $I_D=100\text{ A}$
Gate resistance	R_G	-	1.3	2.0	Ω	-
Transconductance	g_{fs}	48	95	-	S	$ V_{DS} \geq 2 I_D R_{DS(on)max}$, $I_D=100\text{ A}$

表 5 动态特性

Parameter	Symbol	Values			Unit	Note / Test condition
		Min.	Typ.	Max.		
Input capacitance ⁶⁾	C_{iss}	-	13000	17000	pF	$V_{GS}=0\text{ V}$, $V_{DS}=50\text{ V}$, $f=1\text{ MHz}$
Output capacitance ⁶⁾	C_{oss}		1800	2300		
Reverse transfer capacitance ⁶⁾	C_{rss}		35	61		
Turn-on delay time	$t_{d(on)}$	-	33	-	ns	$V_{DD}=50\text{ V}$, $V_{GS}=10\text{ V}$, $I_D=100\text{ A}$, $R_{G,ext}=1.6\text{ }\Omega$
Rise time	t_r		60			
Turn-off delay time	$t_{d(off)}$		49			
Fall time	t_f		23			

⁶⁾ 由设计指定, 未经过生产测试。

表 6 栅极电荷特性⁷⁾

Parameter	Symbol	Values			Unit	Note / Test condition
		Min.	Typ.	Max.		
Gate to source charge	Q_{gs}	-	84	-	nC	$V_{DD}=50\text{ V}$, $I_D=100\text{ A}$, $V_{GS}=0\text{ to }10\text{ V}$
Gate charge at threshold	$Q_{g(th)}$		41	-	nC	
Gate to drain charge ⁸⁾	Q_{gd}		27	41	nC	
Switching charge	Q_{sw}		69	-	nC	
Gate charge total ⁸⁾	Q_g		165	206	nC	
Gate plateau voltage	$V_{plateau}$		6.3	-	V	
Gate charge total, sync. FET	$Q_{g(sync)}$	-	150	-	nC	$V_{DS}=0.1\text{ V}$, $V_{GS}=0\text{ to }10\text{ V}$
Output charge ⁸⁾	Q_{oss}	-	211	281	nC	$V_{DS}=50\text{ V}$, $V_{GS}=0\text{ V}$

⁷⁾ 参数定义请参见“栅极充电波形”。

⁸⁾ 由设计指定，未经过生产测试。

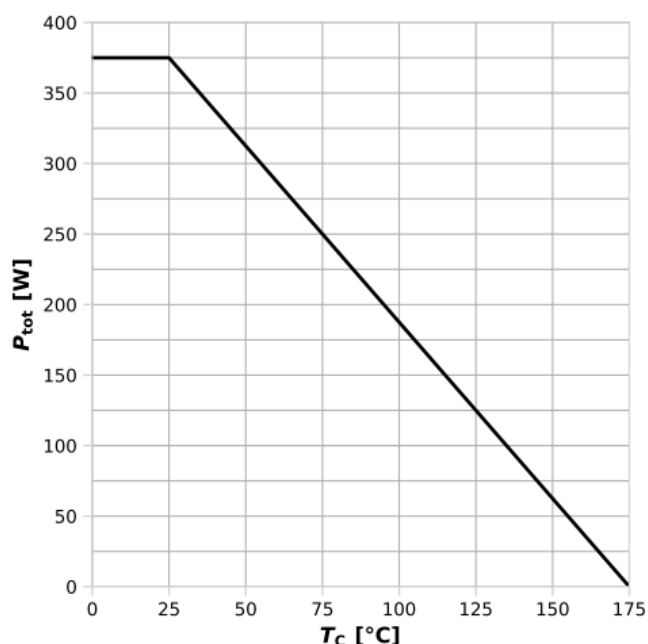
表 7 反向二极管

Parameter	Symbol	Values			Unit	Note / Test condition
		Min.	Typ.	Max.		
Diode continuous forward current	I_S	-	-	156	A	$T_C=25\text{ °C}$
Diode pulse current	$I_{S,pulse}$			704		
Diode forward voltage	V_{SD}	-	0.88	1.2	V	$V_{GS}=0\text{ V}$, $I_F=100\text{ A}$, $T_J=25\text{ °C}$
Reverse recovery time ⁹⁾	t_{rr}	-	66	132	ns	$V_R=50\text{ V}$, $I_F=100\text{ A}$, $di_F/dt=100\text{ A}/\mu\text{s}$
Reverse recovery charge ⁹⁾	Q_{rr}		109	218	nC	

⁹⁾ 由设计指定，未经过生产测试。

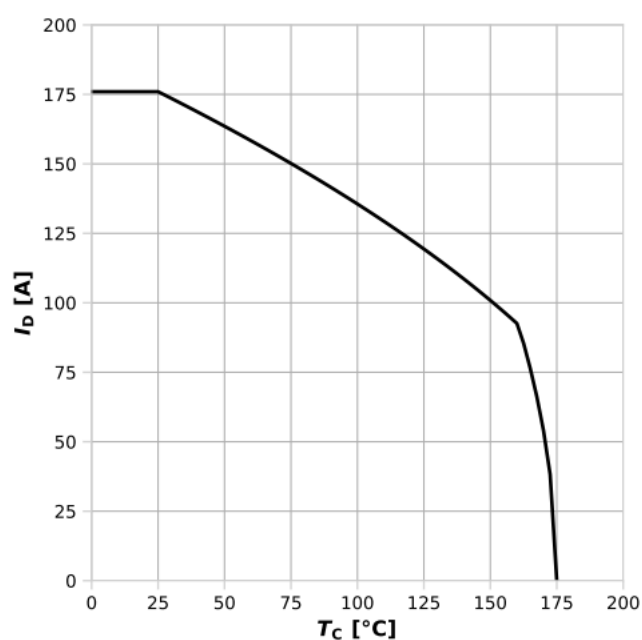
4 电气特性图

图 1: 功率耗散



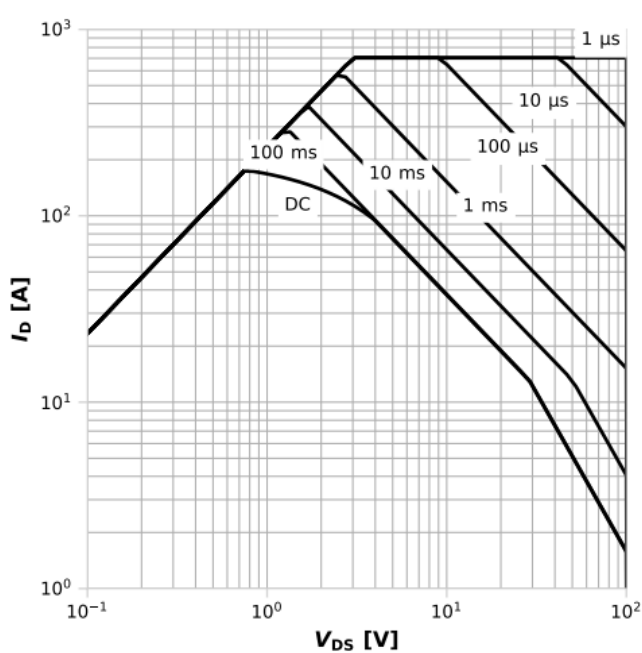
$$P_{tot}=f(T_c)$$

图 2: 漏极电流



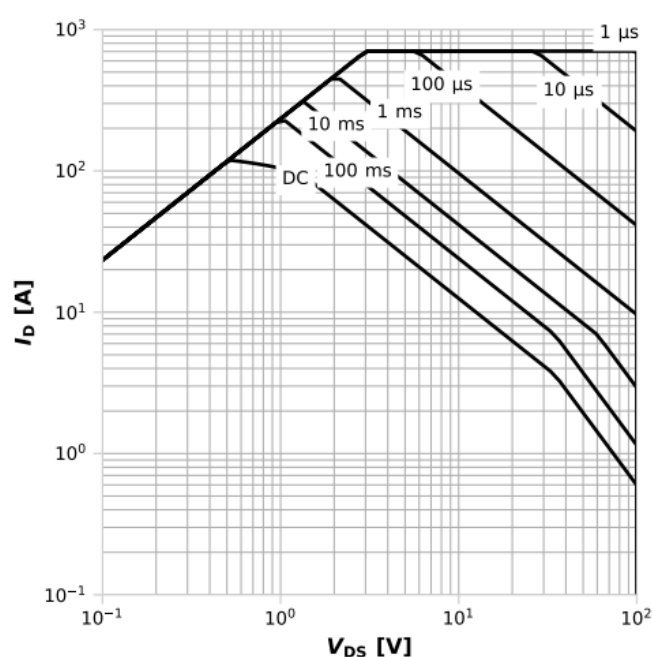
$$I_D=f(T_c); V_{GS} \geq 10 \text{ V}$$

图 3: 安全操作区



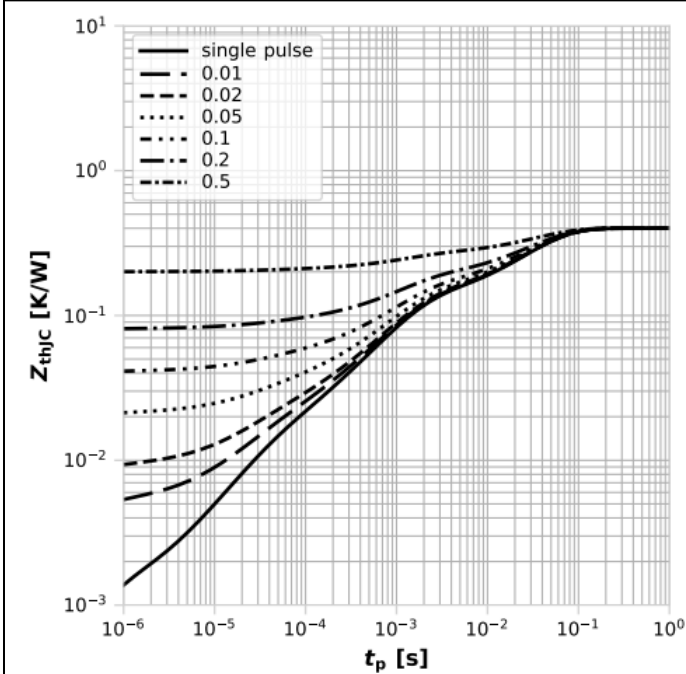
$$I_D=f(V_{DS}); T_c=25^\circ\text{C}; D=0; \text{parameter: } t_p$$

图 4: 安全操作区



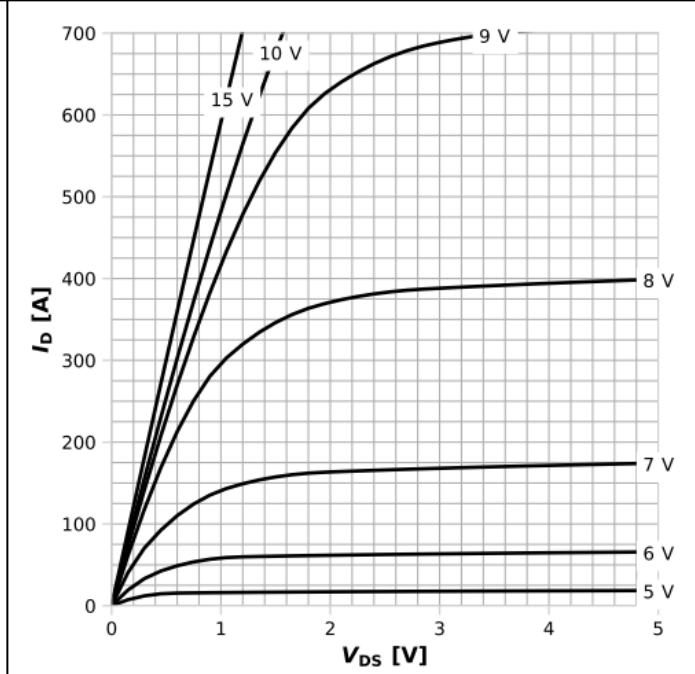
$$I_D=f(V_{DS}); T_c=125^\circ\text{C}; D=0; \text{parameter: } t_p$$

图 5: 最大瞬态热阻抗



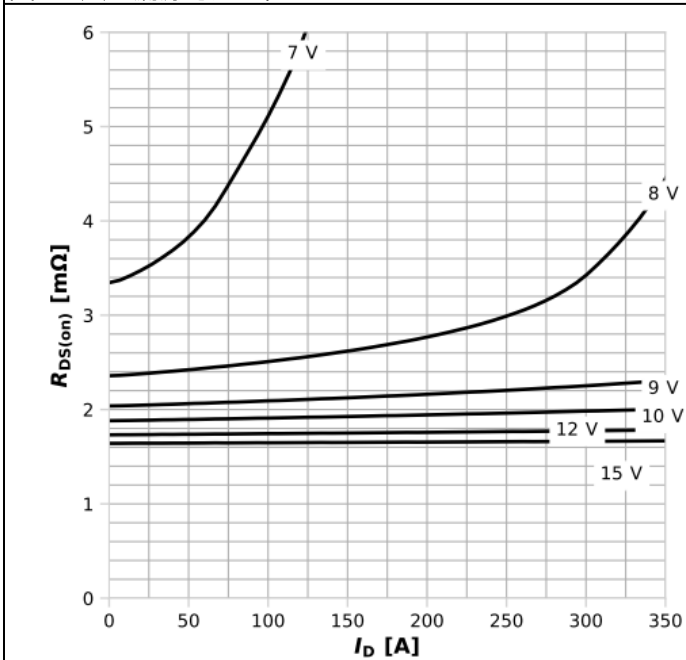
$$Z_{thJC} = f(t_p); \text{ parameter: } D = t_p / T$$

图 6: 典型输出特性



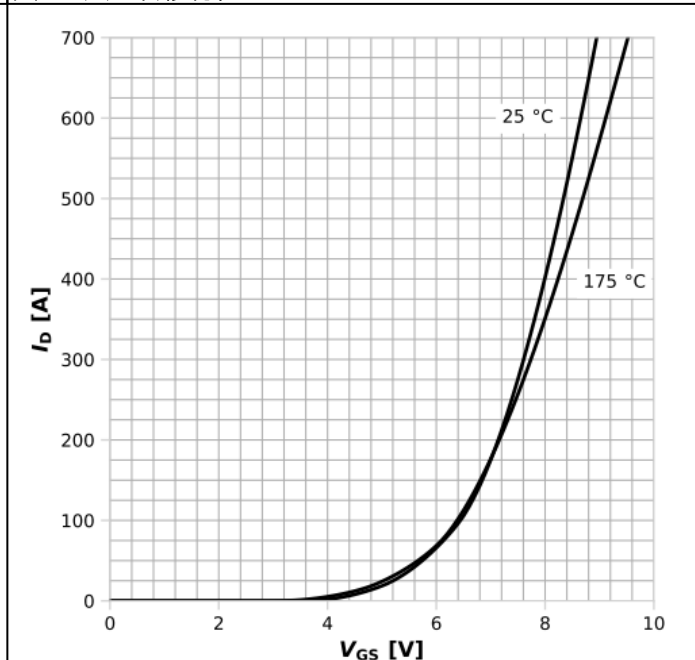
$$I_D = f(V_{DS}), T_j = 25^\circ\text{C}; \text{ parameter: } V_{GS}$$

图 7: 典型漏源导通电阻



$$R_{DS(on)} = f(I_D), T_j = 25^\circ\text{C}; \text{ parameter: } V_{GS}$$

图 8: 典型转移特性



$$I_D = f(V_{GS}), |V_{DS}| > 2 |I_D| R_{DS(on)max}; \text{ parameter: } T_j$$

图 9: 典型漏源导通电阻

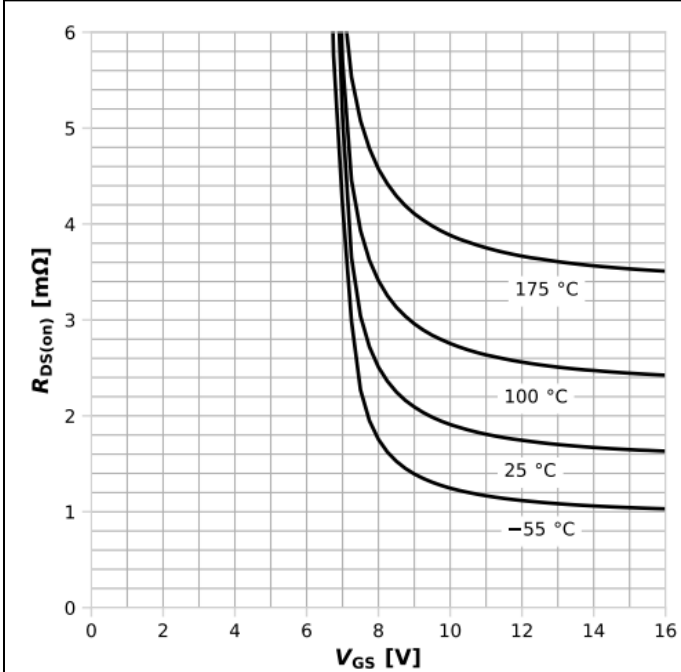

 $R_{DS(on)} = f(V_{GS}), I_D = 100 \text{ A}; \text{ parameter: } T_j$

图 10: 典型漏源导通电阻

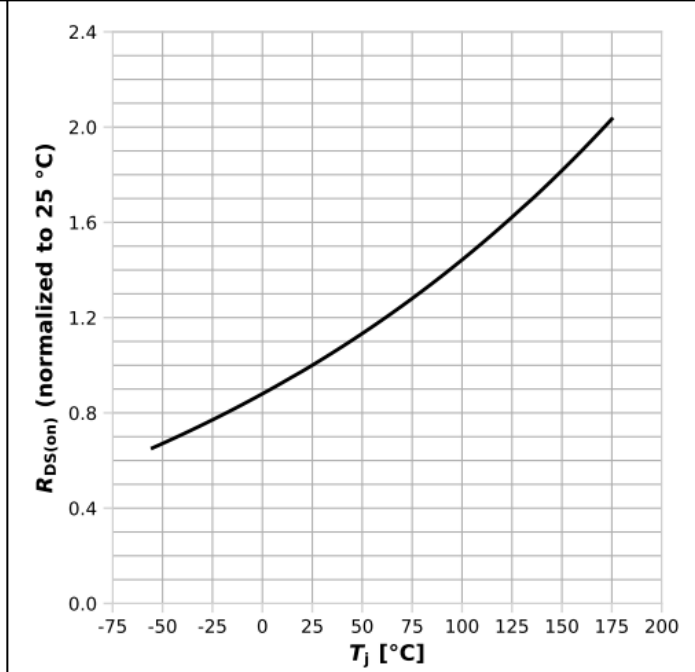

 $R_{DS(on)} = f(T_j), I_D = 100 \text{ A}, V_{GS} = 10 \text{ V}$

图 11: 典型栅极阈值电压

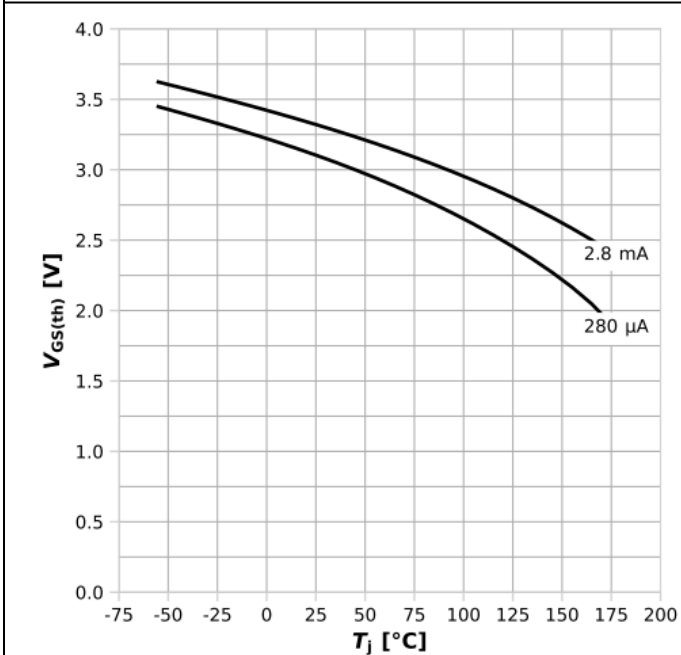

 $V_{GS(th)} = f(T_j), V_{GS} = V_{DS}; \text{ parameter: } I_D$

图 12: 典型电容值

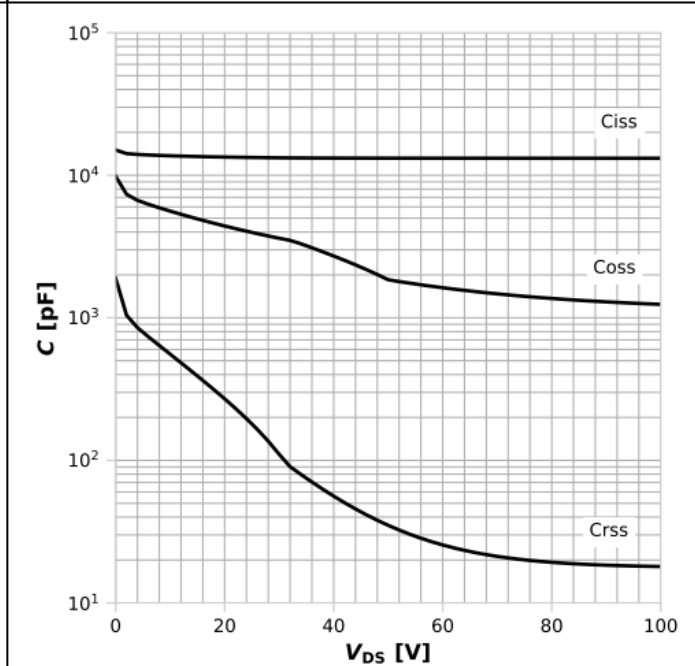

 $C = f(V_{DS}); V_{GS} = 0 \text{ V}; f = 1 \text{ MHz}$

图 13: 反向二极管的正向特性

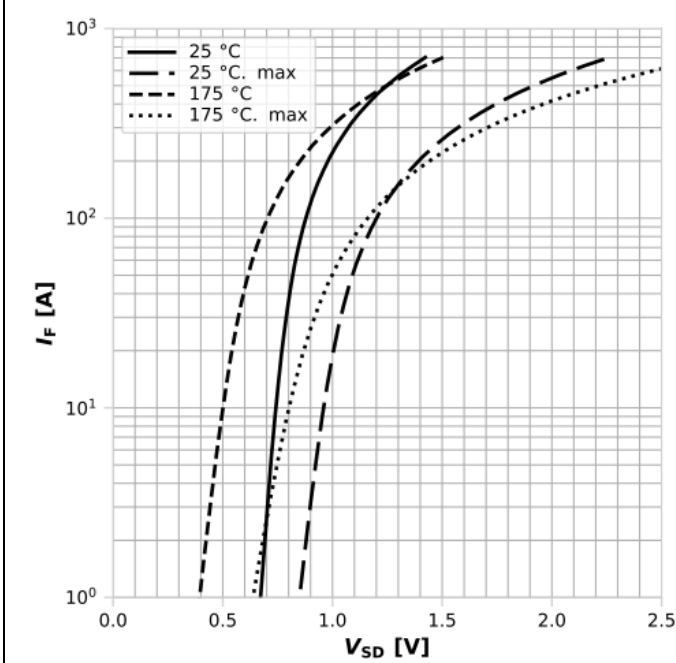

 $I_F = f(V_{DS})$; parameter: T_j

图 14: 雪崩特性

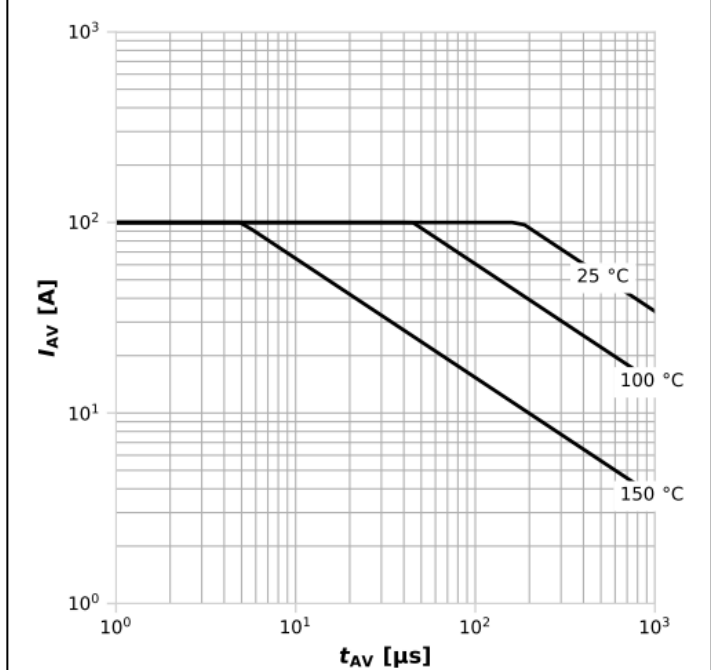

 $I_{AS} = f(t_{AV})$; $R_{GS} = 25 \Omega$; parameter: $T_{j,start}$

图 15: 典型栅极电荷

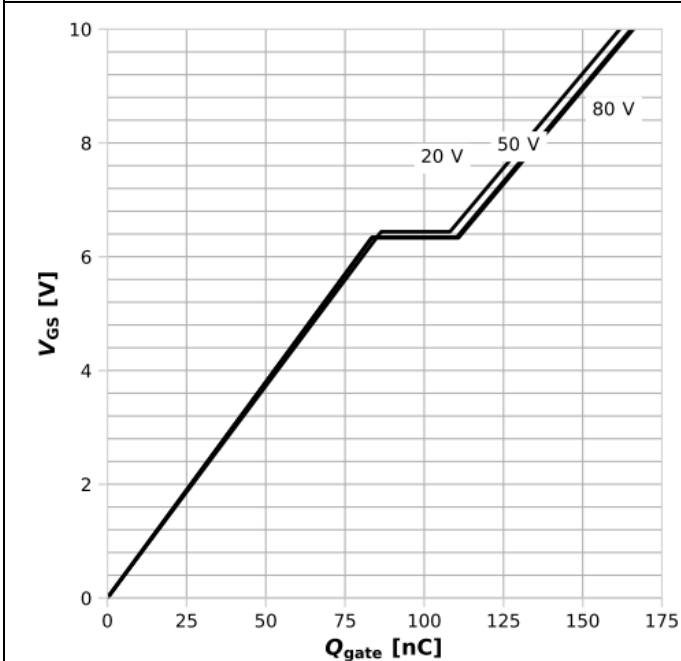
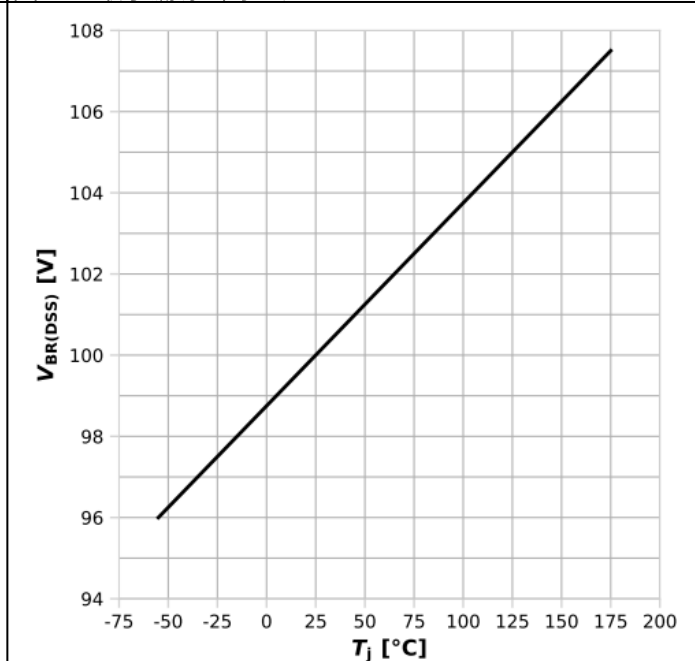
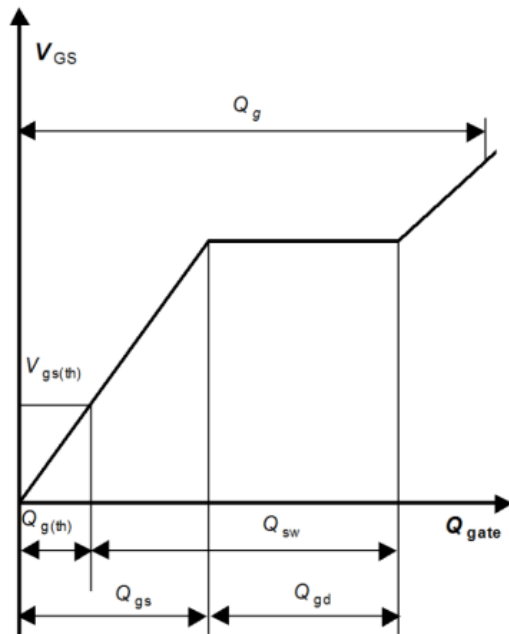

 $V_{GS} = f(Q_{gate})$, $I_D = 100 \text{ A pulsed}$, $T_j = 25 \text{ °C}$; parameter: V_{DD}

图 16: 最小漏源击穿电压


 $V_{BR(DSS)} = f(T_j)$; $I_D = 1 \text{ mA}$

图栅极充电波形



5 封装外形

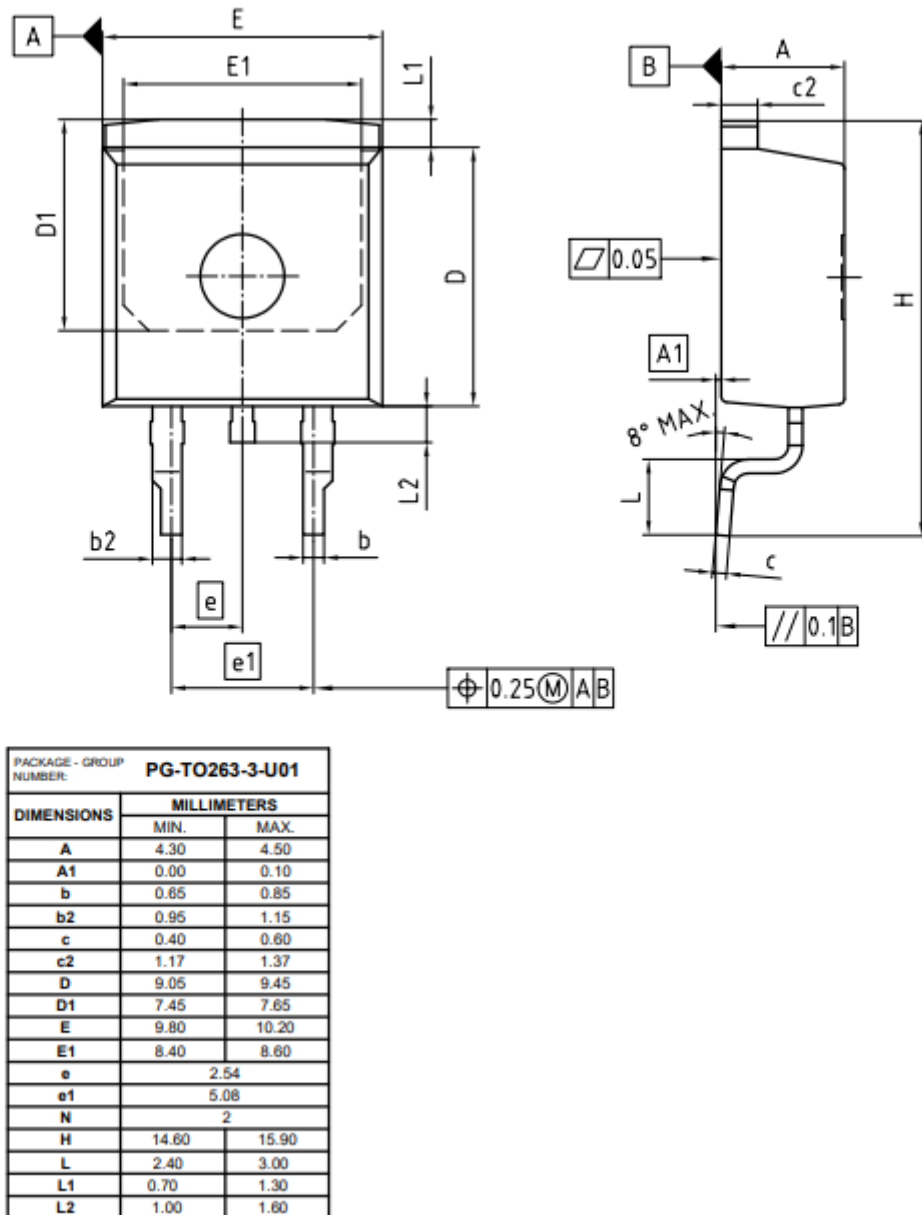


图 1 PG-TO 263 - 3外形图，尺寸单位为毫米

修订记录

IPB021N10NM5LF2

Revision: 2025 - 11 - 07, Rev. 1.2

历史修订版本

Revision	Date	Subjects (major changes since last revision)
1.0	2025-01-24	Release of final datasheet
1.1	2025-08-28	Update gate threshold voltage
1.2	2025-11-07	Update "Features"

Trademarks

All referenced product or service names and trademarks are the property of their respective owners.

Published by Infineon Technologies AG, 81726 Munich, Germany

Copyright (c) 2025 Infineon Technologies AG and its affiliates. All Rights Reserved.

Important notice

Products which may also include samples and may be comprised of hardware or software or both ("Product(s)") are sold or provided and delivered by Infineon Technologies AG and its affiliates ("Infineon") subject to the terms and conditions of the frame supply contract or other written agreement(s) executed by a customer and Infineon or, in the absence of the foregoing, the applicable Sales Conditions of Infineon. General terms and conditions of a customer or deviations from applicable Sales Conditions of Infineon shall only be binding for Infineon if and to the extent Infineon has given its express written consent.

For the avoidance of doubt, Infineon disclaims all warranties of non-infringement of third-party rights and implied warranties such as warranties of fitness for a specific use/purpose or merchantability.

Infineon shall not be responsible for any information with respect to samples, the application or customer's specific use of any Product or for any examples or typical values given in this document.

The data contained in this document is exclusively intended for technically qualified and skilled customer representatives. It is the responsibility of the customer to evaluate the suitability of the Product for the intended application and the customer's specific use and to verify all relevant technical data contained in this document in the intended application and the customer's specific use. The customer is responsible for properly designing, programming, and testing the functionality and safety of the intended application, as well as complying with any legal requirements related to its use.

Unless otherwise explicitly approved by Infineon, Products may not be used in any application where a failure of the Products or any consequences of the use thereof can reasonably be expected to result in personal injury. However, the foregoing shall not prevent the customer from using any Product in such fields of use that Infineon has explicitly designed and sold it for, provided that the overall responsibility for the application lies with the customer.

Infineon expressly reserves the right to use its content for commercial text and data mining (TDM) according to applicable laws, e.g. Section 44b of the German Copyright Act (UrhG).

If the Product includes security features: Because no computing device can be absolutely secure, and despite security measures implemented in the Product, Infineon does not guarantee that the Product will be free from intrusion, data theft or loss, or other breaches ("Security Breaches"), and Infineon shall have no liability arising out of any Security Breaches.

If this document includes or references software:

The software is owned by Infineon under the intellectual property laws and treaties of the United States, Germany, and other countries worldwide. All rights reserved. Therefore, you may use the software only as provided in the software license agreement accompanying the software. If no software license agreement applies, Infineon hereby grants you a personal, non-exclusive, non-transferable license (without the right to sublicense) under its intellectual property rights in the software (a) for software provided in source code form, to modify and reproduce the software solely for use with Infineon hardware products, only internally within your organization, and (b) to distribute the software in binary code form externally to end users, solely for use on Infineon hardware products. Any other use, reproduction, modification, translation, or compilation of the software is prohibited.

For further information on the Product, technology, delivery terms and conditions, and prices, please contact your nearest Infineon office or visit <https://www.infineon.com>.



免责声明

请注意，本文件的原文使用英文撰写，为方便客户浏览英飞凌提供了中文译文。该中文译文仅供参考，并不可作为任何论点之依据。

由于翻译过程中可能使用了自动化程序，以及语言翻译和转换过程中的差异，最后的中文译文与最新的英文版本原文含义可能存在不尽相同之处。

因此，我们同时提供该中文译文版本的最新英文原文供您阅读，请参见 <http://www.infineon.com>

英文原文和中文译文版本之间若存有任何歧异，以最新的英文版本为准，并且仅认可英文版本为正式文件。

您如果使用本文件，即表示您同意并理解上述说明。英飞凌不对因翻译过程中可能存在的任何不完整或不准确信息而产生的任何直接或间接损失或损害负责。英飞凌不承担中文译文版本的完整性和准确性责任。如果您不同意上述说明，请不要使用本文件。

Trademarks

All referenced product or service names and trademarks are the property of their respective owners.

重要通知

版本 2026-06-25

Infineon Technologies AG 出版，
德国 Neubiberg 85579

版权 © 2026 Infineon Technologies AG
及其关联公司。
保留所有权利。

**Do you have a question about this
document?**

Email:

erratum@infineon.com

Infineon Technologies AG 及其关联公司（以下简称“英飞凌”）销售或提供和交付的产品（可能也包括样品，且可能由硬件或软件或两者组成）（以下简称“产品”），应遵守客户与英飞凌签订的框架供应合同或其他书面协议的条款和条件，如无上述合同或其他书面协议，则应遵守适用的英飞凌销售条件。只有在英飞凌明确书面同意的情况下，客户的一般条款和条件或对适用的英飞凌销售条件的偏离才对英飞凌具有约束力。

为避免疑义，英飞凌不承担侵犯第三方权利的所有保证和默示保证，例如对特定用途/目的的适用性或适销性的保证。

英飞凌对与样品、应用或客户对任何产品的具体使用有关的任何信息或本文件中给出的任何示例或典型值概不负责。

本文件中包含的数据仅供具有技术资格和技能的客户代表使用。客户有责任评估产品对预期应用和客户特定用途的适用性，并在预期应用和客户特定用途中验证本文件中包含的所有相关技术数据。客户有责任正确设计、编程和测试预期应用的功能性和安全性，并遵守与其使用相关的法律要求。

除非英飞凌另行明确批准，否则产品不得用于任何因产品故障或使用产品的任何后果可合理预期会导致人身伤害的应用。但是，上述规定并不妨碍客户在英飞凌明确设计和销售的使用领域中使用任何产品，但是客户对应用负有全部责任。

英飞凌明确保留根据适用法律，如《德国版权法》（UrhG）第 44b 条，将其内容用于商业资料和数据勘探（TDM）的权利。

如果产品包含安全功能：

由于任何计算设备都不可能绝对安全，尽管产品采取了安全措施，但英飞凌不保证产品不会被入侵、数据不会被盗或遗失，或不会发生其他漏洞（以下简称“安全漏洞”），英飞凌对任何安全漏洞不承担任何责任。

如果本文件包含或引用软件：

根据美国、德国和世界其他国家的知识产权法律和条约，该软件归英飞凌所有。英飞凌保留所有权利。因此，您只能按照软件附带的软件授权协议的规定使用本软件。

如果没有适用的软件授权协议，英飞凌特此授予您个人的、非排他性的、不可转让的软件知识产权授权（无权转授权）：(a) 对于以源代码形式提供的软件，仅在贵组织内部修改和复制该软件用于英飞凌硬件产品；及 (b) 对于以二进制代码 (binary code) 形式对外向终端用户分发该软件，仅得用于英飞凌硬件产品。禁止对本软件进行任何其他使用、复制、修改、翻译或编译。有关产品、技术、交货条款和条件以及价格的详细信息，请联系离您最近的英飞凌办公室或访问 <https://www.infineon.com>。