

英飞凌工业级CoolGaN™

英飞凌IGC025S08S1工业级 CoolGaN™ 晶体管 80 V G3

特性

- 超快速开关和高效率
- 节省空间且高度稳健的封装
- 不含反向恢复电荷
- 超低栅极电荷和输出电荷
- 裸露的芯片可实现顶部散热性能
- 防潮等级 MSL1
- 工业级3x5封装

潜在应用

- 通信和数据中心 48V IBC
- AC-DC 和 DC-DC 转换器的同步整流
- 机器人和无人机
- 电池供电工具
- 48V伺服驱动器
- 电动汽车、无人机
- 负载点转换器
- 太阳能和储能系统

产品验证

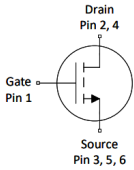
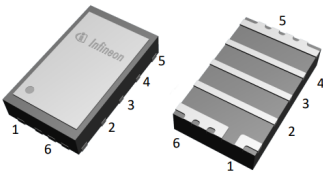
完全符合 JEDEC 工业应用标准

表 1 主要性能参数

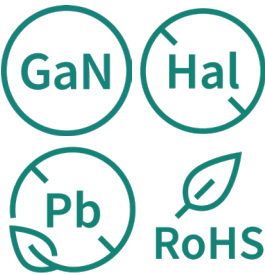
Parameter	Value	Unit
V_{DS}	80	V
$R_{DS(on)}$	1.8	mΩ
I_D	86	A
Q_{oss}	40	nC
Q_G	12	nC
Q_{rr}	0	nC

Part number	Package	Marking	Related links
IGC025S08S1	PG-TSON-6	25SD1	see Appendix A

PG-TSON-6



顶部是暴露的硅衬底，
内部连接至源端子。不建议用作电气
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1 最大额定值

除非另有说明，否则 $T_j = 25^\circ\text{C}$ 。超过最大额定值的应力可能会对器件造成永久性损坏。为获得最佳使用寿命和可靠性，英飞凌建议运行条件不要持续超过所述最大额定值的 80%（除非另有明确说明）。如需更多信息，请联系您当地的英飞凌销售办事处。

表 2 最大额定值

Parameter	Symbol	Values			Unit	Note / Test condition
		Min.	Typ.	Max.		
Continuous drain-source voltage	V_{DS}	-	-	80	V	$V_{GS}=0\text{ V}$
Pulsed drain-source voltage ¹⁾	$V_{DS, pulse}$	-	-	96	V	$V_{GS}=0\text{ V}$, 1 h total time
Continuous drain current	I_D	-	-	86	A	$V_{GS}=5\text{ V}$, $T_C=25^\circ\text{C}$
				23		$V_{GS}=5\text{ V}$, $T_A=25^\circ\text{C}$, $R_{thJA}=38^\circ\text{C/W}$ ²⁾
Pulsed drain current ³⁾	$I_{D, pulse}$	-	-	550	A	$T_j=25^\circ\text{C}$
				290		$T_j=150^\circ\text{C}$
Pulsed gate-source voltage ¹⁾	V_{GS}	-6.5	-	6.5	V	Pulsed 100 h total time
Power dissipation	P_{tot}	-	-	45	W	$T_C=25^\circ\text{C}$
				3.3		$T_A=25^\circ\text{C}$, $R_{thJA}=38^\circ\text{C/W}$ ²⁾
Storage temperature	T_{stg}	-55	-	150	$^\circ\text{C}$	-
Junction temperature	T_j	-40	-	150	$^\circ\text{C}$	-

1) 作为异常操作条件下的稳健性测量，不建议用于正常运行。

2) 器件位于 4 层 FR4 PCB 上，垂直放置在静止空气中。

3) 脉冲电流受转移特性限制。

2 推荐运行条件

表 3 推荐运行条件

Parameter	Symbol	Values			Unit	Note / Test condition
		Min.	Typ.	Max.		
Gate-source voltage	V_{GS}	-4.0	5.0	5.5	V	-

3 热特性

表4 热特性

Parameter	Symbol	Values			Unit	Note / Test condition
		Min.	Typ.	Max.		
Thermal resistance, junction - case, top	R_{thJC}	-	0.5	0.6	°C/W	-
Thermal resistance, junction - case, bottom			1.9	2.8		
Thermal resistance, junction - ambient 1s0p	R_{thJA}	-	60	-	°C/W	On 1 layer PCB, vertical in still air.
Thermal resistance, junction - ambient 2s2p	R_{thJA}	-	38	-	°C/W	With vias on 4 layer PCB, vertical in still air.

4 电气特性

除非另有规定, $T_j = 25\text{ }^{\circ}\text{C}$

Table 5 Static characteristics

Parameter	Symbol	Values			Unit	Note / Test condition
		Min.	Typ.	Max.		
Gate threshold voltage	$V_{GS(th)}$	1.2	2.0	2.9	V	$V_{DS}=V_{GS}, I_D=10\text{ mA}$
Drain-source leakage current	I_{DSS}	-	0.2	2.6	μA	$V_{DS}=80\text{ V}, V_{GS}=0\text{ V}, T_j=25\text{ }^{\circ}\text{C}$
			7.0	60		$V_{DS}=80\text{ V}, V_{GS}=0\text{ V}, T_j=125\text{ }^{\circ}\text{C}$
Gate-source leakage current	I_{GSS}	-	19	315	μA	$V_{GS}=5\text{ V}, T_j=25\text{ }^{\circ}\text{C}$
			0.01	0.1		$V_{GS}=-4\text{ V}, T_j=25\text{ }^{\circ}\text{C}$
			140	1280		$V_{GS}=5\text{ V}, T_j=125\text{ }^{\circ}\text{C}$
			0.01	0.1		$V_{GS}=-4\text{ V}, T_j=125\text{ }^{\circ}\text{C}$
Drain-source on-state resistance	$R_{DS(on)}$	-	1.8	2.5	m Ω	$V_{GS}=5\text{ V}, I_D=25\text{ A}$
Gate resistance ⁴⁾	R_G	-	0.5	-	Ω	-

⁴⁾ 由设计定义。未经过生产测试。

表 6 电容值特性 ⁵⁾

Parameter	Symbol	Values			Unit	Note / Test condition
		Min.	Typ.	Max.		
Input capacitance	C_{iss}	-	1250	1400	pF	$V_{GS}=0\text{ V}, V_{DS}=40\text{ V}, f=1\text{ MHz}$
Output capacitance	C_{oss}		570	630		
Reverse transfer capacitance	C_{rss}		10	13		

⁵⁾ 由设计定义。未经过生产测试。

表 7 栅极电荷特性

Parameter	Symbol	Values			Unit	Note / Test condition
		Min.	Typ.	Max.		
Gate to source charge	Q_{gs}	-	3.6	-	nC	$V_{DS}=40\text{ V}, I_D=25\text{ A}, V_{GS}=0\text{ to }5\text{ V}$
Gate charge at threshold	$Q_{g(th)}$		2.5	-	nC	
Gate to drain charge ⁶⁾	Q_{gd}		3.1	-	nC	
Switching charge	Q_{sw}		4.2	-	nC	
Gate charge total ⁶⁾	Q_g		12	16	nC	
Gate plateau voltage	$V_{plateau}$		2.8	-	V	
Output charge ⁶⁾	Q_{oss}	-	40	44	nC	$V_{DS}=40\text{ V}, V_{GS}=0\text{ V}$

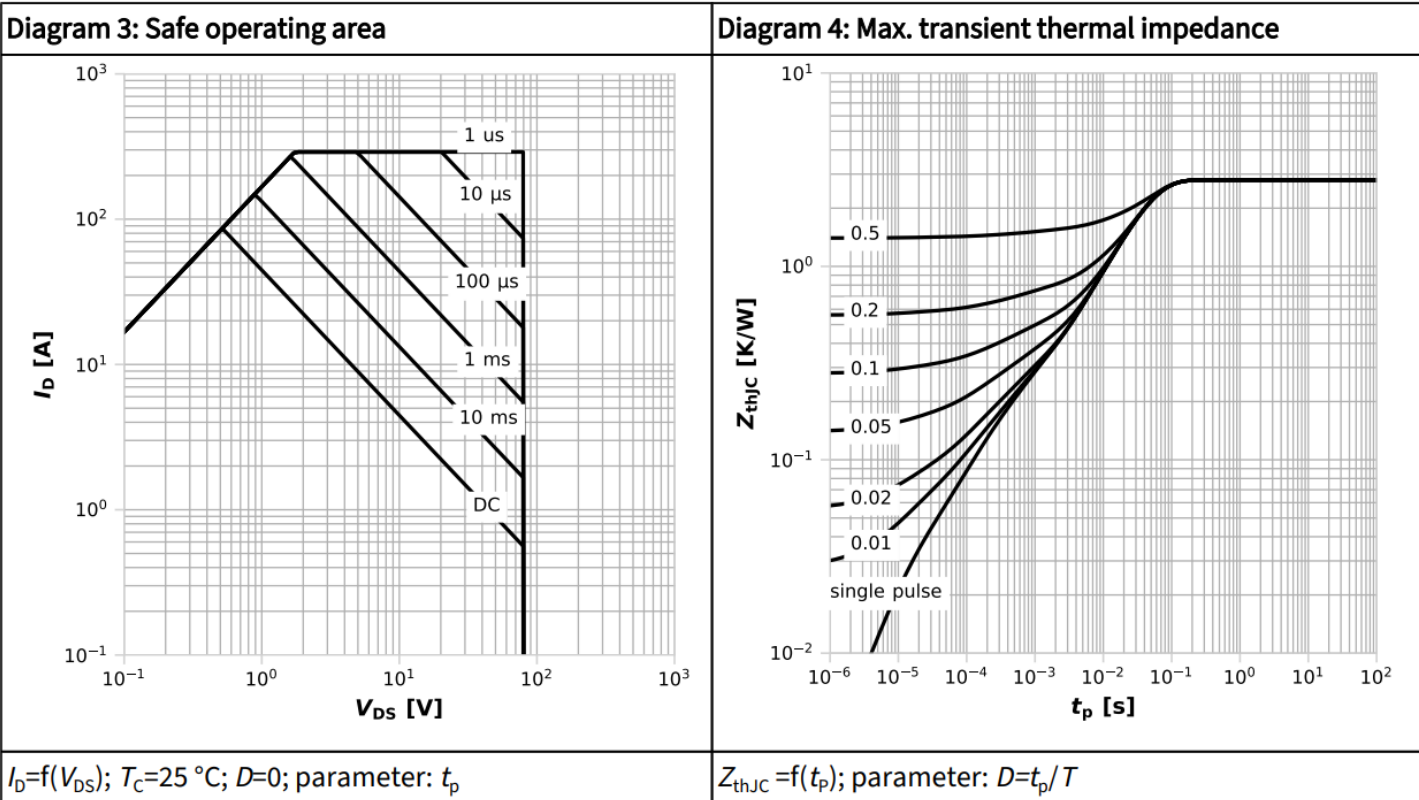
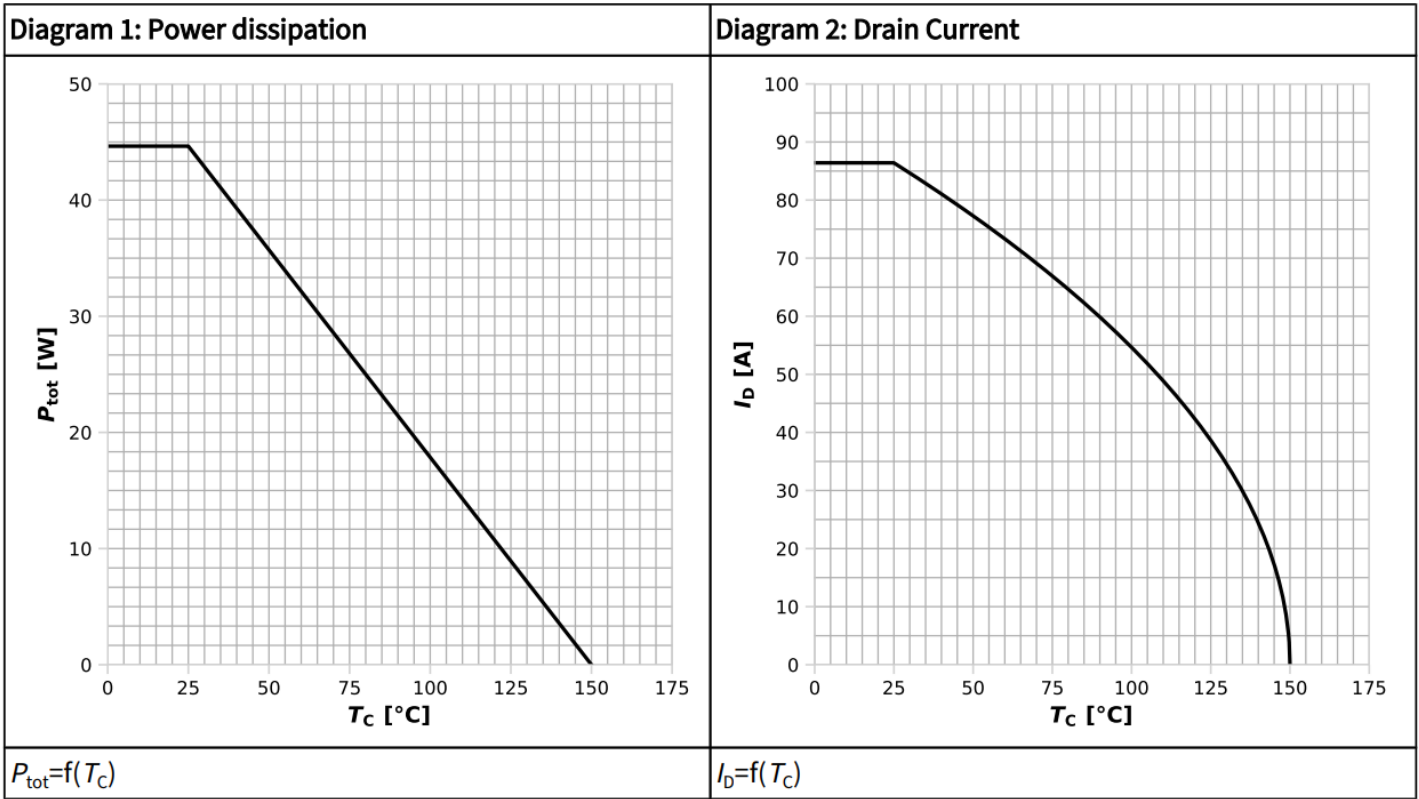
⁶⁾ 由设计定义。未经过生产测试。

Table 8 Reverse operation

Parameter	Symbol	Values			Unit	Note / Test condition
		Min.	Typ.	Max.		
Reverse continuous current	I_S	-	-	15	A	$T_C=25\text{ °C}$
Pulsed current, reverse	$I_{S,pulse}$			344		
Source-Drain reverse voltage	V_{SD}	-	2.6	3.4	V	$V_{GS}=0\text{ V}, I_{S,pulse}=25\text{ A}, T_J=25\text{ °C}$
			2.1	-		$V_{GS}=0\text{ V}, I_{S,pulse}=0.5\text{ A}, T_J=25\text{ °C}$
Reverse recovery charge ⁷⁾	Q_{rr}	-	0	-	nC	$V_R=40\text{ V}, I_{S,pulse}=25\text{ A}, di_{S,pulse}/dt=100\text{ A}/\mu\text{s}$

⁷⁾ 由设计定义。未经过生产测试。

5 电气特性图



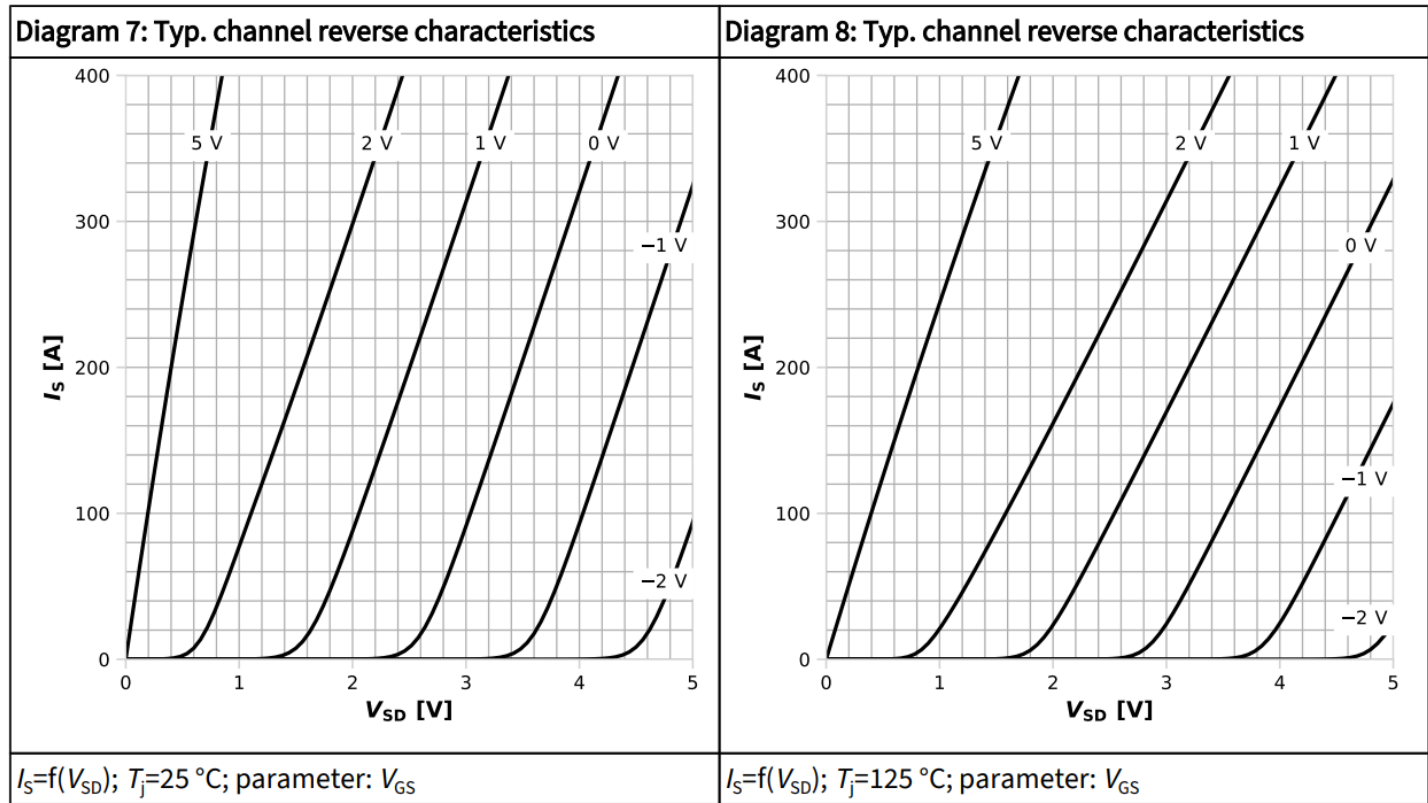
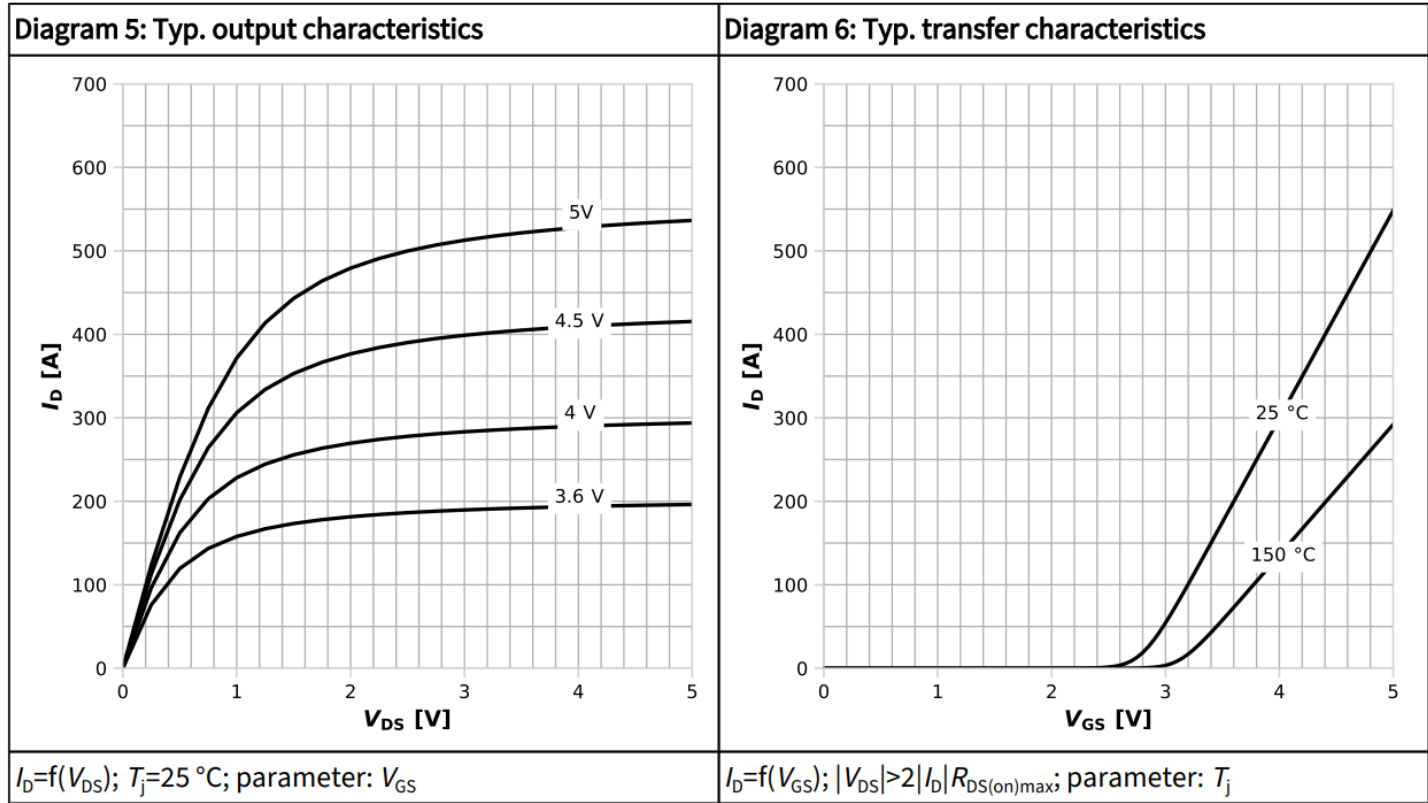
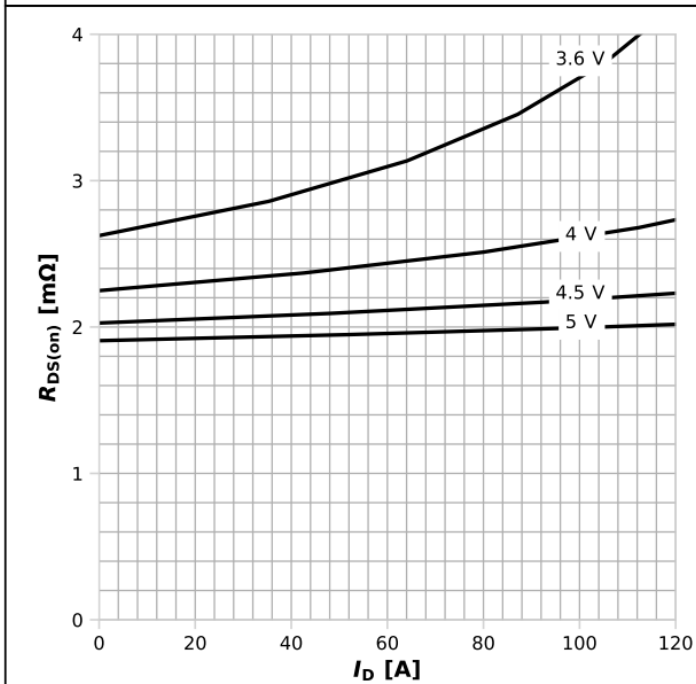
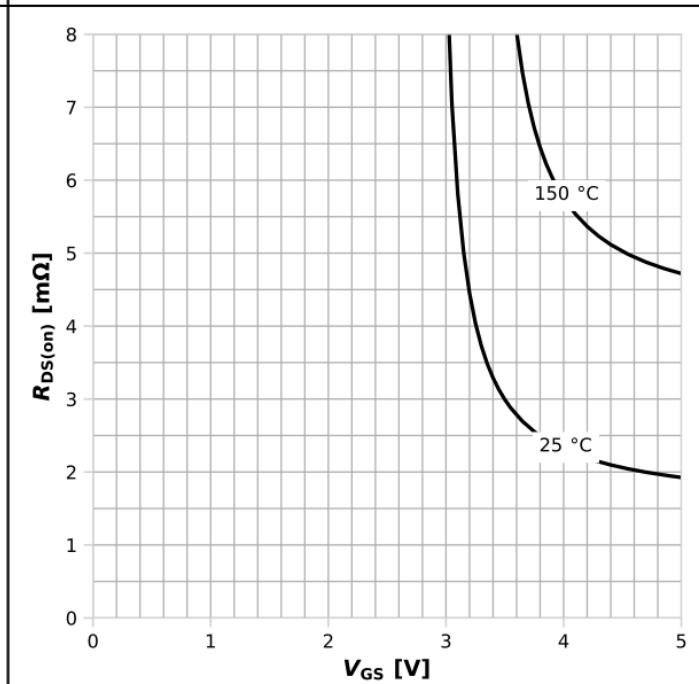


Diagram 9: Typ. drain-source on-state resistance



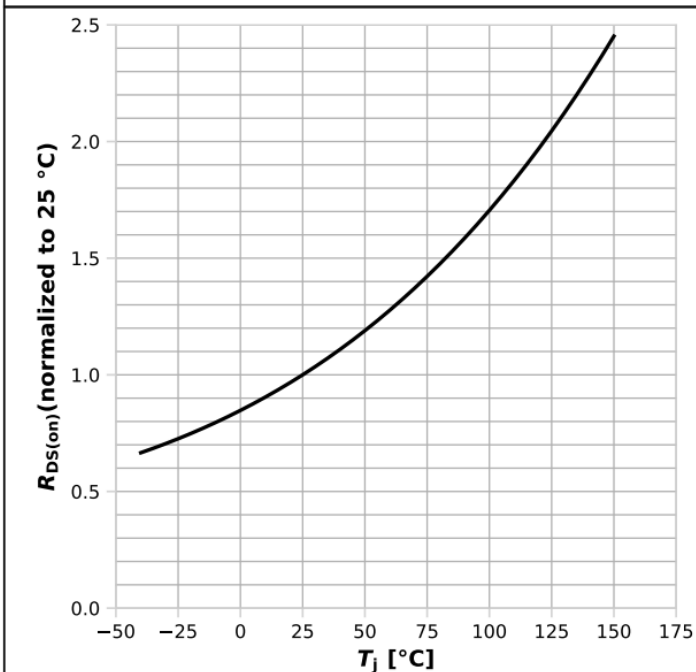
$$R_{DS(on)} = f(I_D); T_j = 25^\circ\text{C}; \text{parameter: } V_{GS}$$

Diagram 10: Typ. Drain-source on-state resistance



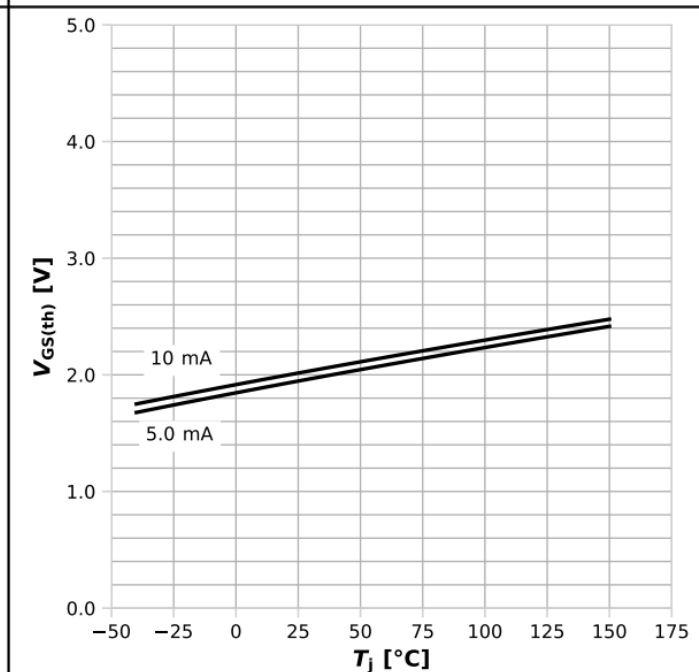
$$R_{DS(on)} = f(V_{GS}); I_D = 25\text{ A}; \text{parameter: } T_j$$

Diagram 11: Drain-source on-state resistance



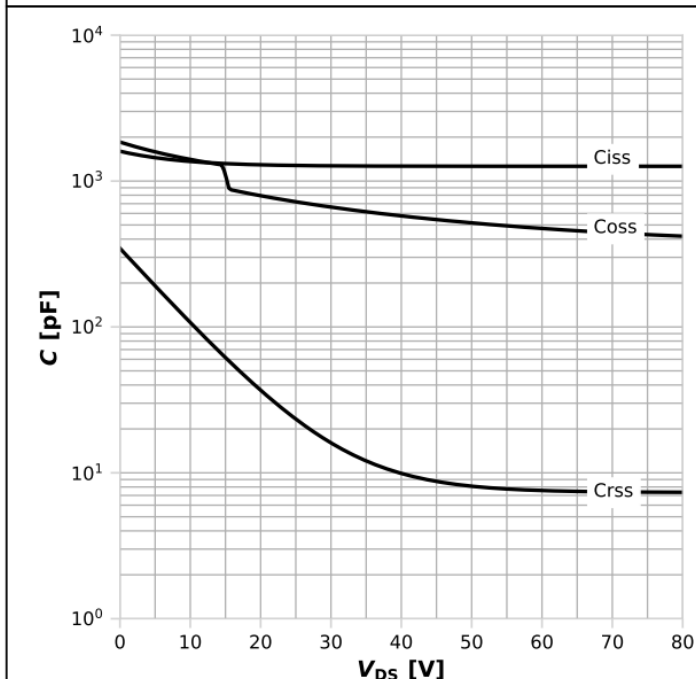
$$R_{DS(on)} = f(T_j); I_D = 25\text{ A}, V_{GS} = 5\text{ V}$$

Diagram 12: Typ. gate threshold voltage



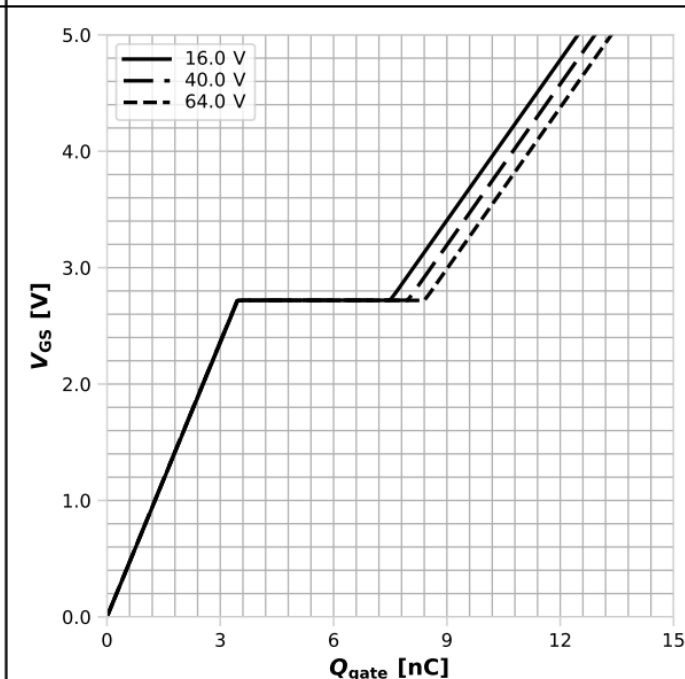
$$V_{GS(th)} = f(T_j), V_{GS} = V_{DS}; \text{parameter: } I_D$$

Diagram 13: Typ. capacitances



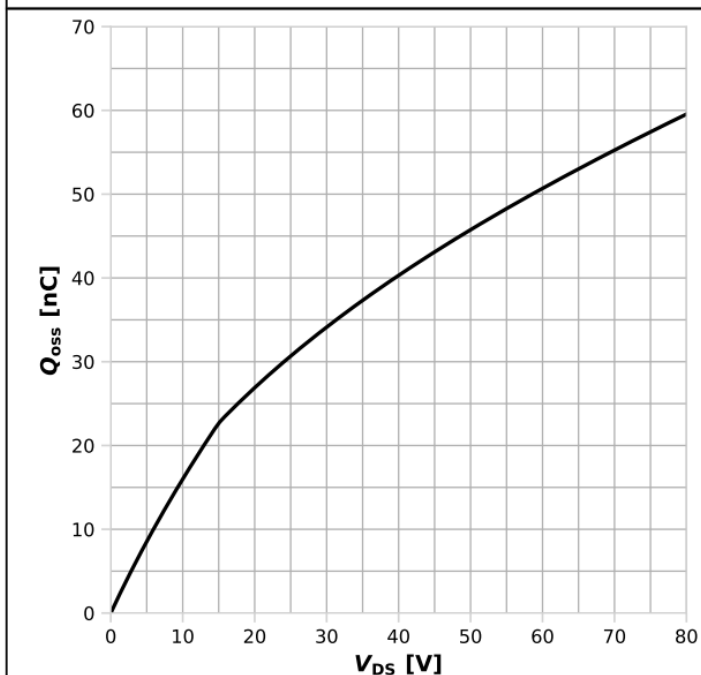
$$C=f(V_{DS}); V_{GS}=0\text{ V}$$

Diagram 14 Typ. gate charge



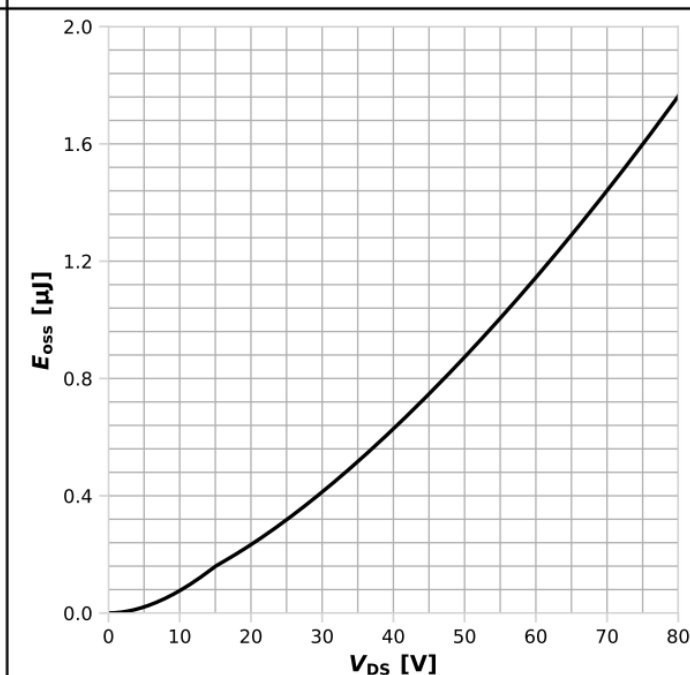
$$V_{GS}=f(Q_{gate}); I_D=25\text{ A pulsed; parameter: }V_{DS}$$

Diagram 15: Typ. output charge



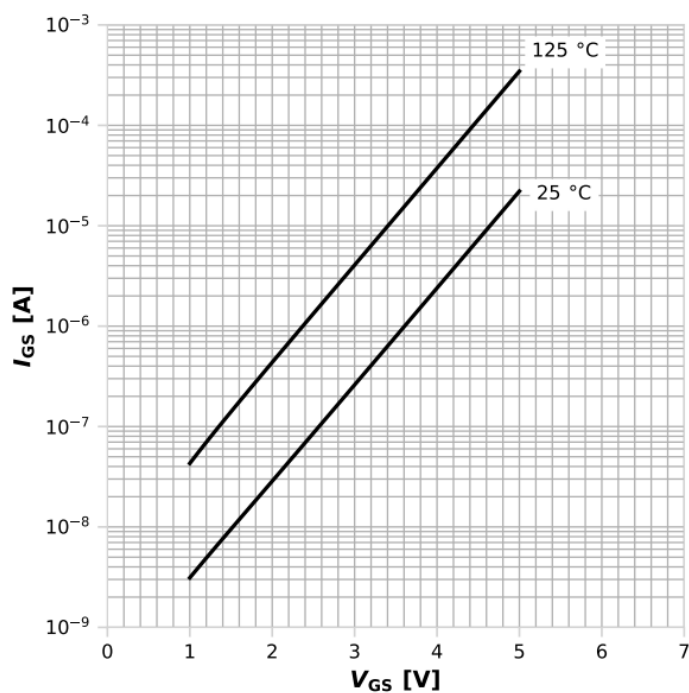
$$Q_{oss}=f(V_{DS}), V_{GS}=0\text{ V}$$

Diagram 16: Typ. Coss stored Energy

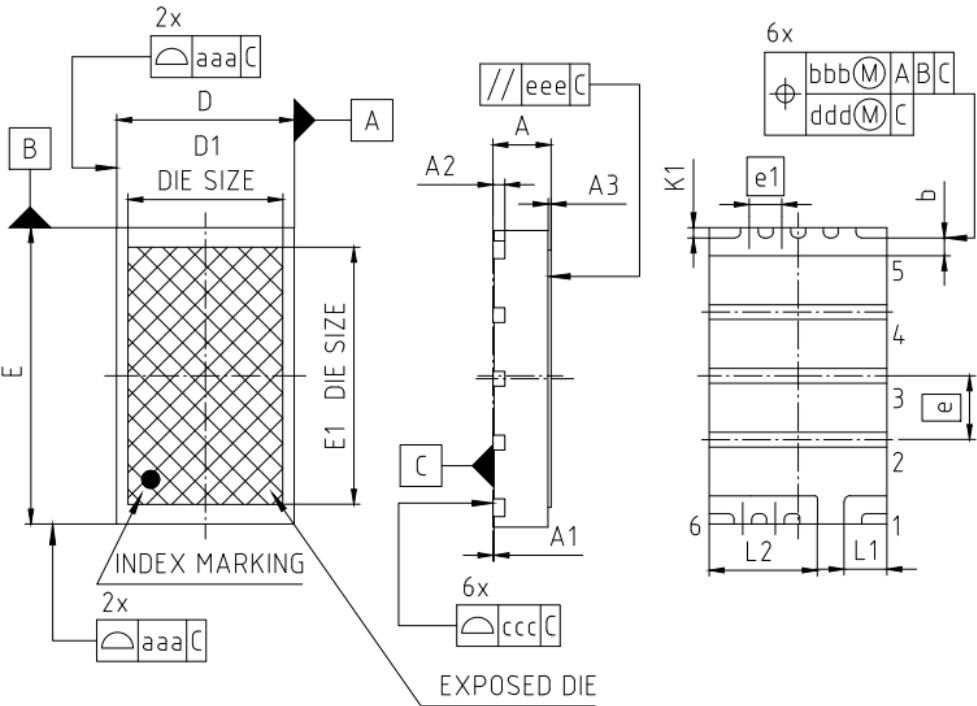


$$E_{oss}=f(V_{DS}), V_{GS}=0\text{ V}$$

Diagram 17: Typ. gate characteristics forward

 $I_{GS}=f(V_{GS}); V_{DS}=0 \text{ V}; \text{parameter: } T_j$

6 封装外形



PACKAGE - GROUP NUMBER: PG-TSON-6-U01		
DIMENSIONS	MILLIMETERS	
	MIN.	MAX.
A	-	1.032
A1	-	0.05
A2	0.20	
A3	-	0.05
b	0.18	0.30
D	2.90	3.10
D1	2.616	
E	4.90	5.10
E1	4.336	
e	1.075	
e1	0.55	
K1	0.125	0.225
L1	0.625	0.825
L2	1.725	1.925
aaa	0.05	
bbb	0.10	
ccc	0.08	
ddd	0.05	
eee	0.10	

NOTE:
DIMENSIONS DO NOT INCLUDE MOLD FLASH,
PROTRUSION OR GATE BURRS

图 1 PG- TSON- 6 外形图，尺寸单位为毫米

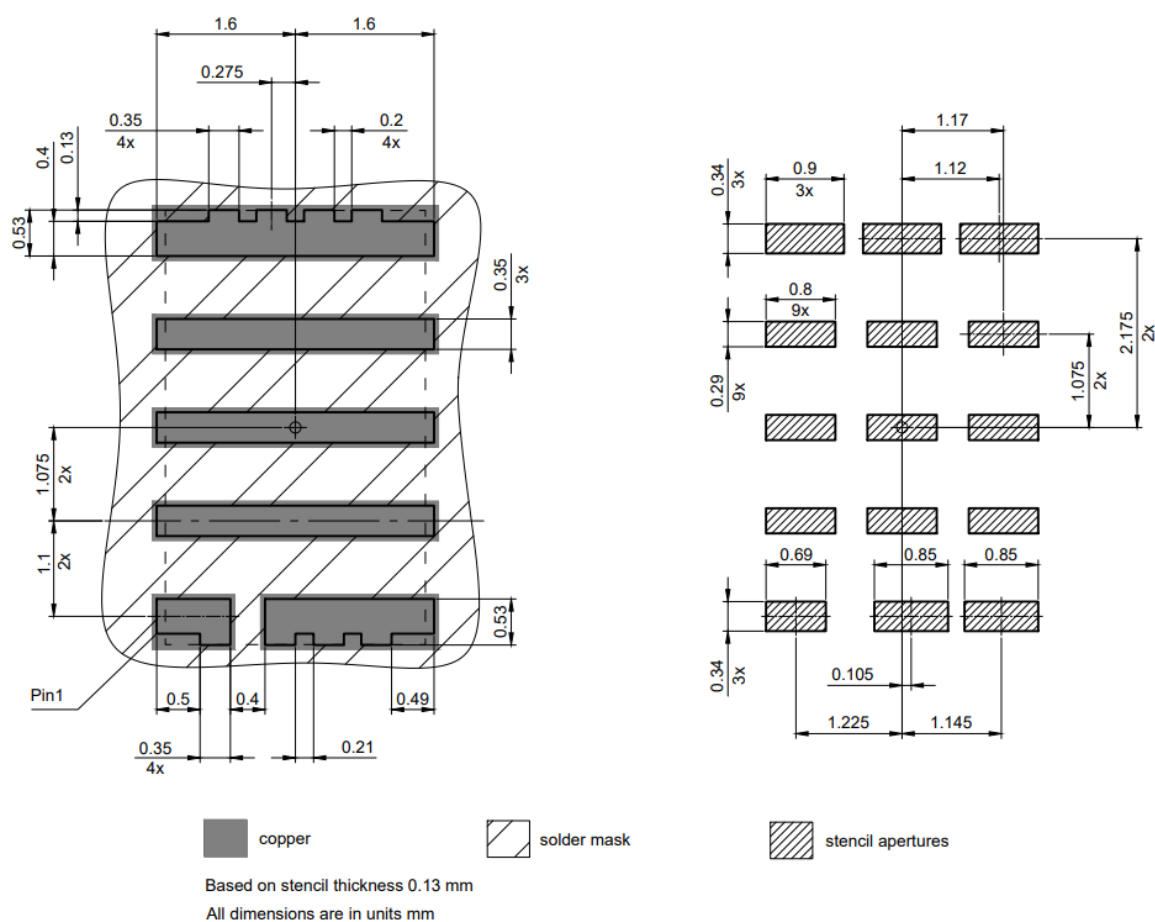


图 2 PG-TSON-6封装图，尺寸单位为毫米

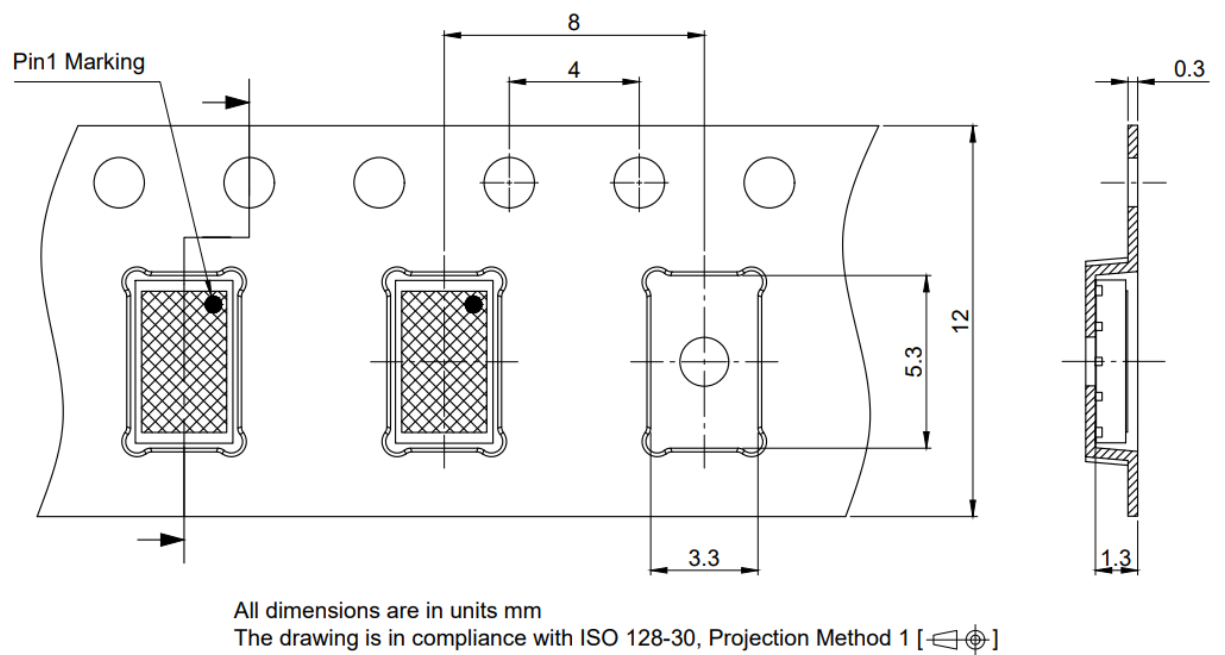


Figure 3 Packaging variant PG- TSON- 6 , dimensions in mm

7 附录A

表 9 相关链接

- [IFX CoolGaN™ GaN网页](#)
- [IFX CoolGaN™ 可靠性白皮书](#)
- [IFX CoolGaN™ 栅极驱动器应用手册](#)

- [IFX CoolGaN™ 评估板](#)
- [IFX 封装说明-PG-TSON-6-2](#)

修订记录

IGC025S08S1

Revision 2025 - 02 - 21 , Rev. 1 . 1

历史修订记录

Revision	Date	Subjects (major changes since last revision)
1.0	2025-01-28	Release of final version
1.1	2025-02-21	Diagram harmonization

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