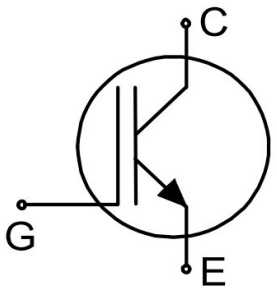
第五代高速开关系列

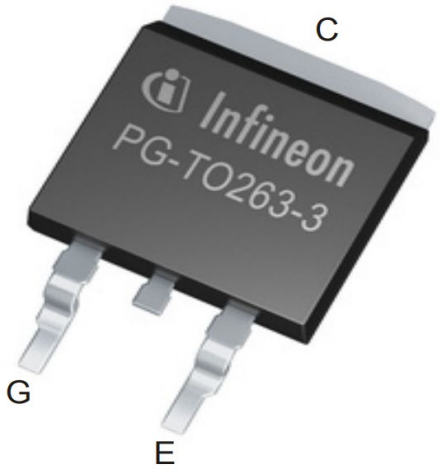
英飞凌 TRENCHSTOP™ 5 高速软开关 IGBT

特性和优点：



英飞凌高速 S5 技术提供：

- 高速平滑开关器件适用于硬开关和软开关应用
- 极低 V_{CEsat}，标称额定电流时为 1.35 V
- 即插即用，替代上一代 IGBT
- 650 V 击穿电压
- 低 Q_G
- 最高结温 175 °C
- 无铅引脚镀层；符合 RoHS 标准
- 完整的产品品类和 PSpice 模型：
http://www.infineon.com/igbt/



潜在应用：

- 能源生产
  - 太阳能组串逆变器
  - 太阳能微型逆变器
- 工业供电
  - 工业开关电源
  - 工业级不间断电源（UPS）
- 金属处理
  - 焊接
- 能源分配
  - 储能
- 基础设施 – 充电
  - 充电器

产品验证

符合 JEDEC47/20/22 相关测试的工业应用要求

关键性能和封装参数

Type	V_{CE}	I_C	V_{CEsat}, T_{vj}=25°C	T_{vjmax}	Marking	Package
IGB20N65S5	650V	20A	1.35V	175°C	G20ES5	PG-TO263-3

本数据手册的原文使用英文撰写。为方便起见，英飞凌提供了译文；由于翻译过程中可能使用了自动化工具，英飞凌不保证译文的准确性。为确认准确性，请务必访问 infineon.com 参考最新的英文版本（控制文档）。

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最大额定值

注意：为了获得最佳的使用寿命和可靠性，英飞凌建议运行条件不超过本数据手册中所述最大额定值的 80%。

Parameter	Symbol	Value	Unit
Collector-emitter voltage, $T_{vj} \geq 25°C$	V_{CE}	650	V
DC collector current, limited by T_{vjmax} $T_c = 25°C$ value limited by bondwire $T_c = 100°C$	I_C	 40.0 28.0	A
Pulsed collector current, t_p limited by T_{vjmax}	I_{Cpuls}	80.0	A
Turn off safe operating area $V_{CE} \leq 650V$, $T_{vj} \leq 175°C$, $t_p = 1\mu s$	-	80.0	A
Gate-emitter voltage Transient Gate-emitter voltage ($t_p \leq 10\mu s$, $D < 0.010$)	V_{GE}	±20 ±30	V
Power dissipation $T_c = 25°C$ Power dissipation $T_c = 100°C$	P_{tot}	125.0 62.5	W
Operating junction temperature	T_{vj}	-40...+175	°C
Storage temperature	T_{stg}	-55...+150	°C
Soldering temperature, reflow soldering (MSL1 according to JEDEC J-STA-020)		 260	°C

热阻

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
R_{th} Characteristics						
IGBT thermal resistance, junction - case	$R_{th(j-c)}$		-	-	1.20	K/W
Thermal resistance, min. footprint junction - ambient	$R_{th(j-a)}$		-	-	65	K/W
Thermal resistance, 6cm² Cu on PCB junction - ambient	$R_{th(j-a)}$		-	-	40	K/W

电气特性，测于 $T_{vj} = 25°C$的条件下，除非另有规定

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Static Characteristic						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{GE} = 0V$, $I_C = 0.20mA$	650	-	-	V
Collector-emitter saturation voltage	V_{CEsat}	$V_{GE} = 15.0V$, $I_C = 20.0A$ $T_{vj} = 25°C$ $T_{vj} = 125°C$ $T_{vj} = 175°C$	 - - -	 1.35 1.50 1.60	 1.70 - -	V
Gate-emitter threshold voltage	$V_{GE(th)}$	$I_C = 0.20mA$, $V_{CE} = V_{GE}$	3.2	4.0	4.8	V
Zero gate voltage collector current	I_{CES}	$V_{CE} = 650V$, $V_{GE} = 0V$ $T_{vj} = 25°C$ $T_{vj} = 175°C$	 - -	 - 1400	 50 -	µA
Gate-emitter leakage current	I_{GES}	$V_{CE} = 0V$, $V_{GE} = 20V$	-	-	100	nA
Transconductance	g_{fs}	$V_{CE} = 20V$, $I_C = 20.0A$	-	28.0	-	S

电气特性，测于 T_{vj}= 25°C 的条件下，除非另有规定

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Dynamic Characteristic						
Input capacitance	C_{ies}	V_{CE} = 25V, V_{GE} = 0V, f = 1MHz	-	1200	-	pF
Output capacitance	C_{oes}		-	24	-	
Reverse transfer capacitance	C_{res}		-	5	-	
Gate charge	Q_G	V_{CC} = 520V, I_C = 20.0A, V_{GE} = 15V	-	48.0	-	nC
Internal emitter inductance measured 5mm (0.197 in.) from case	L_E		-	7.0	-	nH

开关特性、感性负载

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
IGBT Characteristic, at T_{vj} = 25°C						
Turn-on delay time	$t_{d(on)}$	T_{vj} = 25°C, V_{CC} = 400V, I_C = 20.0A, V_{GE} = 0.0/15.0V, $R_{G(on)}$ = 20.0Ω, $R_{G(off)}$ = 20.0Ω, Lσ = 30nH, Cσ = 30pF Lσ, Cσ from Fig. E Energy losses include “tail” and diode reverse recovery.	-	13	-	ns
Rise time	t_r		-	14	-	ns
Turn-off delay time	$t_{d(off)}$		-	115	-	ns
Fall time	t_f		-	22	-	ns
Turn-on energy	E_{on}		-	0.36	-	mJ
Turn-off energy	E_{off}		-	0.15	-	mJ
Total switching energy	E_{ts}		-	0.51	-	mJ
Turn-on delay time	$t_{d(on)}$	T_{vj} = 25°C, V_{CC} = 400V, I_C = 10.0A, V_{GE} = 0.0/15.0V, $R_{G(on)}$ = 20.0Ω, $R_{G(off)}$ = 20.0Ω, Lσ = 30nH, Cσ = 30pF Lσ, Cσ from Fig. E Energy losses include “tail” and diode reverse recovery.	-	12	-	ns
Rise time	t_r		-	8	-	ns
Turn-off delay time	$t_{d(off)}$		-	129	-	ns
Fall time	t_f		-	22	-	ns
Turn-on energy	E_{on}		-	0.19	-	mJ
Turn-off energy	E_{off}		-	0.08	-	mJ
Total switching energy	E_{ts}		-	0.27	-	mJ

开关特性、感性负载

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
IGBT Characteristic, at T_{vj} = 150°C						
Turn-on delay time	$t_{d(on)}$	T_{vj} = 150°C, V_{CC} = 400V, I_C = 20.0A, V_{GE} = 0.0/15.0V, $R_{G(on)}$ = 20.0Ω, $R_{G(off)}$ = 20.0Ω, Lσ = 30nH, Cσ = 30pF Lσ, Cσ from Fig. E Energy losses include “tail” and diode reverse recovery.	-	13	-	ns
Rise time	t_r		-	15	-	ns
Turn-off delay time	$t_{d(off)}$		-	140	-	ns
Fall time	t_f		-	48	-	ns
Turn-on energy	E_{on}		-	0.48	-	mJ
Turn-off energy	E_{off}		-	0.27	-	mJ
Total switching energy	E_{ts}		-	0.75	-	mJ
Turn-on delay time	$t_{d(on)}$	T_{vj} = 150°C, V_{CC} = 400V, I_C = 10.0A, V_{GE} = 0.0/15.0V, $R_{G(on)}$ = 20.0Ω, $R_{G(off)}$ = 20.0Ω, Lσ = 30nH, Cσ = 30pF Lσ, Cσ from Fig. E Energy losses include “tail” and diode reverse recovery.	-	11	-	ns
Rise time	t_r		-	9	-	ns
Turn-off delay time	$t_{d(off)}$		-	155	-	ns
Fall time	t_f		-	52	-	ns
Turn-on energy	E_{on}		-	0.26	-	mJ
Turn-off energy	E_{off}		-	0.15	-	mJ
Total switching energy	E_{ts}		-	0.41	-	mJ

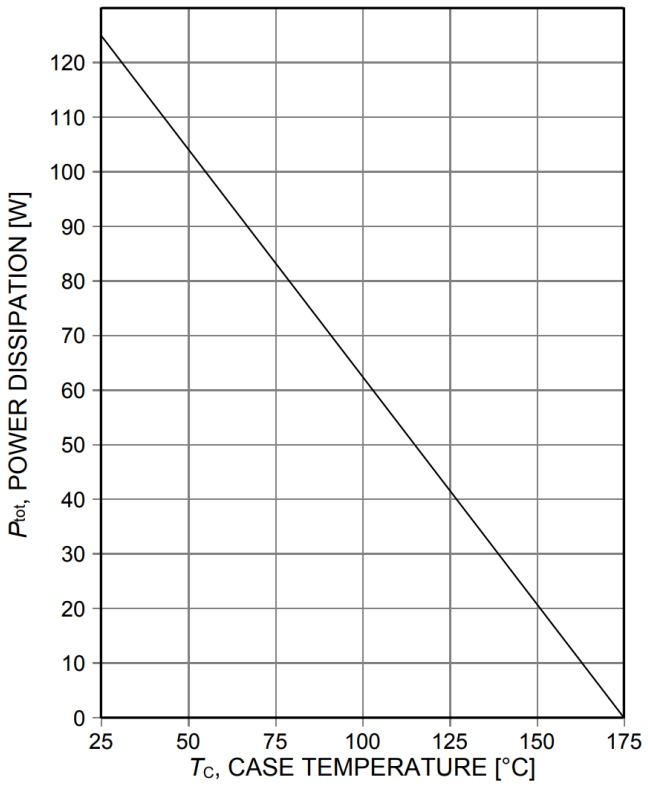


Figure 1. **Power dissipation as a function of case temperature**
(T_{vj}≤175°C)

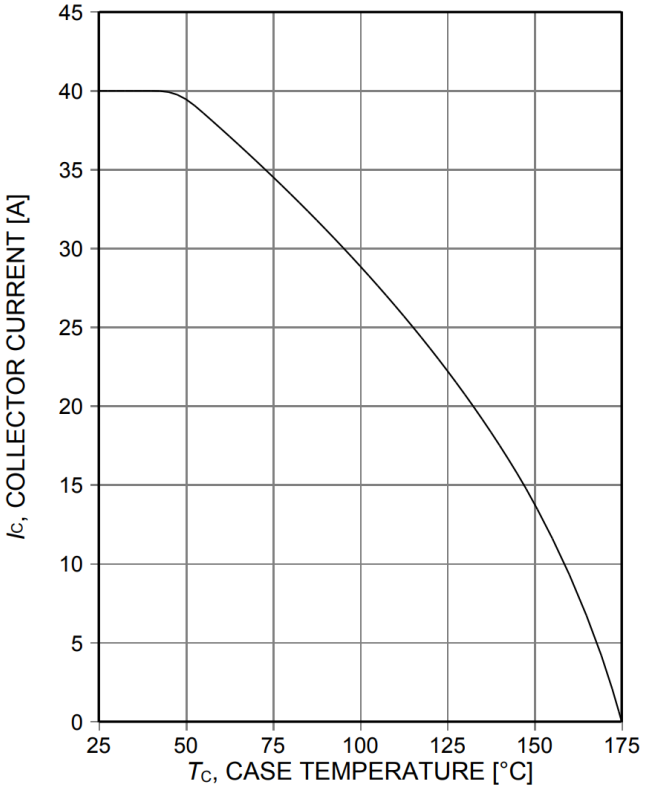


Figure 2. **Collector current as a function of case temperature**
(V_{GE}≥15V, T_{vj}≤175°C)

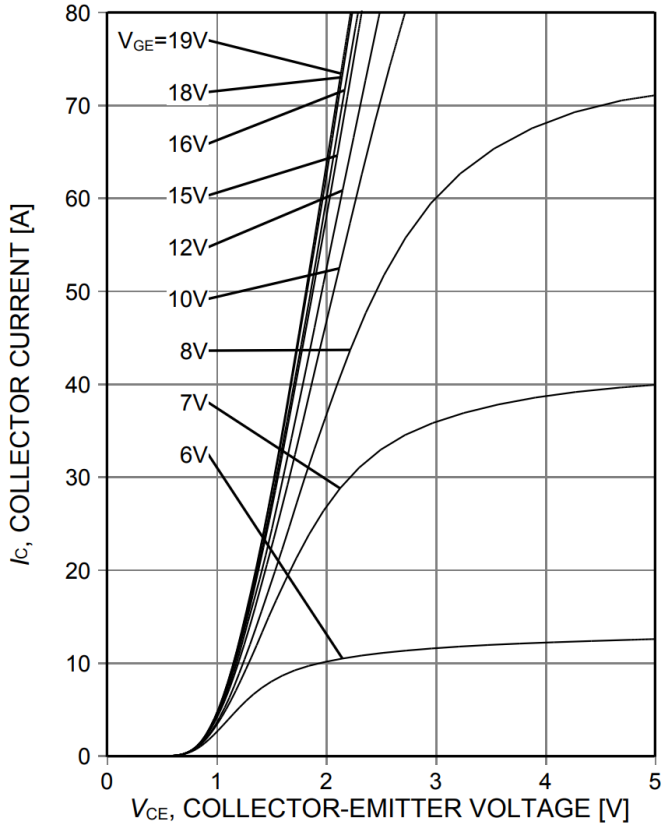


Figure 3. **Typical output characteristic**
(T_{vj}=25°C)

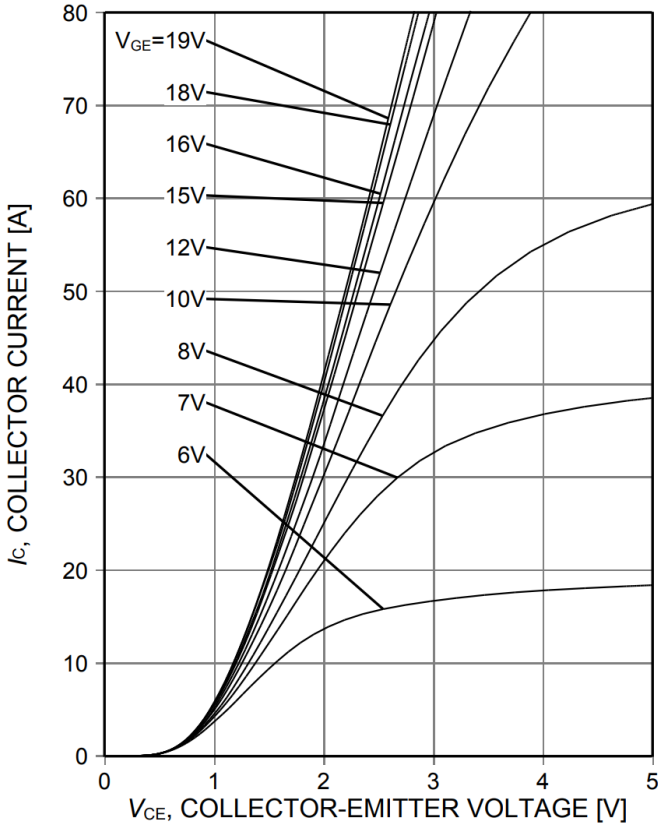


Figure 4. **Typical output characteristic**
(T_{vj}=175°C)

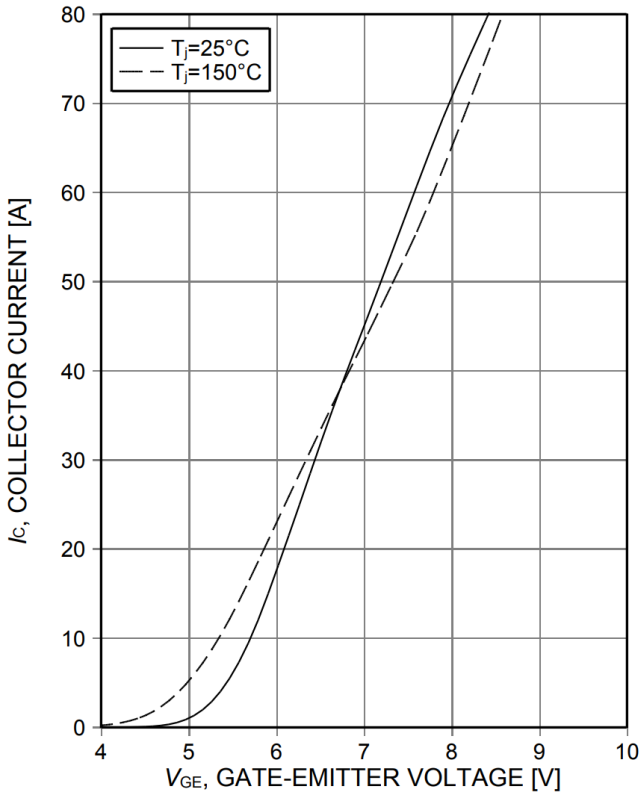


Figure 5. **Typical transfer characteristic**
(V_{CE}=20V)

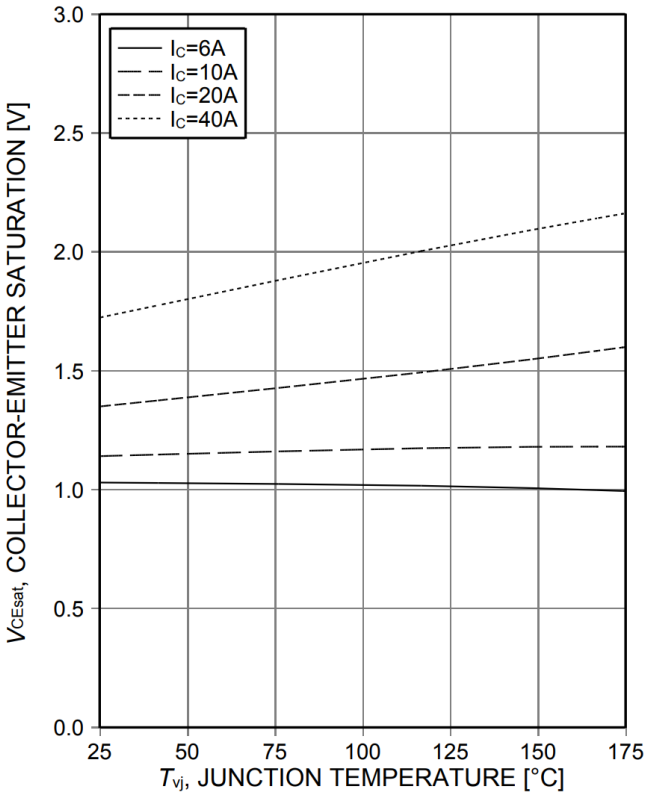


Figure 6. **Typical collector-emitter saturation voltage as a function of junction temperature**
(V_{GE}=15V)

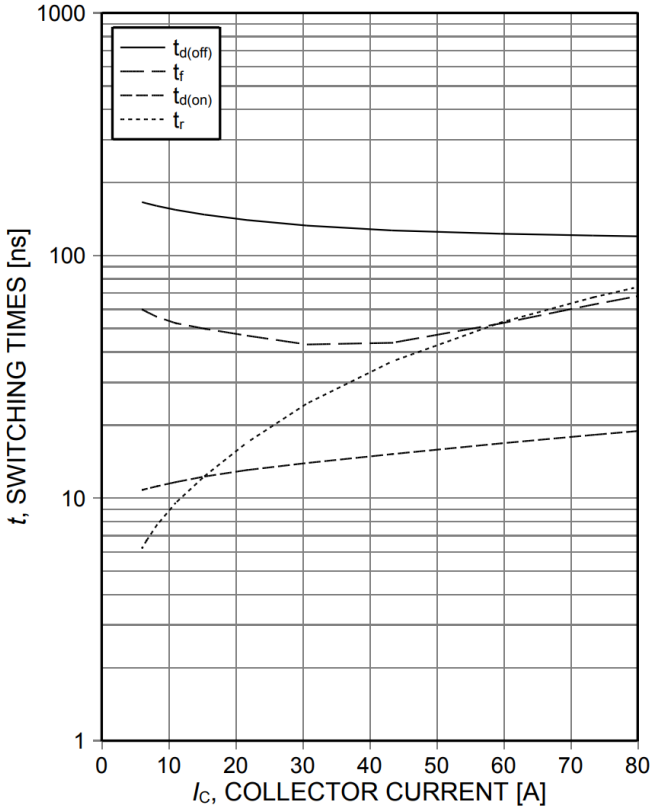


Figure 7. **Typical switching times as a function of collector current**
(inductive load, T_{vj}=150°C, V_{CE}=400V, V_{GE}=0/15V, R_G=20Ω, Dynamic test circuit in Figure E)

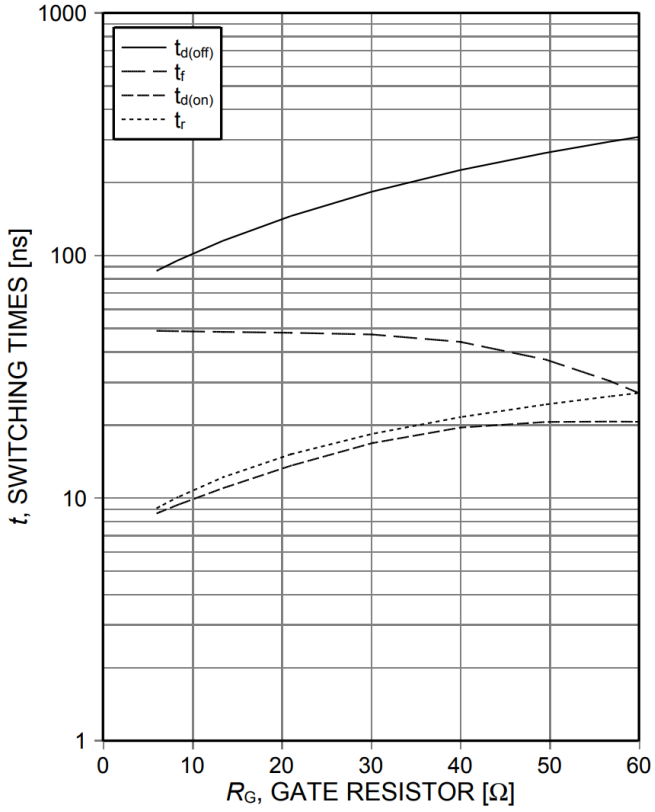


Figure 8. **Typical switching times as a function of gate resistor**
(inductive load, T_{vj}=150°C, V_{CE}=400V, V_{GE}=0/15V, I_C=20A, Dynamic test circuit in Figure E)

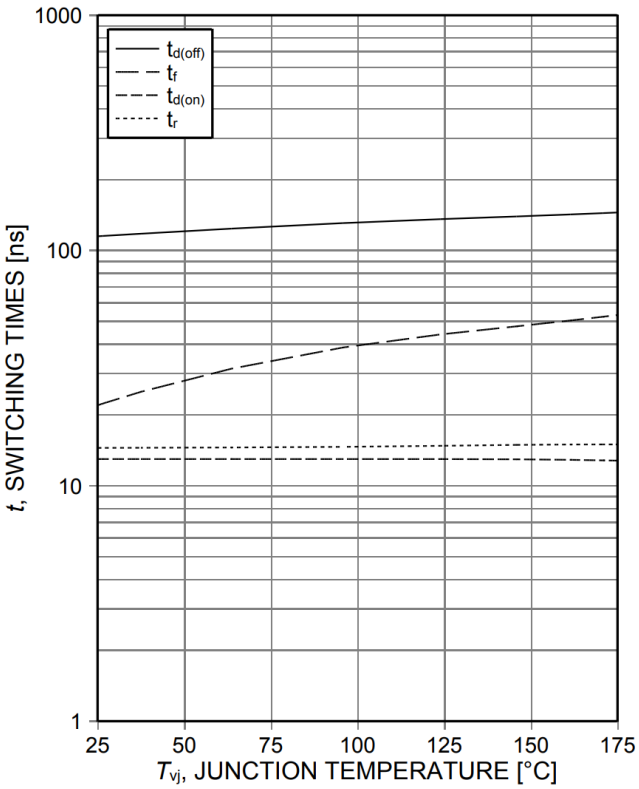


Figure 9. **Typical switching times as a function of junction temperature** (inductive load, V_{CE}=400V, V_{GE}=0/15V, I_C=20A, R_G=20Ω, Dynamic test circuit in Figure E)

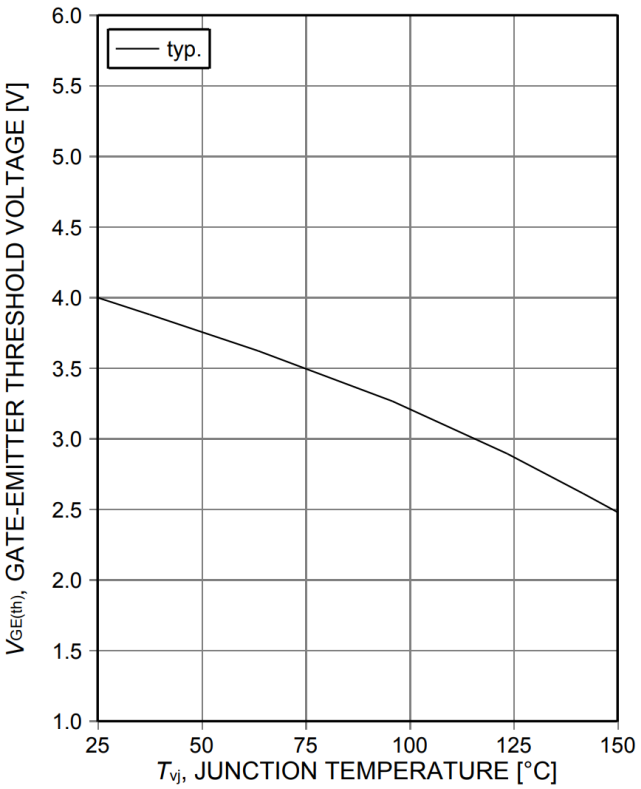


Figure 10. **Gate-emitter threshold voltage as a function of junction temperature** (I_C=0.2mA)

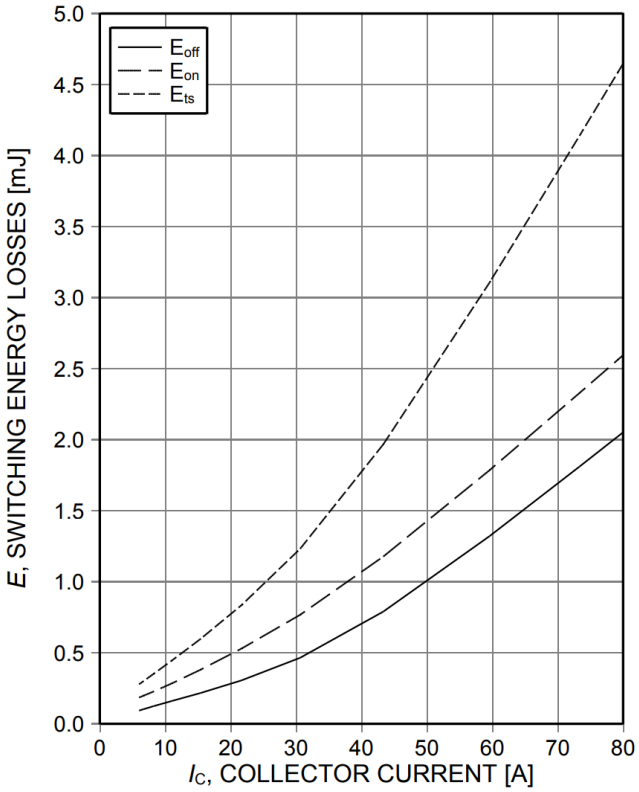


Figure 11. **Typical switching energy losses as a function of collector current** (inductive load, T_{vj}=150°C, V_{CE}=400V, V_{GE}=0/15V, R_G=20Ω, Dynamic test circuit in Figure E)

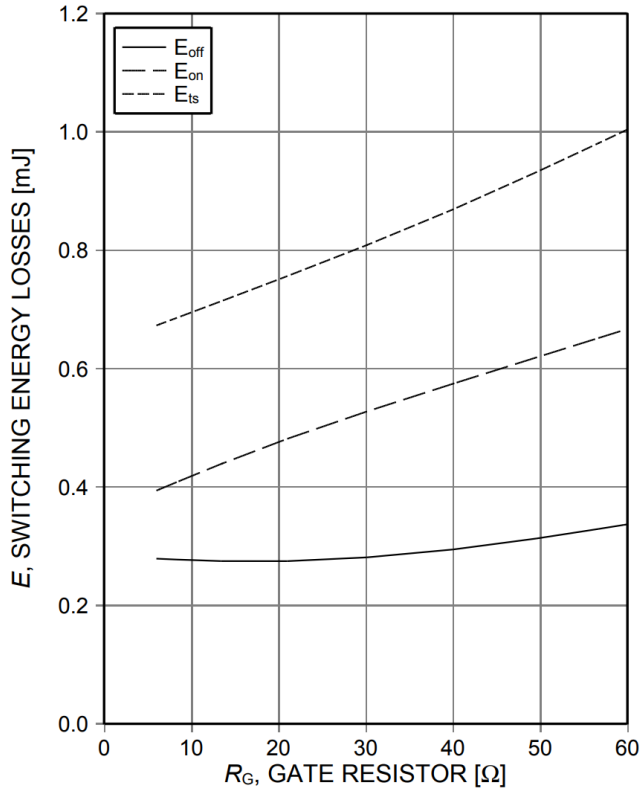


Figure 12. **Typical switching energy losses as a function of gate resistor** (inductive load, T_{vj}=150°C, V_{CE}=400V, V_{GE}=0/15V, I_C=20A, Dynamic test circuit in Figure E)

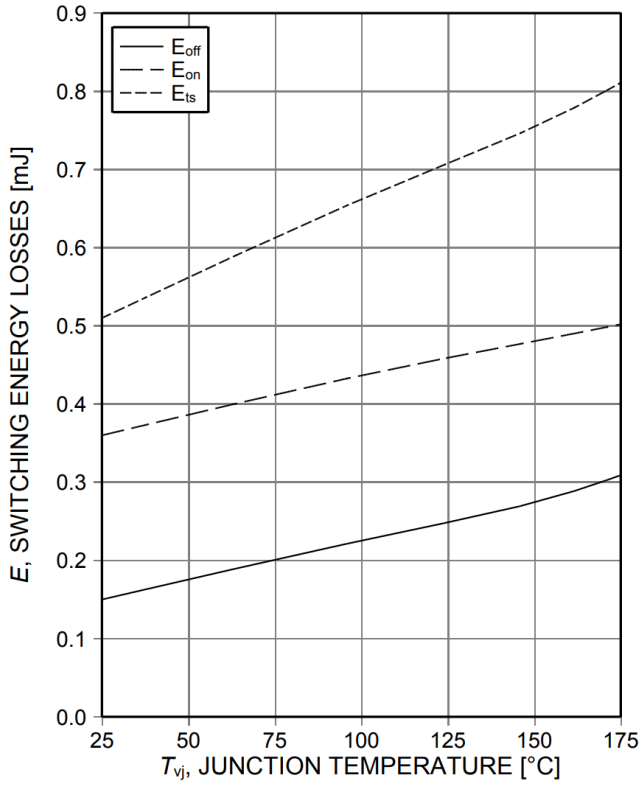


Figure 13. **Typical switching energy losses as a function of junction temperature** (inductive load, V_{CE}=400V, V_{GE}=0/15V, I_C=20A, R_G=20Ω, Dynamic test circuit in Figure E)

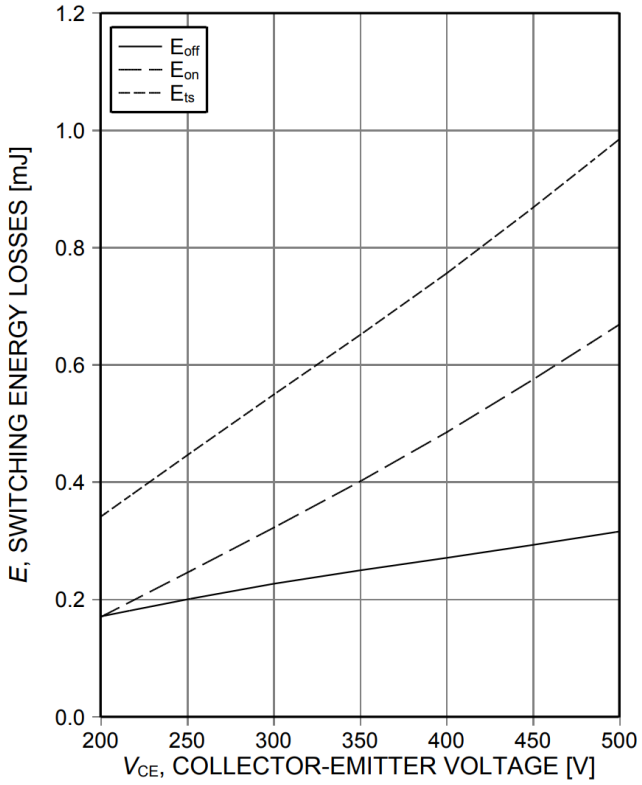


Figure 14. **Typical switching energy losses as a function of collector emitter voltage** (inductive load, T_{vj}=150°C, V_{GE}=0/15V, I_C=20A, R_G=20Ω, Dynamic test circuit in Figure E)

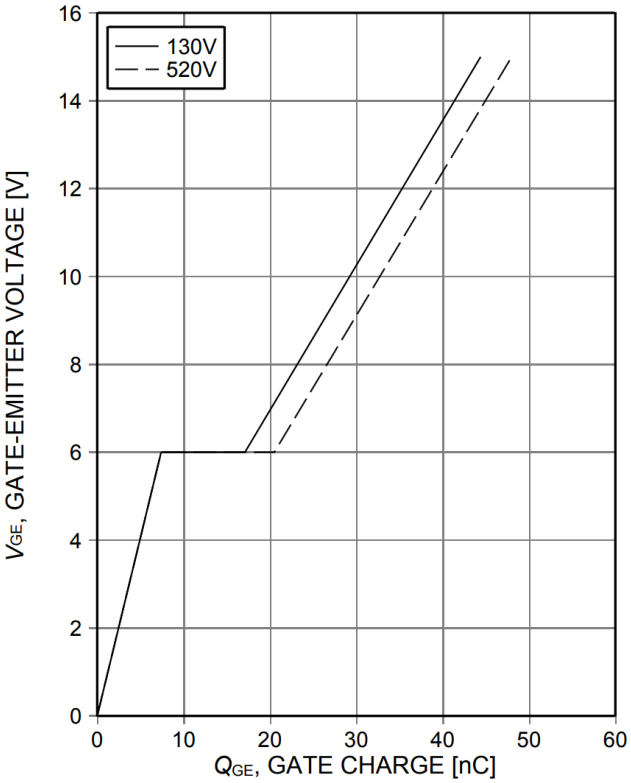


Figure 15. **Typical gate charge** (I_C=20A)

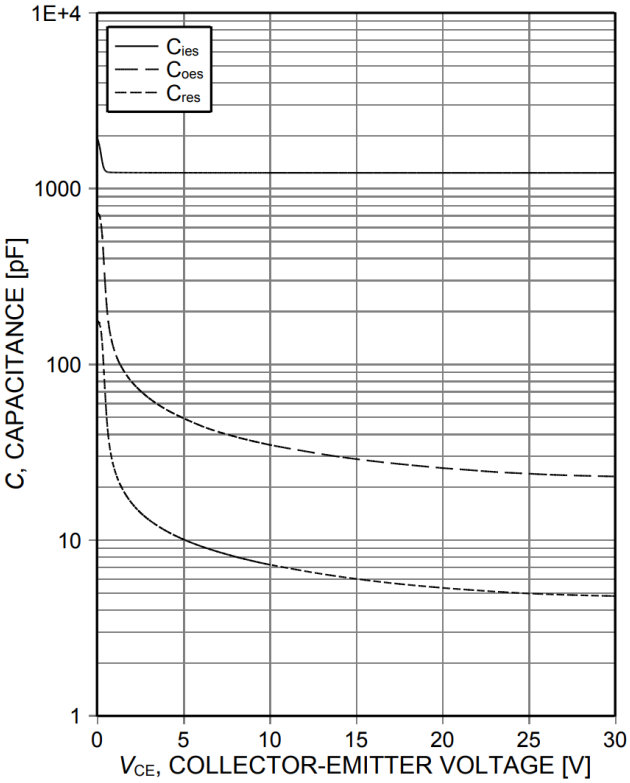


Figure 16. **Typical capacitance as a function of collector-emitter voltage** (V_{GE}=0V, f=1MHz)

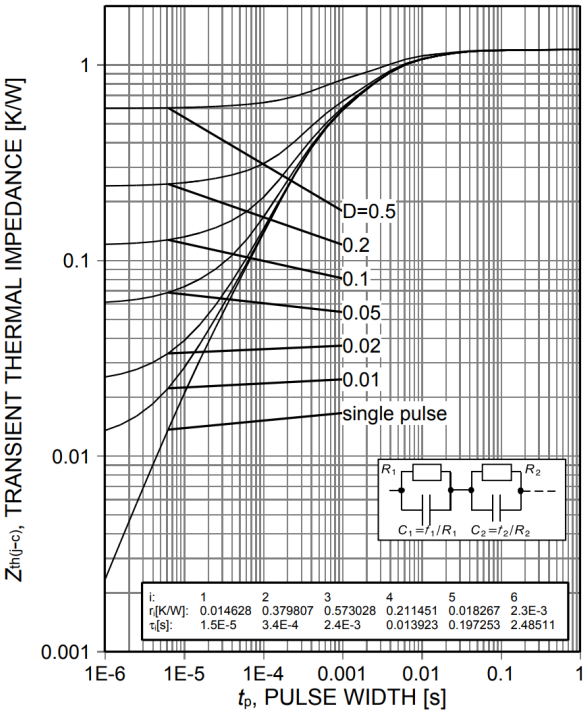
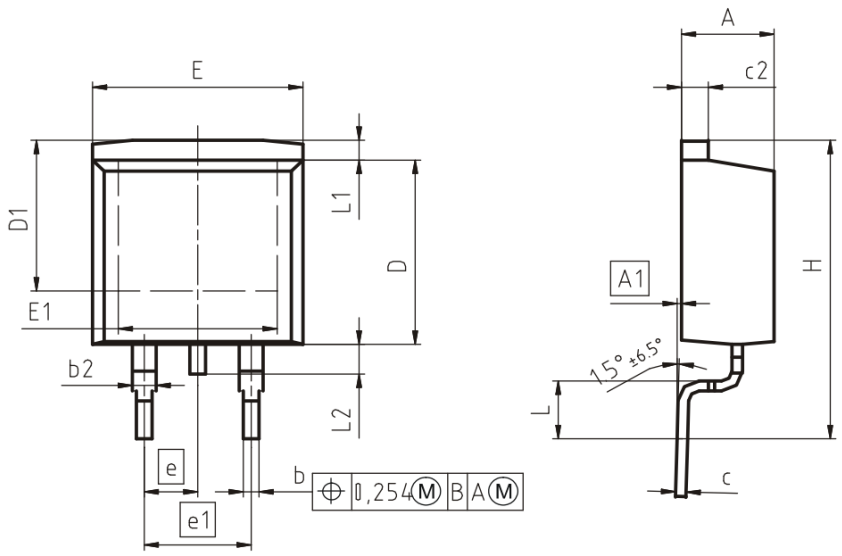
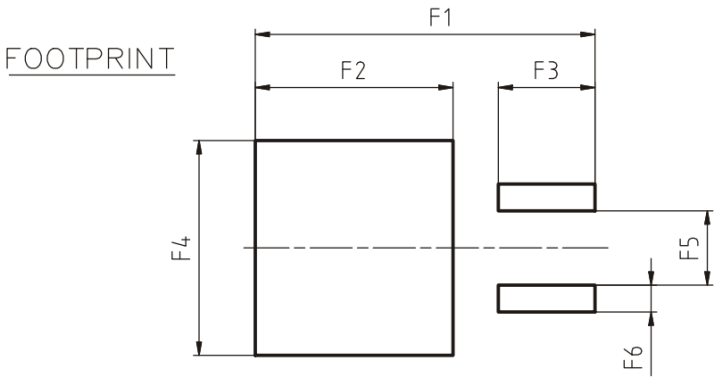


Figure 17. **IGBT transient thermal impedance**
(D=t_p/T)

封装图 PG-TO263-3





DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.30	4.57	0.169	0.180
A1	0.00	0.25	0.000	0.010
b	0.65	0.85	0.026	0.033
b2	0.95	1.15	0.037	0.045
c	0.33	0.65	0.013	0.026
c2	1.17	1.40	0.046	0.055
D	8.51	9.45	0.335	0.372
D1	7.10	7.90	0.280	0.311
E	9.80	10.31	0.386	0.406
E1	6.50	8.60	0.256	0.339
e	2.54		0.100	
e1	5.08		0.200	
N	2		2	
H	14.61	15.88	0.575	0.625
L	2.29	3.00	0.090	0.118
L1	0.70	1.60	0.028	0.063
L2	1.00	1.78	0.039	0.070
F1	16.05	16.25	0.632	0.640
F2	9.30	9.50	0.366	0.374
F3	4.50	4.70	0.177	0.185
F4	10.70	10.90	0.421	0.429
F5	3.65	3.85	0.144	0.152
F6	1.25	1.45	0.049	0.057

DOCUMENT NO.
Z8B00003324

SCALE
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7.5mm

EUROPEAN PROJECTION

ISSUE DATE
30-08-2007

REVISION
01

测试条件

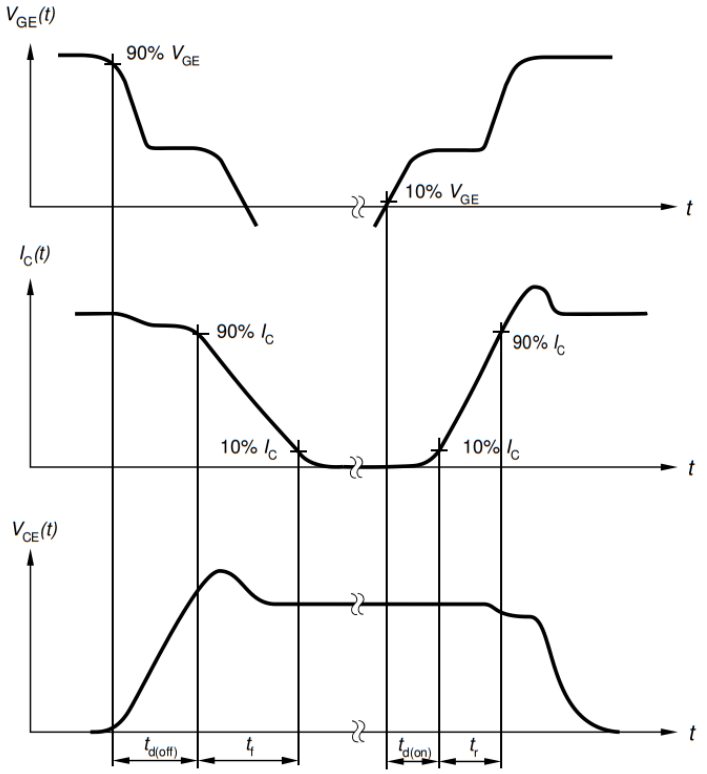


Figure A. **Definition of switching times**

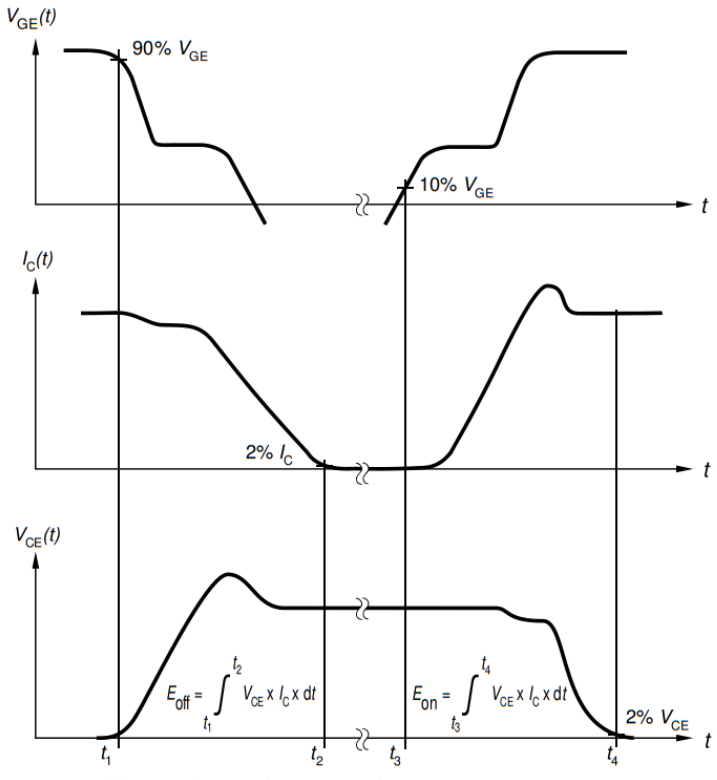


Figure B. **Definition of switching losses**

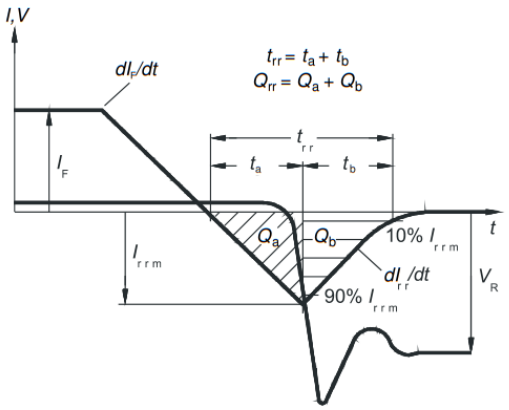


Figure C. **Definition of diode switching characteristics**

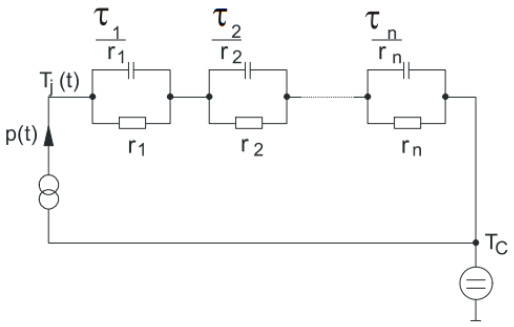


Figure D. **Thermal equivalent circuit**

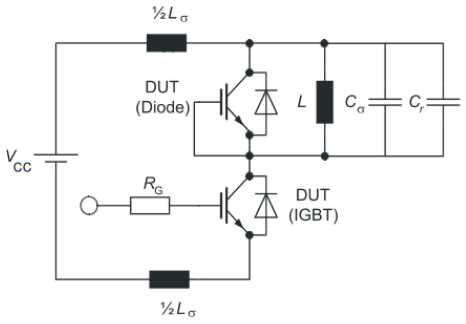


Figure E. **Dynamic test circuit**
Parasitic inductance L_σ,
parasitic capacitor C_σ,
relief capacitor C_r,
(only for ZVT switching)

第五代高速开关系列

修订记录

IGB20N65S5

Revision: 2018-01-11, Rev. 2.2

Previous Revision

Revision	Date	Subjects (major changes since last revision)
2.1	2017-05-19	Final data sheet
2.2	2018-01-11	Remove of Pb-free symbol and editorial changes.

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