# Product Qualification Report

# XDPP1188-200C

Digital power supply controller for PWM and PFM applications

## Description

This product qualification report describes the characteristics of the product with respect to quality and reliability.

The qualification sample selection was done on production lots which were manufactured and tested on standard production processes and meet the defined requirements.

The qualification test results of those products as outlined in this document are based on **JEDEC** for target applications and may reference existing qualification results of similar products. Such referencing is justified by the structural similarity of the products.

## Qualification Assessment

Fully qualified according to **JEDEC** for **Industrial Applications** and assessed as PASS

For further information about comparable products, please contact the nearest Infineon Technologies office (www.infineon.com).

**XDPP1180-200C** **qualified since April 2026**
**PG-VQFN-40**
**MSL: 1; 260°C**

**Electrical Stress Test Results:**

| Test Description | Abbr. | Condition | Duration | Lots/SS | Fail/Qty | Result |
|---|---|---|---|---|---|---|
| High Temperature Storage Life<br>JESD22 A103 | HTSL | Ta ≥ 150°C | 1000 h | 3 x 77 | 0 / 231 | PASS |
| High Temperature Operating Life<br>JESD22 A108 | HTOL | Ta = 125°C<br>$V_{stress} = V_{max}$ | 2000h | 6 x 77 | 0 / 462 | PASS |
| Latch-up<br>JESD78 | LU | Class 2 | | | | PASS |
| ESD (HBM)<br>JESD22-A114 | HBM | Class 2<br>(2000V to <4000V) | | | | PASS |
| ESD (CDM)<br>JESD22-C101 | CDM | Class C3<br>(>1000 V) | | | | PASS |

**Environmental Stress Test Results:**

| Test Description | Abbr. | Condition | Duration | Lots/SS | Fail/Qty | Result |
|---|---|---|---|---|---|---|
| Pre-conditioning<br>J-STD020 / JESD22 A113 | PC | MSL and 3 x reflow | | 7 x 77 | 0 / 539 | PASS |
| Temperature Cycling<br>JESD22 A104 | TC* | -65°C to +150°C | 1000 cyc | 4 x 77 | 0 / 308 | PASS |
| Biased Highly Accelerated Stress Test<br>JESD22 A110 | HAST* | Ta = 130°C<br>rh = 85%<br>$V_{stress} = V_{max}$ | 96 h | 3 x 77 | 0 / 231 | PASS |

**Mechanical Stress Test Results:**

| Test Description | Abbr. | Condition | Duration | Lots | Fail/Qty | Result |
|---|---|---|---|---|---|---|
| Destructive Physical Analysis | DPA | Samples from HAST, HTSL and TC | | 10 | 0 / 150 | PASS |

**Notes:**
* For SMD devices reliability stress tests performed after preconditioning test (PC) according to JESD

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**Document reference**

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