Product Qualification Report

XDPP1100-Q024

Digital power supply controller with PMBus interface & ARM CORTEX™ M0

Description

This product qualification report describes the characteristics of the product with respect to quality and reliability.

The qualification sample selection was done on production lots which were manufactured and tested on standard production processes and meet the defined requirements.

The qualification test results of those products as outlined in this document are based on **JEDEC** for target applications and may reference existing qualification results of similar products. Such referencing is justified by the structural similarity of the products.

Qualification Assessment

Fully qualified according to **JEDEC** for **Industrial Applications** and assessed as PASS

For further information about comparable products, please contact the nearest Infineon Technologies office (www.infineon.com).

XDPP1100-Q024
PG-VQFN-24
MSL: 2; 260°C

qualified since November 2020

Electrical Stress Test Results:

Test Description	Abbr.	Condition	Duration	Lots/SS	Fail/Qty	Result
High Temperature Storage Life JESD22 A103	HTSL	Ta ≥ 150°C	1000 h	3 x 77	0 / 231	PASS
High Temperature Operating Life JESD22 A108	HTOL	Ta = 125°C $V_{stress} = V_{max}$	2000h	3 x 77	0 / 231	PASS
Latch-up JESD78	LU	Class 2				PASS
ESD (HBM) JESD22-A114	HBM	Class 2 (2000V to <4000V)				PASS
ESD (CDM) JESD22-C101	CDM	Class C3 (>1000 V)				PASS

Environmental Stress Test Results:

Test Description	Abbr.	Condition	Duration	Lots/SS	Fail/Qty	Result
Pre-conditioning J-STD020 / JESD22 A113	PC	MSL and 3 x reflow		20 x 77	0 / 1540	PASS
Temperature Cycling JESD22 A104	TC*	-65°C to +150°C	1000 cyc	10 x 77	0 / 770	PASS
Biased Highly Accelerated Stress Test JESD22 A110	HAST*	Ta = 130°C rh = 85% $V_{stress} = V_{max}$	96 h	10 x 77	0 / 770	PASS

Mechanical Stress Test Results:

Test Description	Abbr.	Condition	Duration	Lots	Fail/Qty	Result
Destructive Physical Analysis	DPA	Samples from HAST, HTSL and TC		9	0 / 135	PASS

Notes:
* For SMD devices reliability stress tests performed after preconditioning test (PC) according to JESD

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Document reference

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