# Product Qualification Report

# IPA65R150CFD

CoolMOS™

## Description

This product qualification report describes the characteristics of the product with respect to quality and reliability.

The qualification sample selection was done on production lots which were manufactured and tested on standard production processes and meet the defined requirements.

The qualification test results of those products as outlined in this document are based on **JEDEC** for target applications and may reference existing qualification results of similar products. Such referencing is justified by the structural similarity of the products.

## Qualification Assessment

Fully qualified according to **JEDEC** for **Industrial Applications** and assessed as PASS

For further information about comparable products, please contact the nearest Infineon Technologies office (www.infineon.com).

# IPA65R150CFD
# PG-TO220-3
# MSL: NA; through hole device

**qualified since 2010**

**Electrical Stress Test Results:**

| Test Description | Abbr. | Condition | Duration | Lots/SS | Fail/Qty | Result |
|---|---|---|---|---|---|---|
| Parametric Verification | PV | -55°C, +25°C, +150°C | | 5 x 25 | 0 / 125 | PASS |
| High Temperature Reverse Bias JESD22 A108 | HTRB* | Ta ≥ 150°C $V_{DS}$ ≥ 520V | 1000 h | 5 x 77 | 0 / 385 | PASS |
| High Temperature Gate Bias JESD22 A108 | HTGB* | Ta = 150°C $V_{GS}$ = +/-20V | 1000 h | 5 x 77 | 0 / 385 | PASS |
| High Humidity High Temp. Reverse Bias JESD22 A101 | H3TRB* | Ta = 85°C rh = 85% $V_{DS}$ = 80V | 1000 h | 5 x 77 | 0 / 385 | PASS |
| Intermitted Operational Life Test MIL-STD 750 / Meth.1037 | IOL* | Delta T =100K | 15000 cyc | 5 x 77 | 0 / 385 | PASS |
| ESD (HBM) JESD22-A114 | HBM | Class 2 2000V to < 4000V | | | | PASS |
| ESD (CDM) JESD22-C101 | CDM | Class C3 ≥ 1000V | | | | PASS |

**Environmental Stress Test Results:**

| Test Description | Abbr. | Condition | Duration | Lots/SS | Fail/Qty | Result |
|---|---|---|---|---|---|---|
| Pre-conditioning J-STD020 / JESD22 A113 | PC | MSL and 3 x reflow | | 3 x 539 | 0 / 1617 | PASS |
| Temperature Cycling JESD22 A104 | TC* | -55°C to +150°C | 1000 cyc | 5 x 77 | 0 / 385 | PASS |
| Autoclave JESD22 A102 | AC* | Ta = 121°C rh = 100% | 96 h | 5 x 77 | 0 / 385 | PASS |
| Unbiased Highly Accelerated Stress Test JESD22 A118 | UHAST* | Ta = 130°C rh = 85% | 96 h | 5 x 77 | 0 / 385 | PASS |
| Wave solder simulation for SMD devices JESD22 A111 | WS* | T=260°C / 1 x 10sec and Temperature Cycling -55°C to +150°C; 100 cyc. | | 3 x 30 | 0 / 90 | PASS |

**Mechanical Stress Test Results:**

| Test Description | Abbr. | Condition | Duration | Lots | Fail/Qty | Result |
|---|---|---|---|---|---|---|
| Destructive Physical Analysis | DPA | samples from AC or UHAST& TC | | 5 | 0 / 5 | PASS |

**Notes:**

* For SMD devices reliability stress tests performed after preconditioning test (PC) according to JESD22

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**Do you have a question about this document?**

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**Document reference**

**n.a.**

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