Product Qualification Report

IMBG65R163M1H

CoolSiC™

Description

This product qualification report describes the characteristics of the product with respect to quality and reliability.

The qualification sample selection was done on production lots which were manufactured and tested on standard production processes and meet the defined requirements.

The qualification test results of those products as outlined in this document are based on **JEDEC** for target applications and may reference existing qualification results of similar products. Such referencing is justified by the structural similarity of the products.

Qualification Assessment

Fully qualified according to **JEDEC** for **Industrial Applications** and assessed as PASS

For further information about comparable products, please contact the nearest Infineon Technologies office (www.infineon.com).

IMBG65R163M1H **qualified 2021**
PG-TO263-7
MSL: 1; 260°C

Electrical Stress Test Results:

Test Description	Abbr.	Condition	Duration	Lots/SS	Fail/Qty	Result
Parametric Verification	PV	-55°C, +25°C, +175°C		3 x 50	0 / 150	PASS
High Temperature Reverse Bias JESD22 A108	HTRB*	Ta = 175°C V_{DS} ≥ 80% VDS max	1000 h	3 x 77	0 / 231	PASS
High Temperature Gate Bias JESD22 A108	HTGB*	Ta = 175°C V_{GS} = +/-20V	1000 h	3 x 77	0 / 231	PASS
High Humidity High Temp. Reverse Bias JESD22 A101	H3TRB*	Ta = 85°C rh = 85% V_{DS} = 80% VDS max	1000 h	3 x 77	0 / 231	PASS
Intermitted Operational Life Test MIL-STD 750 / Meth.1037	IOL*	Delta T =100K	15000 cyc	3 x 77	0 / 231	PASS
ESD (HBM) JEDEC JS-001	HBM	Class 1B (500 V to < 1000 V)				PASS
ESD (CDM) JEDEC JS-002	CDM	Class C3 (> 1000 V)				PASS

Environmental Stress Test Results:

Test Description	Abbr.	Condition	Duration	Lots/SS	Fail/Qty	Result
Pre-conditioning JESD22 A113	PC	MSL and 3 x reflow		3 x 462	0 / 1386	PASS
Temperature Cycling JESD22 A104	TC*	-55°C to +150°C	1000 cyc	3 x 77	0 / 231	PASS
Unbiased Highly Accelerated Stress Test JESD22 A118	UHAST*	Ta = 130°C rh = 85%	96 h	3 x 77	0 / 231	PASS

Mechanical Stress Test Results:

Test Description	Abbr.	Condition	Duration	Lots	Fail/Qty	Result
Destructive Physical Analysis	DPA	samples from AC & TC		3	0 / 3	PASS

Notes:
* For SMD devices reliability stress tests performed after preconditioning test (PC) according to JESD22

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Document reference

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