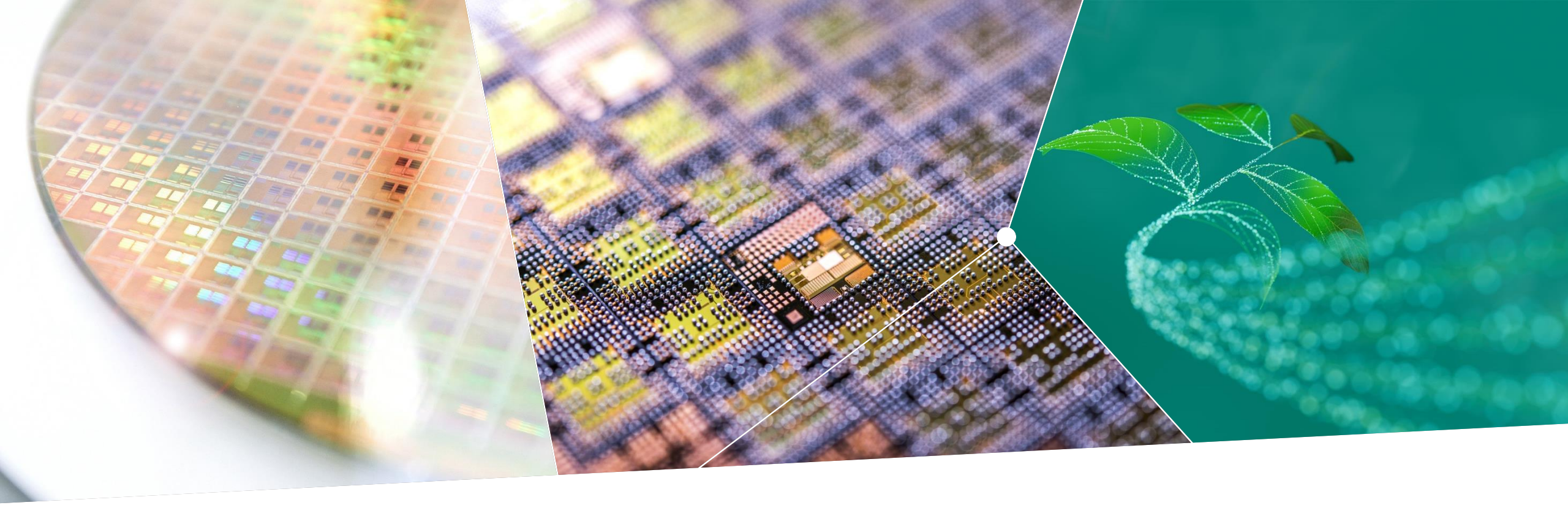


Mapping of OPTIREG™ LDO with Transceivers

ATV SP APS

H2 2025

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- We are constantly improving this document, therefore if you have any suggestions, feedback, complaints, questions, ideas, etc. please write us an e-mail with subject title „mapping table“ to the address: R-IFX-APSProductMar@infineon.com

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Mapping OPTIREG™ linear with Transceivers

Infineon Transceivers			Scenario*	LDO is supplying...?	Power Requirement			OPTIREG™ linear	
					Maximum Power Requirements_ for Transceiver (real power pattern) mA	Maximum Power Requirements_ for MC (Traveo™ 1st Gen) mA**	Total Power Requirement mA	Basic LDO	LDO with Enable
CAN	FD	TLE935xBSJ/BLE TLE9371SJ/LE	Scenario 1 (5V)*	LDO Supplying (TRX+MC)	5V@115mA (max)	350	465	TLS850A4TE V50	TLS850B0TE V50
	FD	TLE9350BxSJ/TLE9350BxLE TLE9351BVSJ/LE TLT9251VLE	Scenario 2 (5V)*	LDO Supplying TRX	5V@120mA (max)	-	120	TLS830A4EP V50	TLS715B0EJ V50
			Scenario 3 (3.3V)*	LDO Supplying TRX	5V@120mA (max)	-	120	TLS830A4EP V50	TLS715B0EJ V50
				LDO Supplying (TRX+MC)	3.3V@5mA (max)	350	355	TLF80511EJ V33	TLS850B0TE V33
	FD SIC	TLE9371VSJ/LE	Scenario 2 (5V)*	LDO Supplying TRX	5V@120mA (max)	-	120	TLS830A4EP V50	TLS715B0EJ V50
			Scenario 3 (3.3V)*	LDO Supplying TRX	5V@120mA (max)	-	120	TLS830A4EP V50	TLS715B0EJ V50
				LDO Supplying (TRX+MC)	3.3V@5mA (max)	350	355	TLF80511EJ V33	TLS850B0TE V33
	FD	TLE9252x/TLE9255x/TLT925x	Scenario 4 (5V)*	LDO Supplying (TRX+MC)	5V@120mA (max)	350 EN input pin on LDO supplying MC	470	-	TLS850B0TE V50
			Scenario 5 (3.3V)*	LDO Supplying TRX	5V@120mA (max)	-	120	TLS830A4EP V50	TLS715B0EJ V50
				LDO Supplying (TRX+MC)	3.3V@5mA (max)	350 EN input pin on LDO supplying MC	355	-	TLS850B0TE V33

*Please refer to scenario slides.
**Traveo 1st Gen is just an example for power requirment calculation, for more microntrollers options please refer to our Mapping of OPTIREG™ product portfolio with various microcontrollers

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Mapping OPTIREG™ linear with Transceivers

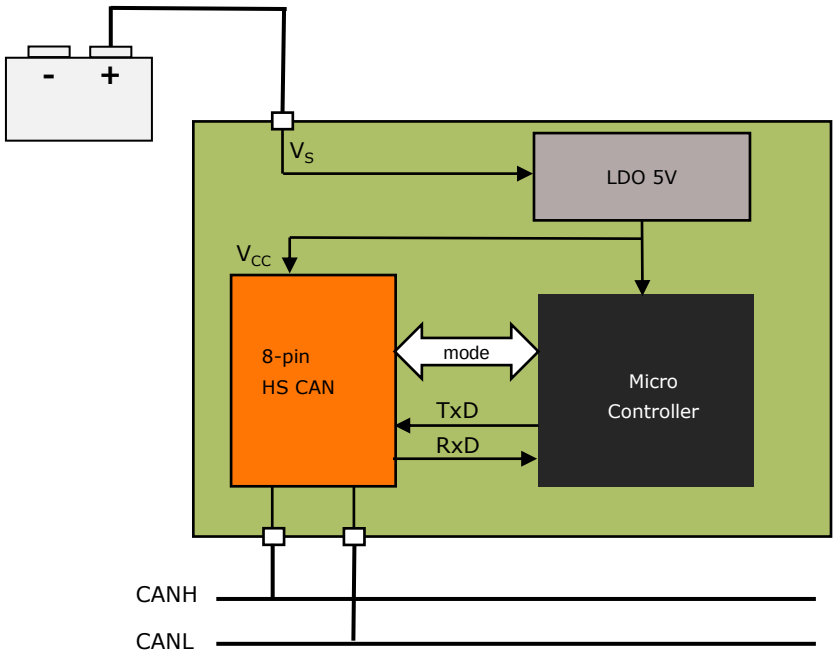
Infineon Transceivers			Scenario*	LDO is supplying...?	Power Requirement			OPTIREG™ linear	
					Maximum Power Requirements_ for Transceiver (real power pattern) mA	Maximum Power Requirements_ for MC (Traveo™ 1st Gen) mA**	Total Power Requirement mA	Basic LDO	LDO with Enable
LIN	**Basic and Dual**	TLE7257xx/TLE7258xx/ TLE7259-3xx	Scenario 6	LDO Supplying (TRX+MC)	13.5V@130mA (max)	350 EN input pin on LDO supplying MC	350		TLS850B0TE V50

*Please refer to scenario slides.
**Traveo 1st Gen is just an example for power requirment calculation, for more microntrollers options please refer to our Mapping of OPTIREG™ product portfolio with various microcontrollers

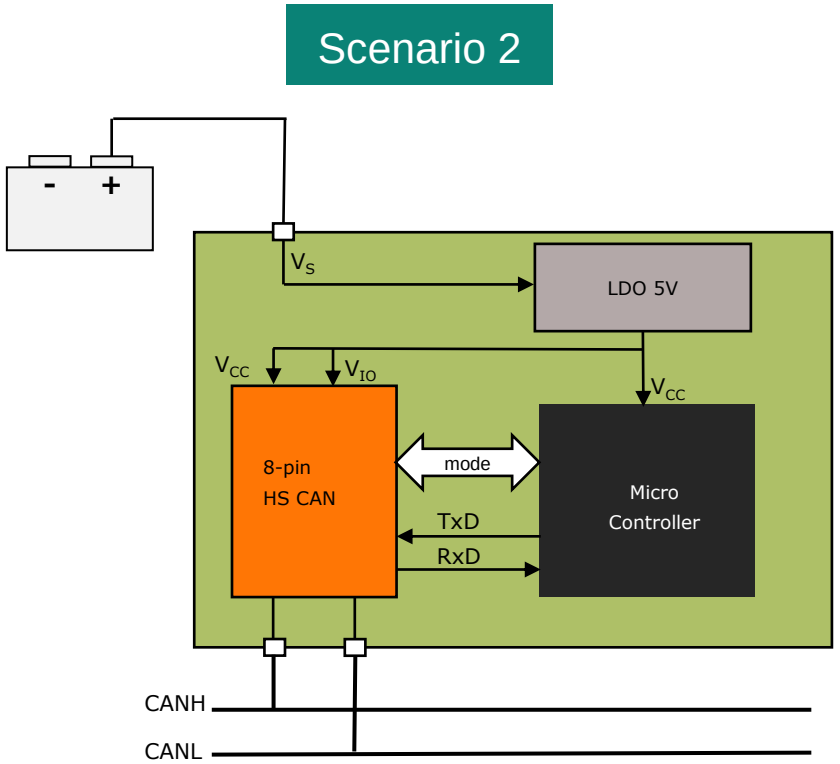
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CAN power supply scenarios

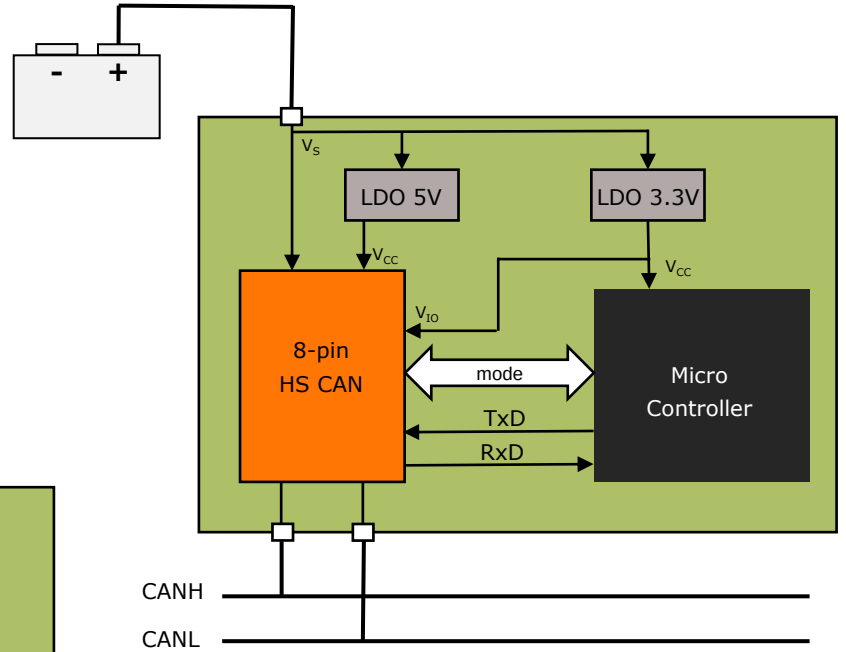
Scenario 1



Scenario 2

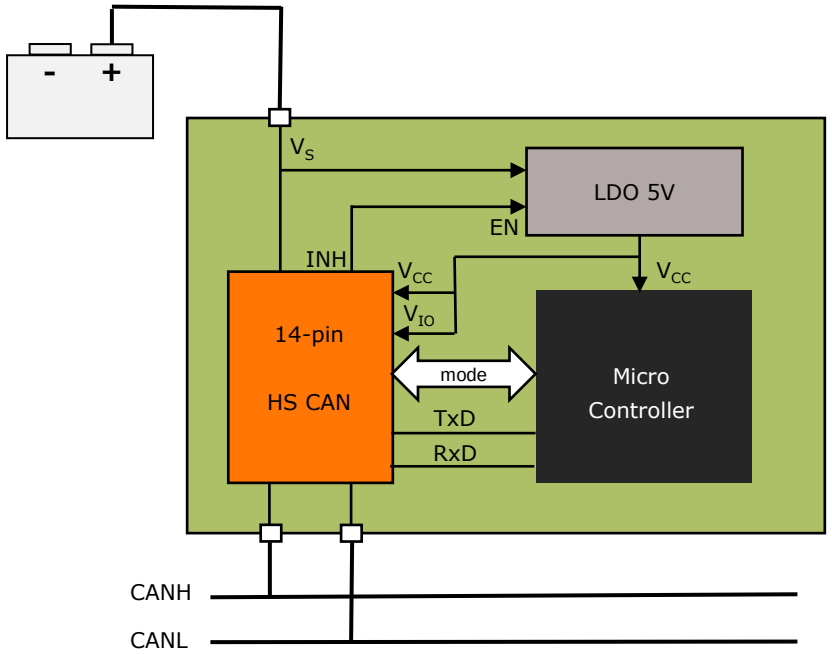


Scenario 3

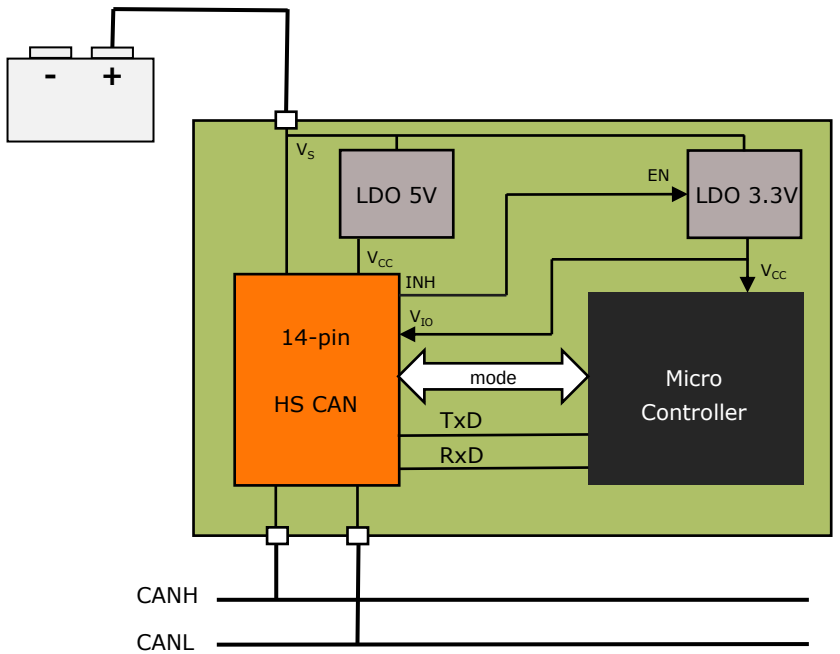


CAN power supply scenarios

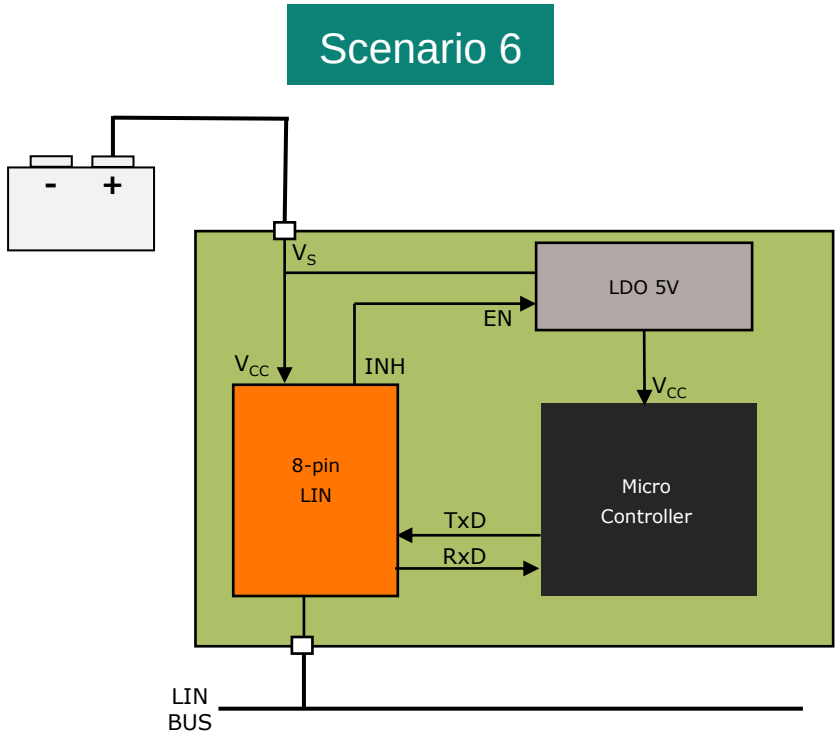
Scenario 4



Scenario 5



LIN power supply scenario




Infineon