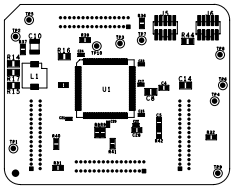
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
1	ALPHA RELEASE	08/28/09	DFK
2	BETA RELEASE	03/24/10	DFK
**	PRODUTION BUILD	03/23/10	DFK



UNLESS OTHERWISE SPECIFIED	SIGNATURES	DATE
DIMENSIONS ARE IN INCHES		
TOLERANCES ON; ANGLES +/- 2°	DRAWN ZMK	08/24/09
2 PL DECIMALS +/- .010	CHECKED DFK	08/24/09
3 PL DECIMALS +/- .005	ENGRG DFK	08/24/09
THIRD ANGLE PROJECTIONS	ISSUED	

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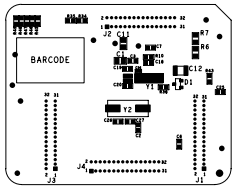
CYPRESS SEMICONDUCTOR
2700 162nd Street SW. Bldg D
Lynnwood, WA 98087
(425) 787-4400

ASSEMBLY DRAWING
CY8C55 FAMILY
PROCESSOR MODULE

SIZE	FSCM NO	DWG NO	REV
C		121R-54600	**

SCALE: 1/1 SHEET: 1 OF 2



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SHEET: 2 OF 2