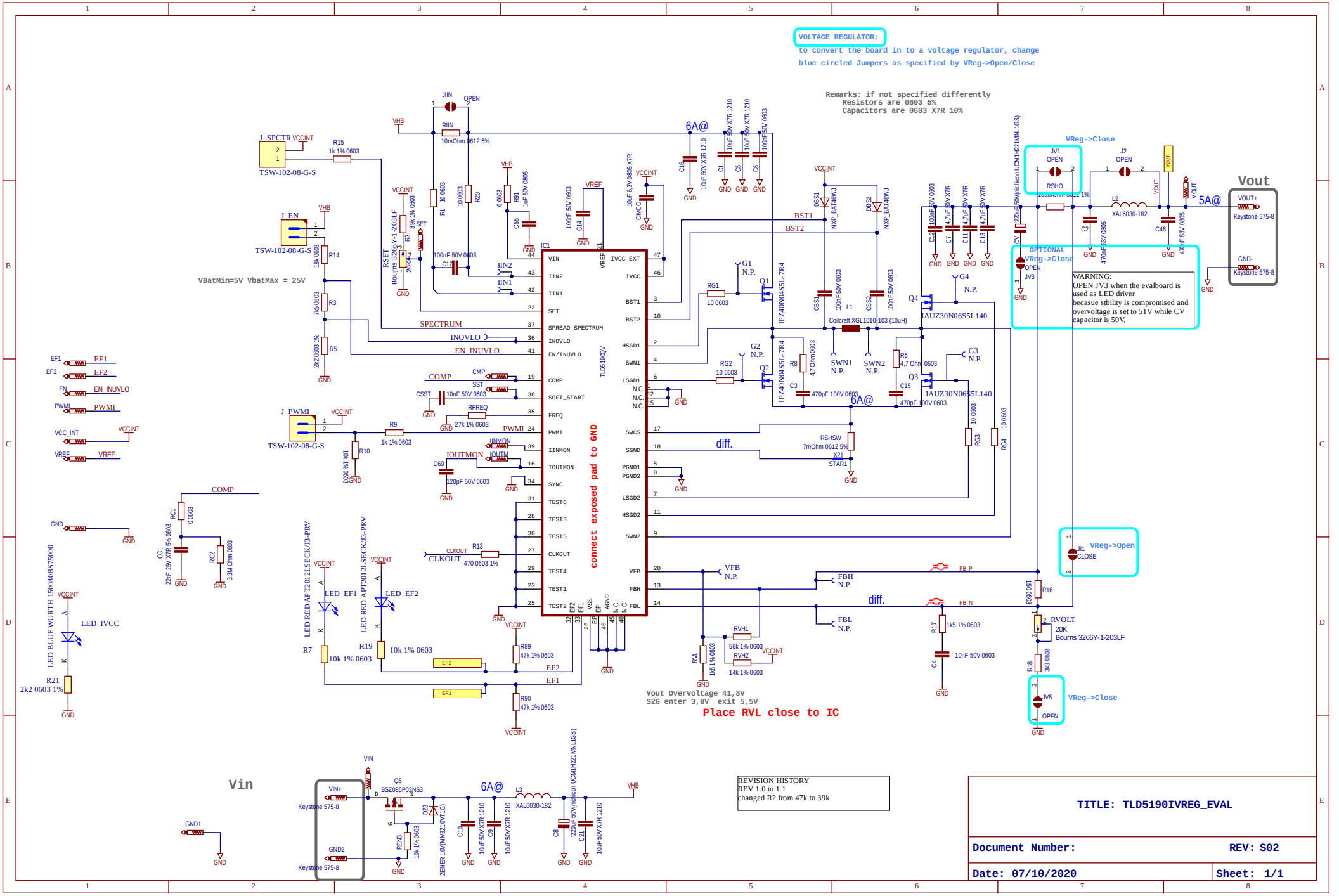

VOLTAGE REGULATOR:
to convert the board in to a voltage regulator, change
blue circled Jumpers as specified by VReg->Open/Close
Remarks: if not specified differently
Resistors are 0603 5%
Capacitors are 0603 X7R 10%
VBatMin=5V VbatMax = 25V
connect exposed pad to GND
diff.
Vout Overvoltage 41,8V
S2G enter 3,8V exit 5,5V
Place RVL close to IC
VReg->Close
VReg->Open
OPTIONAL
WARNING:
OPEN JV3 when the evalboard is used as LED driver
becasue stbility is compromised and overvoltage is set to 51V while CV capacitor is 50V,
REVISION HISTORY
REV 1.0 to 1.1
changed R2 from 47k to 39k
TITLE: TLD5190IVREG_EVAL
Document Number:
REV: S02
Date: 07/10/2020
Sheet: 1/1