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MB91590 Series

32-BIT MICROCONTROLLER
FR81S

HARDWARE MANUAL (Supplementary Information)

For the information for microcontroller supports, see the following web site.

http://www.spansion.com/support/microcontrollers/

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Preface

Objective of this supplementary Information

This document includes supplementary information with MB91590 HARDWARE MANUAL. When you need to set up EPFR53 and EPFR54 of Extended Port Function Register in order to use HSSPI, see this documentation as CHAPTER 4.13.18 on HARDWARE MANUAL.

Object document

Document name: FR81S 32-BIT MICROCONTROLLER MB91590 Series HARDWARE MANUAL

Document code: MN705-00009-5v0-E

4.13.18. Extended Port Function Register 53, 54 : EPFR53, EPFR54 (Extended Port Function Register 53, 54)

The bit configuration of extended port function register 53, 54 is shown below.

This register is used to set enable/disable for the HSSPI function.

■ EPFR53 : Address 0E95$_H$ (Access : Byte, Half-word, Word)

	bit7	bit6	bit5	bit4	bit3	bit2	bit1	bit0
	Reserved			QUADSCKE	QUADSDE3	QUADSDE2	QUADSDE1	QUADSDE0
Initial value	1	1	1	0	0	0	0	0
Attribute[*1]	R1,WX	R1,WX	R1,WX	R0,W0	R0,W0	R0,W0	R0,W0	R0,W0
Attribute[*2]	R1,WX	R1,WX	R1,WX	R/W	R/W	R/W	R/W	R/W

[bit4] QUADSCKE: HSSPI clock enable
[bit3] QUADSDE3: HSSPI data enable
[bit2] QUADSDE2: HSSPI data enable
[bit1] QUADSDE1: HSSPI data enable
[bit0] QUADSDE0: HSSPI data enable

*1: MB91F591/2/4/6//9
*2: MB91F59A/B

QUADSDEn, QUADSCKE	Operation
0	Disable output
1	Enable output

n: channel number

■ EPFR54 : Address 0E96$_H$ (Access : Byte, Half-word, Word)

	bit7	bit6	bit5	bit4	bit3	bit2	bit1	bit0
	Reserved				QUADSSELE3	QUADSSELE2	QUADSSELE1	QUADSSELE0
Initial value	1	1	1	1	0	0	0	0
Attribute[*1]	R1,WX	R1,WX	R1,WX	R1,WX	R0,W0	R0,W0	R0,W0	R0,W0
Attribute[*2]	R1,WX	R1,WX	R1,WX	R1,WX	R/W	R/W	R/W	R/W

[bit3] QUADSSELE3: HSSPI Slave select output enable 3
[bit2] QUADSSELE2: HSSPI Slave select output enable 2
[bit1] QUADSSELE1: HSSPI Slave select output enable 1
[bit0] QUADSSELE0: HSSPI Slave select output enable 0

*1: MB91F591/2/4/6//9
*2: MB91F59A/B

QUADSSELEn	Operation
0	Disable output
1	Enable output

n: channel number

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FR81S
32-BIT MICROCONTROLLER
MB91590 Series
HARDWARE MANUAL (Supplementary Information)

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Colophon

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