Defense Non-volatile Parallel NOR Flash nomenclature



Device Family

S29GL 3.0 V core, with VIO option, Page-Mode parallel flash memory

Density

128 128 Mb
256 256Mb
512 512 Mb
01G 1 Gbit

Process Technology

S 65-nm MIRRORBIT™ process technology

Speed

10 100 ns random access time
12 120 ns random access time
13 130 ns random access time

Package

D FBGA (LAE064) 9 mm x 9 mm
F FBGA (LAA064) 13 mm x 11 mm
T TSOP Standard Pinout

Packing Type

0 Tray
3 13" Tape and reel

Model Number

01 VIO = VCC = 2.7 to 3.6 V, highest address sector protected
02 VIO = VCC = 2.7 to 3.6 V, lowest address sector protected
V1 VIO = 1.65 to VCC, VCC = 2.7 to 3.6 V, highest address sector protected
V2 VIO = 1.65 to VCC, VCC = 2.7 to 3.6 V, lowest address sector protected

Temp Range

E Military (-55°C to +125°C)
F Automotive (-55°C to +105°C)

Package Material

A Leaded (Sn/Pb)-balls - BGA only
F Low-Halogen, Lead (Pb)-free
H Low-Halogen, Lead (Pb)-free
K Leaded (Sn/Pb) balls, Au Bond Wire - BGA only

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