SIEMENS

Microcomputer Components
Technical Support Group Munich
HL MCB AT1

Errata Sheet

August 23, 1995 / Release 3.5

Device : SAB 80C517

Marking : DB

These parts of the SAB 80C517 can be identified by the letters "DB" below the part number. The parts are mounted in a Plastic Leaded Chip Carrier (P-LCC-84) or in a Plastic Metric Quad Flat Pack (P-MQFP-100-2) package.

This errata sheet describes both the *functional problems* (see part 1) and the *deviations from the electrical and timing specifications* (see part 2) known in this step.

If a problem was already introduced with an errata sheet of an earlier step, its initial number is still retained in this errata sheet. Thus, the numerical order of the problems described in the following may contain gaps.

At the end of this document, you will find two history tables showing the problems found in the SAB 80C517 up to now. Changes to the last revision are shaded light grey in the history tables.

1) Functional Problems

No functional problems are known in this step (history see table 1).

2) Electrical- and Timing-Spec. Deviations

No electrical problem are known in this step (history see table 2).

Functional Problem No.	Marking	Description	Remarks
10	ES-CA	Oscillator Watchdog	fixed in ES-DB and later
9	ES-CA	Start of Watchdog-Timer	fixed in ES-DB and later
8	BB	MDU - Division-Problem	fixed in BC and later
7	BB, BC	Software Power Down Mode	fixed in ES-CA and later
6	BB, BC	Watchdog-Timer at high temperature	fixed in ES-CA and later
5	BB, BC	Edge-triggered External Interrupts 0,1	fixed in ES-CA and later
4	BB, BC	Compare Timer	fixed in ES-CA and later
3	BB, BC	Slow Down Mode	fixed in ES-CA and later
2	BB, BC	External Start of the ADC	fixed in ES-CA and later
1	BB, BC	MDU	fixed in ES-CA and later

Table 1: History of Functional Problems

Electrical- / Timing-Problem No.	Marking	Description	Remarks
8	BC, ES-CA	ADC - DNLE, INLE, Gain, Offset	fixed in specification (data sheet 4'94)
7	ES-CA	$f_{OCS,}$ Minimum Limit Value	fixed in specification (data sheet 4'94)
6	ES-CA	Idle Current I_{CC}	fixed in ES-CA and later
5	BB, BC	ADC, Offset Error	fixed in ES-CA and later
4	BB	ADC, Internal Reference Error, $V_{INTREFERR}$	changed in data sheet 11.92
3	BB	Input Low Current PE# /SWD, OWE, I_{IL4}	changed in data sheet 11.92
2	BB	Input Low Current XTAL 2, I_{IL3}	changed in data sheet 11.92
1	BB	Logic Input Current, I_{IL}	changed in data sheet 11.92

Table 2: History of Electrical- and Timing-Spec. Deviations

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