

16/32-Bit

**Architecture** 

## XC2361E

16/32-Bit Single-Chip Microcontroller with 32-Bit Performance XC2000 Family Derivatives / Premium Line

Addendum to Data Sheet V1.2 V1.0 2012-07

### Microcontrollers

Edition 2012-07
Published by
Infineon Technologies AG
81726 Munich, Germany
© 2012 Infineon Technologies AG
All Rights Reserved.

#### Legal Disclaimer

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Infineon Technologies hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party.

#### Information

For further information on technology, delivery terms and conditions and prices, please contact the nearest Infineon Technologies Office (<a href="https://www.infineon.com">www.infineon.com</a>).

#### Warnings

Due to technical requirements, components may contain dangerous substances. For information on the types in question, please contact the nearest Infineon Technologies Office.

Infineon Technologies components may be used in life-support devices or systems only with the express written approval of Infineon Technologies, if a failure of such components can reasonably be expected to cause the failure of that life-support device or system or to affect the safety or effectiveness of that device or system. Life support devices or systems are intended to be implanted in the human body or to support and/or maintain and sustain and/or protect human life. If they fail, it is reasonable to assume that the health of the user or other persons may be endangered.



16/32-Bit

**Architecture** 

### XC2361E

16/32-Bit Single-Chip Microcontroller with 32-Bit Performance XC2000 Family Derivatives / Premium Line

Addendum to Data Sheet V1.2 V1.0 2012-07

Microcontrollers



# XC2361E XC2000 Family Derivatives / Premium Line

XC2361E Addendum to Data Sheet V1.2 Revision History: V1.0 2012-07				
Previous Versions:				
Page	Subjects (major changes since last revision)			
_	This is the first release			

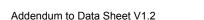
#### **Trademarks**

C166™, TriCore™ and DAVE™ are trademarks of Infineon Technologies AG.

#### We Listen to Your Comments

Is there any information in this document that you feel is wrong, unclear or missing? Your feedback will help us to continuously improve the quality of this document. Please send your proposal (including a reference to this document) to:

mcdocu.comments@infineon.com





# XC2361E XC2000 Family Derivatives / Premium Line

### **Table of Contents**

Tم	h		٥f	0	nto	nts
ıα	DI	ıe	OT	CO	nte	nts

1	Special Device Types	 2
•	opecial bevice Types	 _

## XC2361E XC2000 Family Derivatives / Premium Line

**Special Device Types** 

16/32-Bit Single-Chip Microcontroller with 32-Bit Performance XC2361E (XC2000 Family)

### 1 Special Device Types

The following special device type list entries

Table 1 Synopsis of XC2361E Special Device Types

•	•	-			
Derivative <sup>1)</sup>	Flash Memory	PSRAM DSRAM <sup>2)</sup>	Capt./Comp. Modules	ADC Chan.	Interfaces
XC2361E-72FxxL	576 Kbytes	64 Kbytes 24 Kbytes	CC2 CCU60/1	11 + 5	2 CAN Nodes 6 Serial Chan.
XC2361E-104FxxL	832 Kbytes	64 Kbytes 24 Kbytes	CC2 CCU60/1	11 + 5	2 CAN Nodes 6 Serial Chan.
XC2361E-136FxxL	1 088 Kbytes	64 Kbytes 24 Kbytes	CC2 CCU60/1	11 + 5	2 CAN Nodes 6 Serial Chan.

<sup>1)</sup> xx is a placeholder for the available speed grade (in MHz).

are replaced by

Table 2 Synopsis of XC2361E Special Device Types

Derivative <sup>1)</sup>	Flash Memory	PSRAM DSRAM <sup>2)</sup>	Capt./Comp. Modules	ADC Chan.	Interfaces
XC2361E-72FxxL	576 Kbytes	64 Kbytes 24 Kbytes	CC2 CCU60/1	11 + 5	3 CAN Nodes 6 Serial Chan.
XC2361E-104FxxL	832 Kbytes	64 Kbytes 24 Kbytes	CC2 CCU60/1	11 + 5	3 CAN Nodes 6 Serial Chan.
XC2361E-136FxxL	1 088 Kbytes	64 Kbytes 24 Kbytes	CC2 CCU60/1	11 + 5	3 CAN Nodes 6 Serial Chan.

<sup>1)</sup> xx is a placeholder for the available speed grade (in MHz).

Availability of the 3<sup>rd</sup> CAN node is confirmed to be tested in the production test flow.

<sup>2)</sup> All derivatives additionally provide 8 Kbytes SBRAM and 2 Kbytes DPRAM.

<sup>2)</sup> All derivatives additionally provide 8 Kbytes SBRAM and 2 Kbytes DPRAM.

www.infineon.com

Published by Infineon Technologies AG