

英飞凌-40V 175°C P沟道

增强型OptiMOS®-P2 功率晶体管



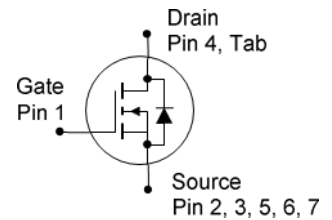
产品概述

V_{DS}	-40	V
$R_{DS(on),max}$	2.4	mΩ
I_D	-180	A

特点

- P 沟道 - 逻辑电平 - 增强模式
- 符合 AEC-Q100
- MSL1 回流焊峰值温度高达 260°C
- 工作温度 175°C
- 绿色产品：符合 RoHS 标准
- 100% 雪崩测试
- 用于反向电池保护

PG-TO263-7-3



Type	Package	Marking
IPB180P04P4L-02	PG-TO263-7-3	4QP04L02

除非另有规定，否则均为 $T_j=25^\circ\text{C}$ 的最大额定值。

Parameter	Symbol	Conditions	Value	Unit
Continuous drain current	I_D	$T_c=25^\circ\text{C}$, $V_{GS}=-10\text{V}^{(1)}$	-180	A
		$T_c=100^\circ\text{C}$, $V_{GS}=-10\text{V}^{(2)}$	-140	
Pulsed drain current ⁽²⁾	$I_{D,pulse}$	$T_c=25^\circ\text{C}$	-720	
Avalanche energy, single pulse	E_{AS}	$I_D=-90\text{A}$	84	mJ
Avalanche current, single pulse	I_{AS}	-	-180	A
Gate source voltage	V_{GS}	-	+5/-16	V
Power dissipation	P_{tot}	$T_c=25^\circ\text{C}$	150	W
Operating and storage temperature	T_j, T_{stg}	-	-55 ... +175	°C
IEC climatic category; DIN IEC 68-1	-	-	55/175/56	

本数据手册的原文使用英文撰写。为方便起见，英飞凌提供了译文；由于翻译过程中可能使用了自动化工具，英飞凌不保证译文的准确性。为确认准确性，请务必访问 infineon.com 参考最新的英文版本（控制文档）。

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	

热特性²⁾

Thermal resistance, junction - case	R_{thJC}	-	-	-	1	K/W
SMD version, device on PCB	R_{thJA}	minimal footprint	-	-	62	
		6 cm ² cooling area ³⁾	-	-	40	

除非另有规定，否则均为 $T_j=25^\circ\text{C}$ 的**电气特性**。

静态参数特性

Drain-source breakdown voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=-1mA$	-40	-	-	V
Gate threshold voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-410\mu A$	-1.2	-1.7	-2.2	
Zero gate voltage drain current	I_{DSS}	$V_{DS}=-32V, V_{GS}=0V,$ $T_j=25^\circ\text{C}$	-	-0.1	-1	μA
		$V_{DS}=-32V, V_{GS}=0V,$ $T_j=125^\circ\text{C}^{2)}$	-	-20	-200	
Gate-source leakage current	I_{GSS}	$V_{GS}=-16V, V_{DS}=0V$	-	-	-100	nA
Drain-source on-state resistance	$R_{DS(on)}$	$V_{GS}=-4.5V, I_D=-100A$	-	2.6	3.9	m Ω
		$V_{GS}=-10V, I_D=-100A$	-	1.8	2.4	

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	

动态参数特性²⁾

Input capacitance	C_{iss}	$V_{GS}=0V, V_{DS}=-25V,$ $f=1MHz$	-	14400	18700	pF
Output capacitance	C_{oss}		-	4570	5900	
Reverse transfer capacitance	C_{rss}		-	180	360	
Turn-on delay time	$t_{d(on)}$	$V_{DD}=-20V,$ $V_{GS}=-10V, I_D=-180A,$ $R_G=3.5 \Omega$	-	32	-	ns
Rise time	t_r		-	28	-	
Turn-off delay time	$t_{d(off)}$		-	146	-	
Fall time	t_f		-	119	-	

栅极电荷特性²⁾

Gate to source charge	Q_{gs}	$V_{DD}=-32V,$ $I_D=-180A,$ $V_{GS}=0 \text{ to } -10V$	-	50	65	nC
Gate to drain charge	Q_{gd}		-	38	76	
Gate charge total	Q_g		-	220	286	
Gate plateau voltage	$V_{plateau}$		-	-3.5	-	V

体内寄生反向二极管

Diode continuous forward current ²⁾	I_S	$T_C=25^\circ C$	-	-	-180	A
Diode pulse current ²⁾	$I_{S,pulse}$		-	-	-720	
Diode forward voltage	V_{SD}	$V_{GS}=0V, I_F=-100A,$ $T_j=25^\circ C$	-	-1.0	-1.3	V
Reverse recovery time ²⁾	t_{rr}	$V_R=-20V, I_F=-50A,$ $di_F/dt=-100A/\mu s$	-	71	-	ns
Reverse recovery charge ²⁾	Q_{rr}		-	101	-	

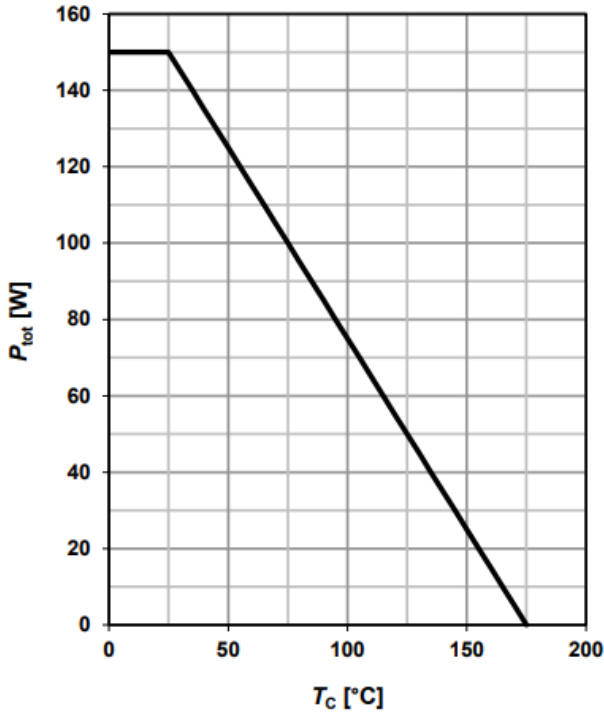
¹⁾ 电流受封装限制；当 $R_{thJC} = 1 \text{ K/W}$ 时，该芯片在 $25^\circ C$ 时能够承载 200 A 。

²⁾ 由设计标定，不受制于生产测试。

³⁾ 器件安装在 $40 \text{ mm} \times 40 \text{ mm} \times 1.5 \text{ mm}$ 环氧树脂印刷电路板 FR4 上，漏极连接用铜面积为 6 cm^2 （一层， $70 \mu\text{m}$ 厚）。印刷电路板垂直放置在静止空气中。

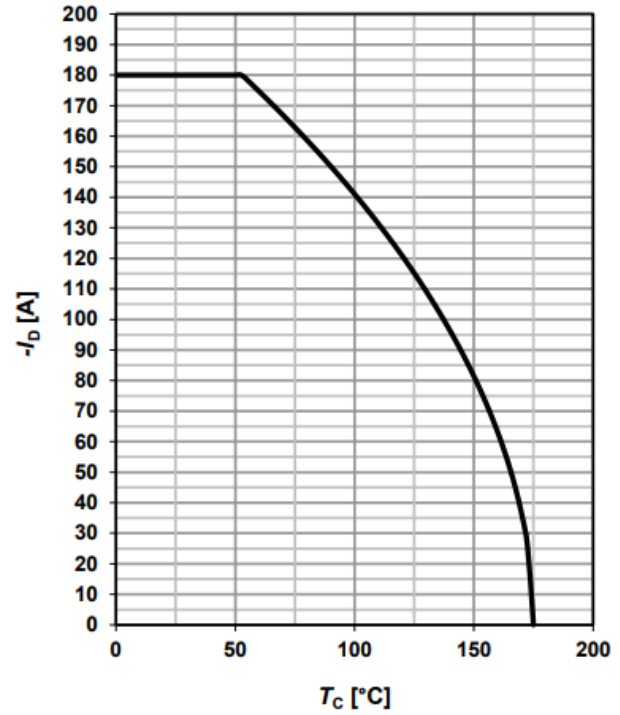
1 功率耗散

$$P_{tot} = f(T_C); V_{GS} \leq -6V$$



2 漏极电流

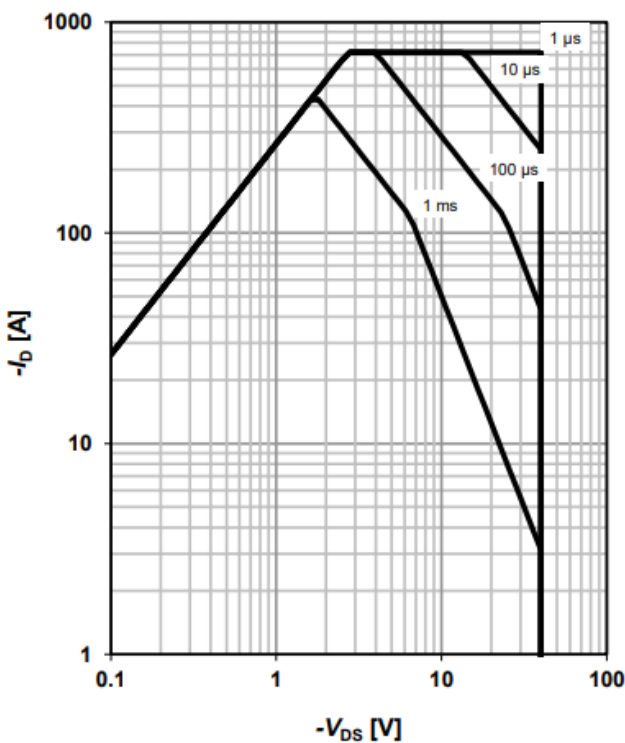
$$I_D = f(T_C); V_{GS} \leq -6V$$



3 安全工作区

$$I_D = f(V_{DS}); T_C = 25\text{ °C}; D = 0$$

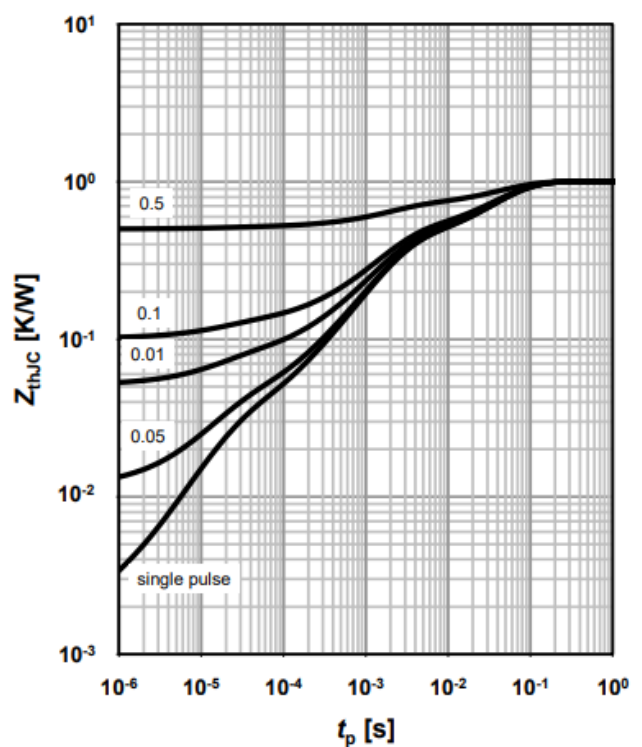
parameter: t_p



4 最大瞬态热阻

$$Z_{thJC} = f(t_p)$$

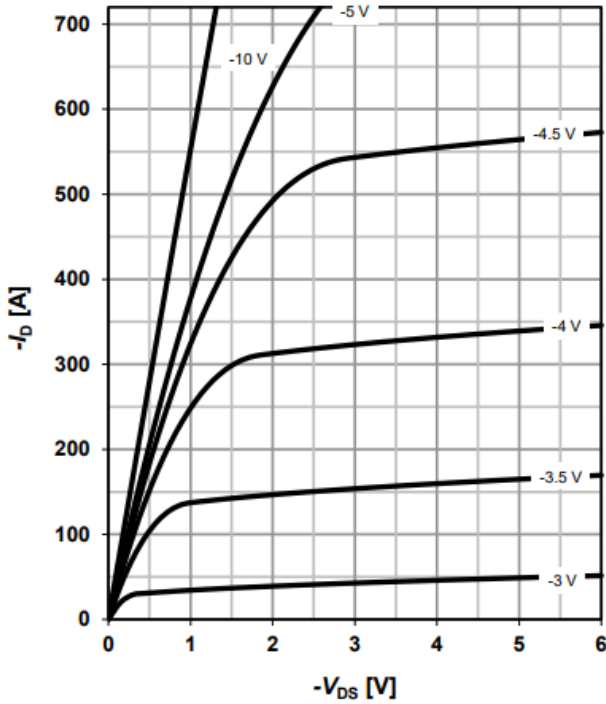
parameter: $D = t_p/T$



5 典型输出特性

$I_D = f(V_{DS}); T_j = 25^\circ\text{C}$

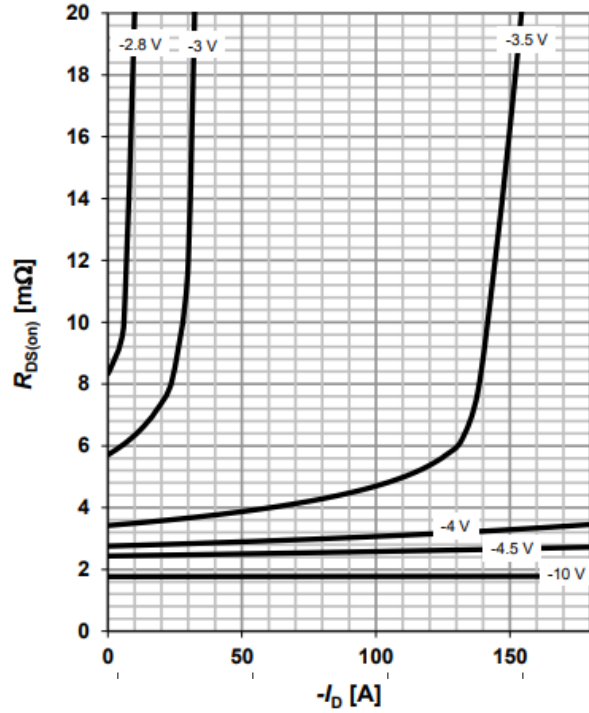
parameter: V_{GS}



6 典型漏源导通电阻

$R_{DS(on)} = f(I_D); T_j = 25^\circ\text{C}$

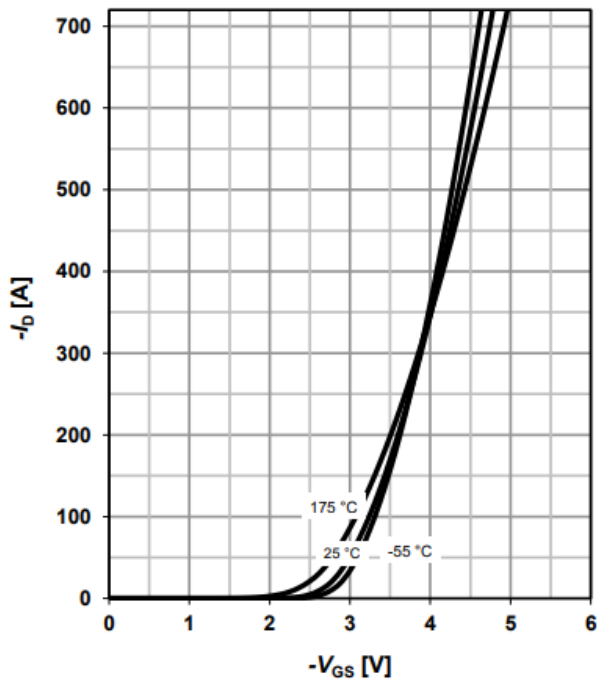
parameter: V_{GS}



7 典型转移特性

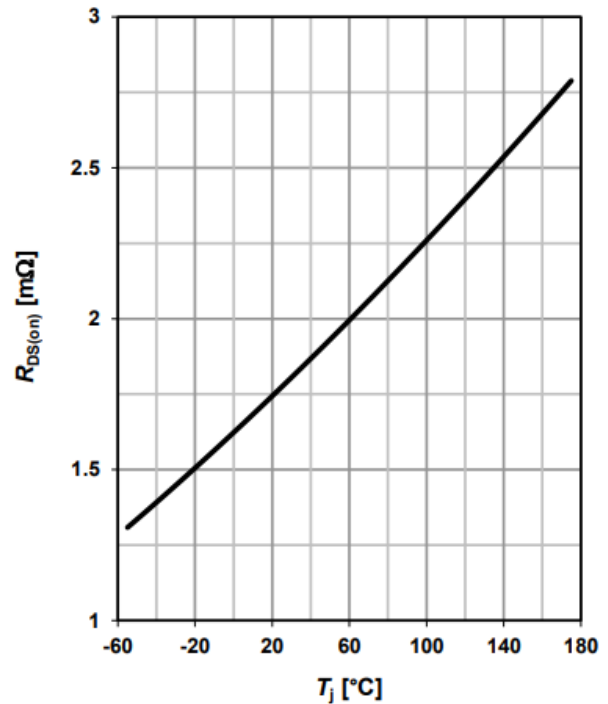
$I_D = f(V_{GS}); V_{DS} = -6\text{V}$

parameter: T_j



8 典型漏源导通电阻

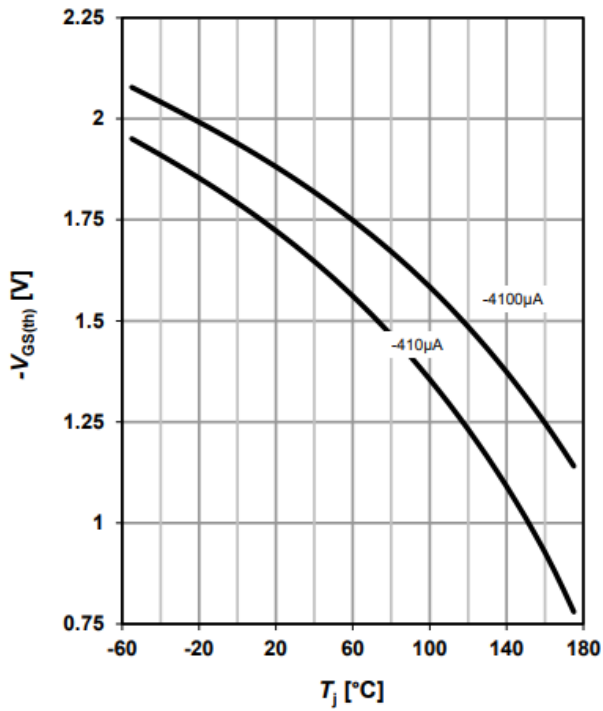
$R_{DS(on)} = f(T_j); I_D = -100\text{A}; V_{GS} = -10\text{V}$



9 典型栅极阈值电压

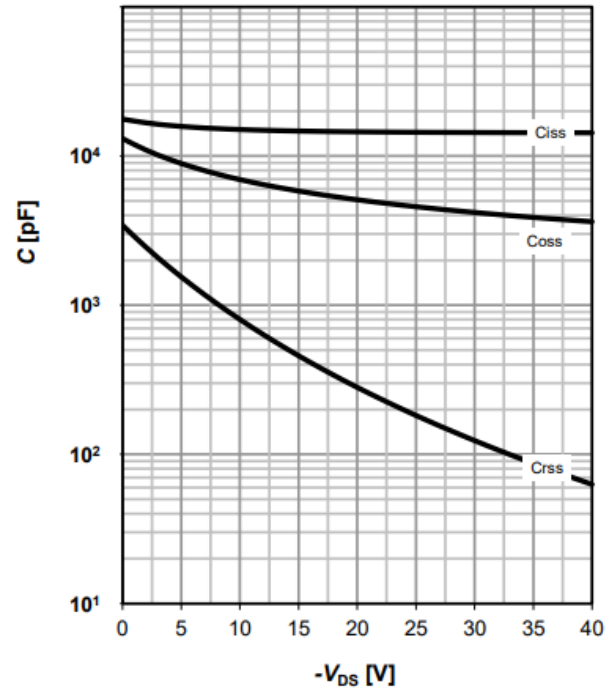
$$V_{GS(th)} = f(T_j); V_{GS} = V_{DS}$$

parameter: I_D



10 典型电容值

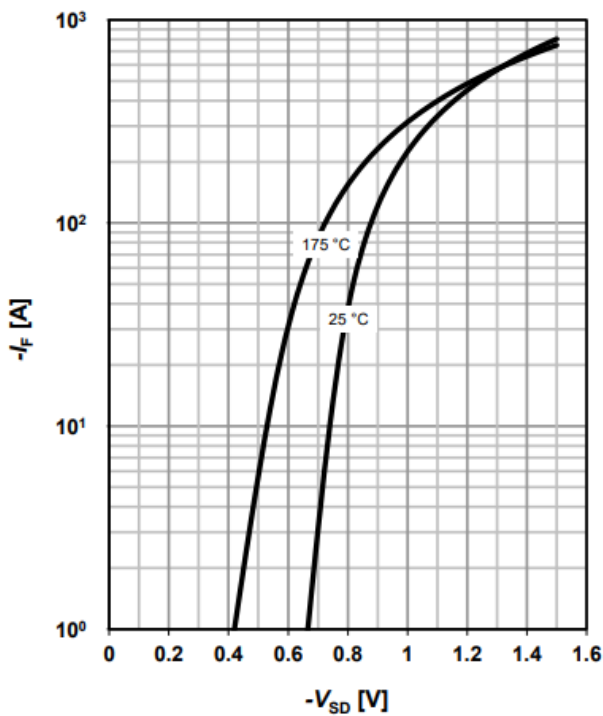
$$C = f(V_{DS}); V_{GS} = 0 V; f = 1 MHz$$



11 典型正向二极管特性

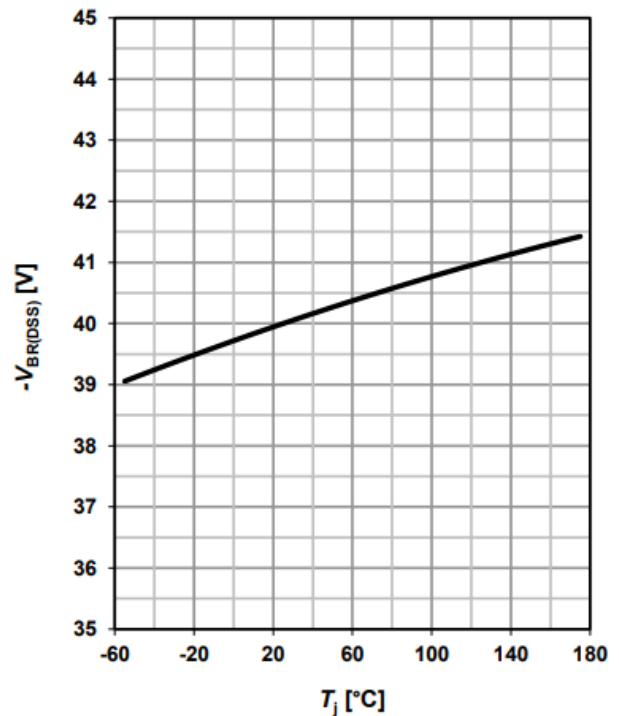
$$I_F = f(V_{SD})$$

parameter: T_j



12 漏源击穿电压

$$V_{BR(DSS)} = f(T_j); I_D = -1mA$$

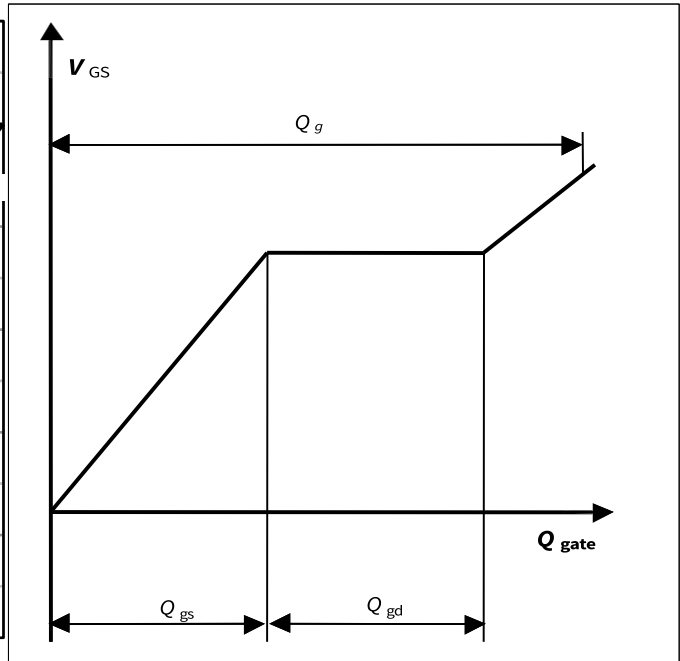
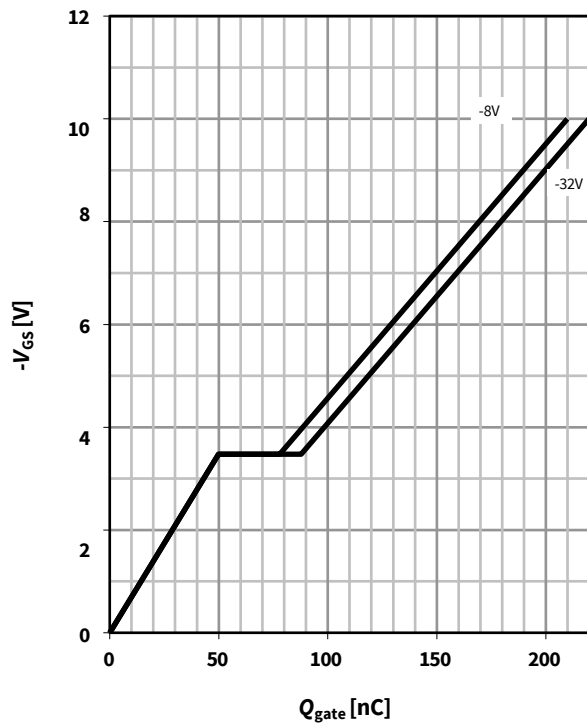


13 典型栅极电荷

$V_{GS} = f(Q_{gate}); I_D = -180A$ pulsed

parameter: V_{DD}

14 栅极充电波形



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Infineon Technologies AG
81726 Munich, Germany

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修订记录

Version	Date	Changes
1.1	02.09.2009	$R_{DS(on)}$ @4.5V: I_D changed to 135A
1.1	02.09.2009	$V_{GS(th)}$: I_D changed to 410uA
1.2	07.10.2009	$R_{DS(on)}$ @4.5V: I_D changed to 80A
1.2	07.10.2009	$R_{DS(on)}$ @10V: I_D changed to 80A
1.2	07.10.2009	V_{SD} : I_D changed to 80A
1.3	27.04.2011	Final Data Sheet
1.4	23.04.2019	V_{GS} changed



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Edition 2025-04-17

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