

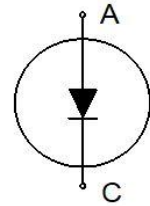
EDT3 Diode for Automotive Applications

Diode

Quality Requirement Category: Automotive

Features

- 750V Emitter Controlled Diode technology
- Soft, fast switching
- Low reverse recovery charge
- Improved commutation behavior for reduced IGBT turn-on losses
- Small temperature coefficient
- Solderable front side pads¹
- 185°C maximum junction temperature



Applications

- Drives

Description

- Recommended for power modules

Product Validation

- Technology qualified for Automotive Applications. Product validation according to AEC-Q101.

Key Performance Parameters

Chip Type	V_{RRM}	I_{Fn}	Die Size	Package
IDC103D75H8DA	750V	696A	102.95 mm ²	Sawn on foil

¹ Depending on customer specific assembly process

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1 Parameters and Characteristics

Table 1 Mechanical Parameters

Raster size	12.95 x 7.95	mm ²
Area total	102.95	mm ²
Anode pad size	See chip drawing	
Silicon thickness	68	μm
Wafer size	200	mm
Maximum possible chips per wafer	249	
Passivation frontside	Photoimide	
Pad metal	NiP/Pd	
Backside metal	NiP/Pd	
Die bond ¹	Soft solder	
Frontside interconnect ¹	Soft solder	
Reject ink dot size	Inkless	
Storage environment (<6 months)	For original and sealed MBB bags ²	Ambient atmosphere air, temperature 17°C – 25°C

Table 2 Maximum Ratings³

Parameter	Symbol	Conditions	Value	Unit
Maximum reverse voltage	V_{RRM}	$25^{\circ}\text{C} \leq T_{vj} \leq 185^{\circ}\text{C}$	750	V
		$T_{vj} = -40^{\circ}\text{C}^4$	710	
Continuous forward current, limited by $T_{vj\ max}$	I_F		_5	A
Pulsed forward current, t_p limited by $T_{vj\ max}$	$I_{F,pulse}$		2088	A
Virtual junction temperature	T_{vj}		-40 ... +185	°C
Safe operating area	SOA	$I_{F,max} = 1392\ \text{A}, V_{R,max} = V_{RRM}, -40^{\circ}\text{C} \leq T_{vj} \leq 185^{\circ}\text{C}$		

¹ Depending on customer specific assembly process

² https://www.infineon.com/dgdl/Storage_of_Products_Supplied_by_Infineon_Technologie.pdf?fileId=5546d461641369bf01643b95d8500011

³ Not subject to production test - verified by design/characterization.

⁴ V_{RRM} increases linearly between -40°C and 25°C.

⁵ Depending on thermal properties of assembly.

Table 3 Static Characteristics (Tested on Wafer), $T_{vj}=25^{\circ}\text{C}$

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Forward voltage drop	V_F	$I_F = 70\text{A}$	-	1.4	1.55	V
Reverse leakage current	I_R	$V_R = 750\text{V}$	-	-	100	μA

Table 4 Electrical Characteristics¹

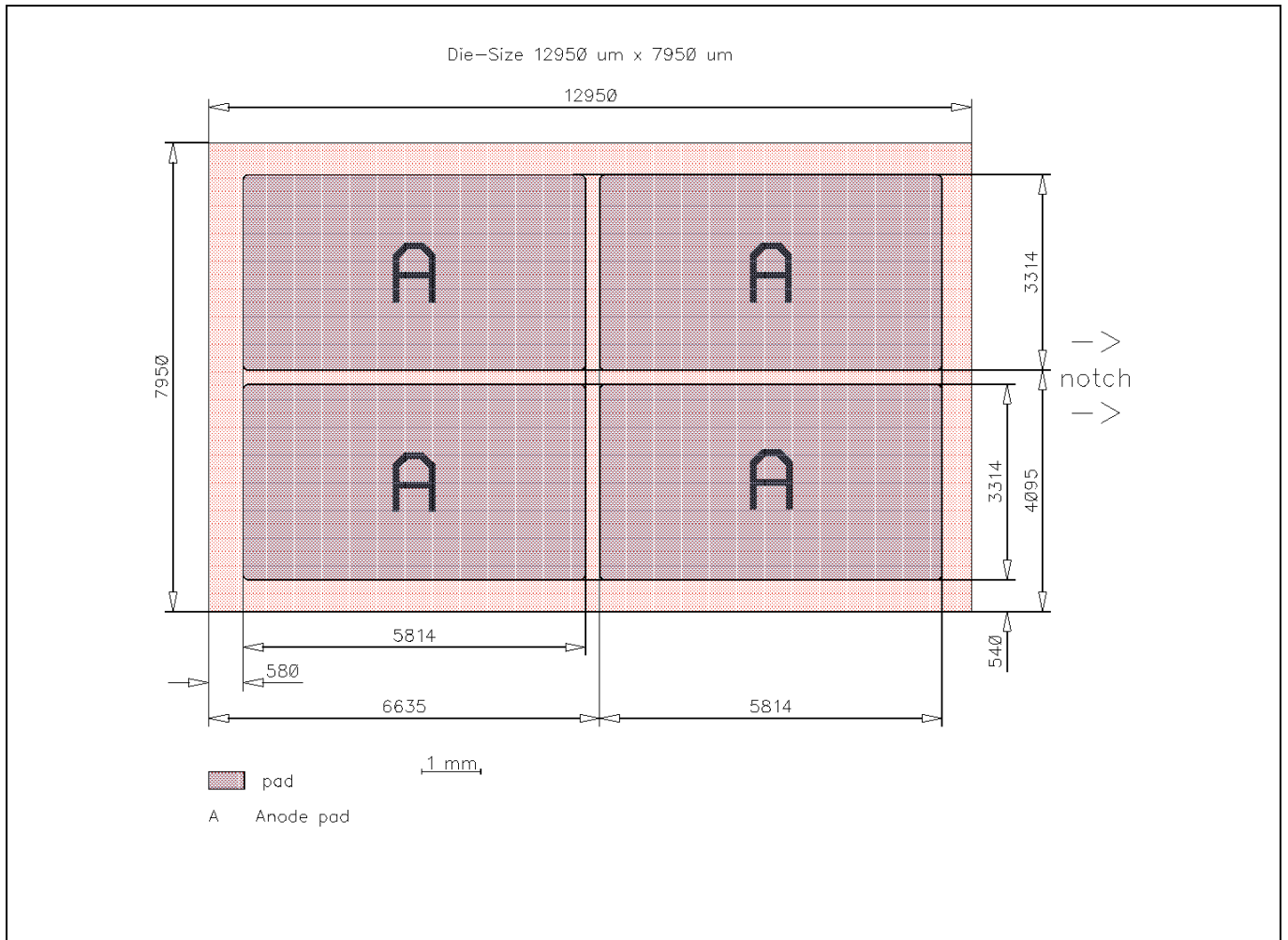
Parameter	Symbol	Conditions	Value			Unit	
			min.	typ.	max.		
Forward voltage drop	V_F	$I_F = 696\text{A}$	$T_{vj} = 25^{\circ}\text{C}$	-	2.00	2.3	V
			$T_{vj} = 185^{\circ}\text{C}$	-	1.70	-	

2 Further Electrical Characteristics

Note: Switching characteristics and thermal properties are dependent on module design and mounting technology and can therefore not be specified for a bare die.

¹ Not subject to production test - verified by design/characterization.

3 Chip Drawing



4 Bare Die Product Specifics

Note: Test coverage at wafer level for diodes cannot cover the full range of customer application conditions. Therefore it is the responsibility of the customer to test all performance characteristics, which are relevant for their specific application, at the package level, including SOA.

Description

- AQL 0.1 for visual inspection according to failure catalogue

Revision History

Document version	Date of release	Description of changes
V1.00	17-July-2025	Initial Datasheet
V1.10	10-September-2025	Changes of V_{ces} , T_{vj} , SOA, V_f for 185°C

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